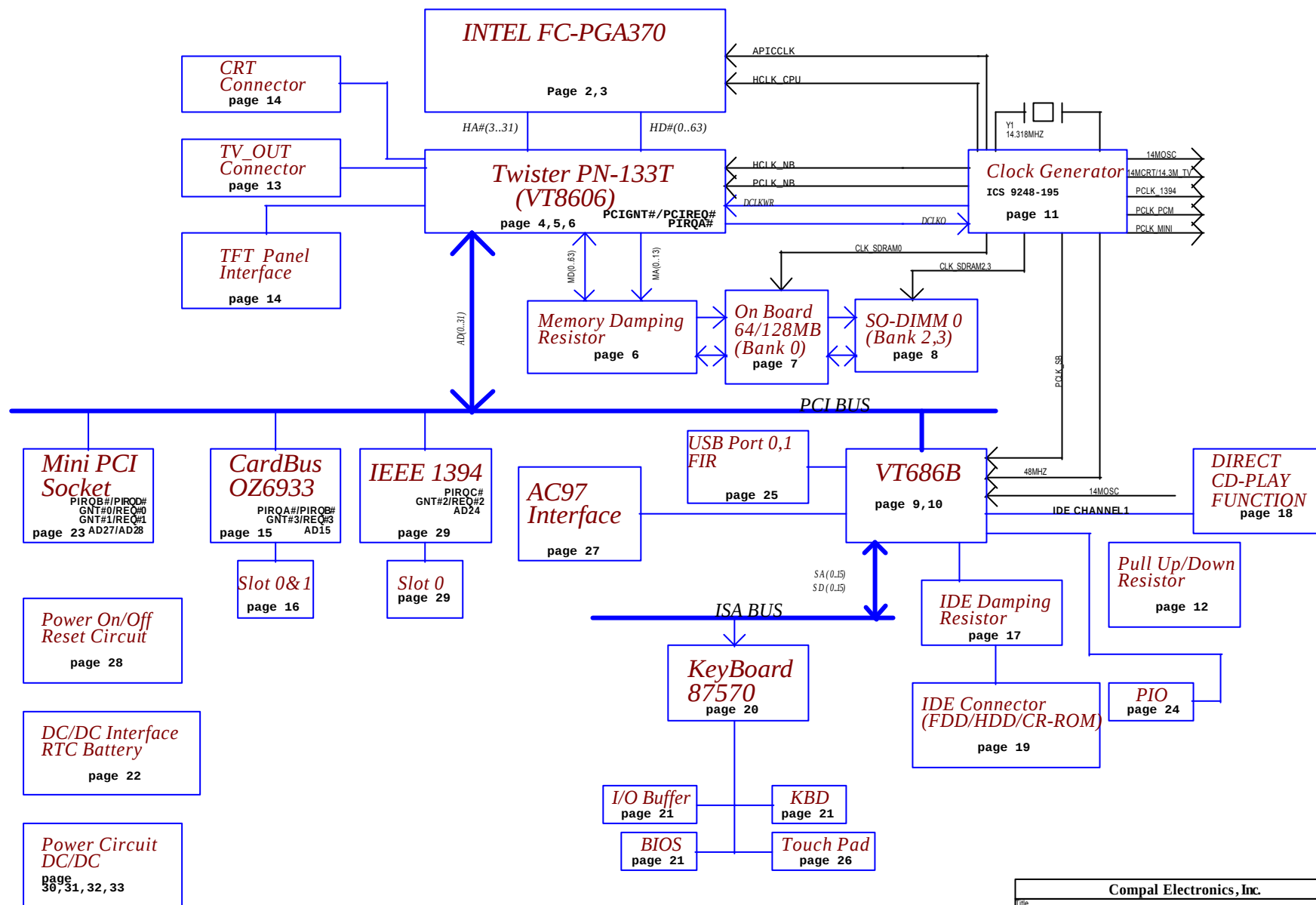
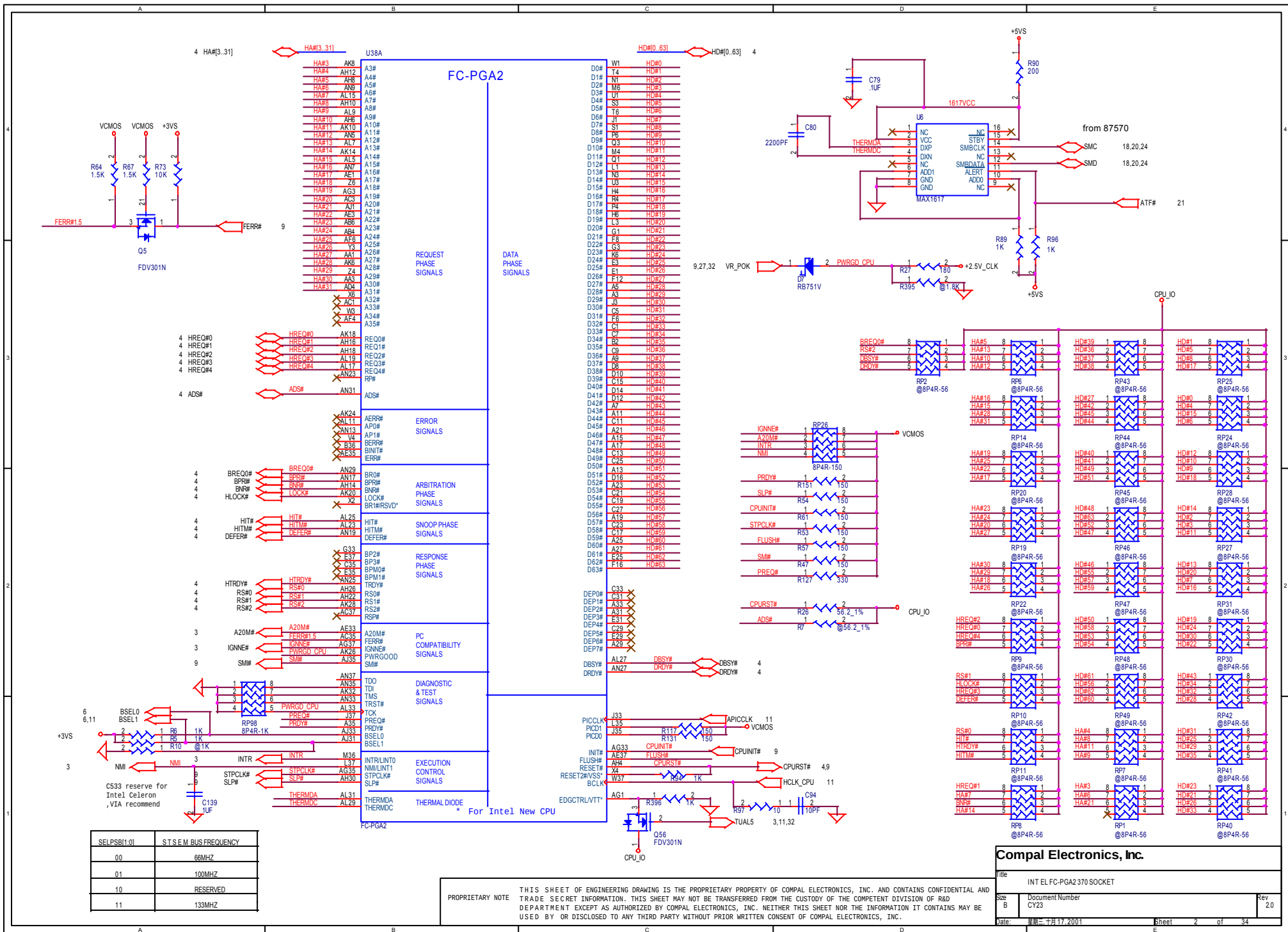


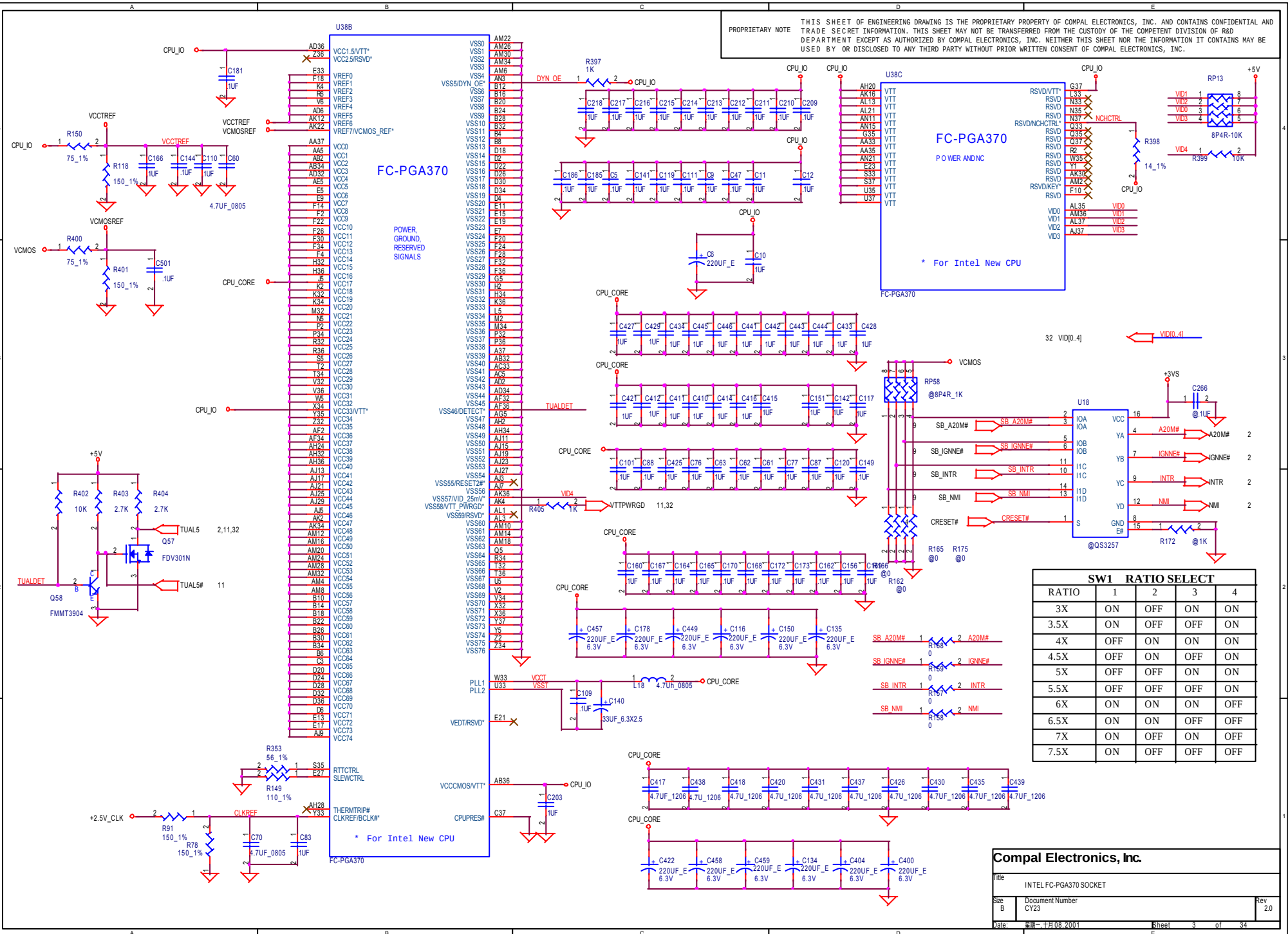


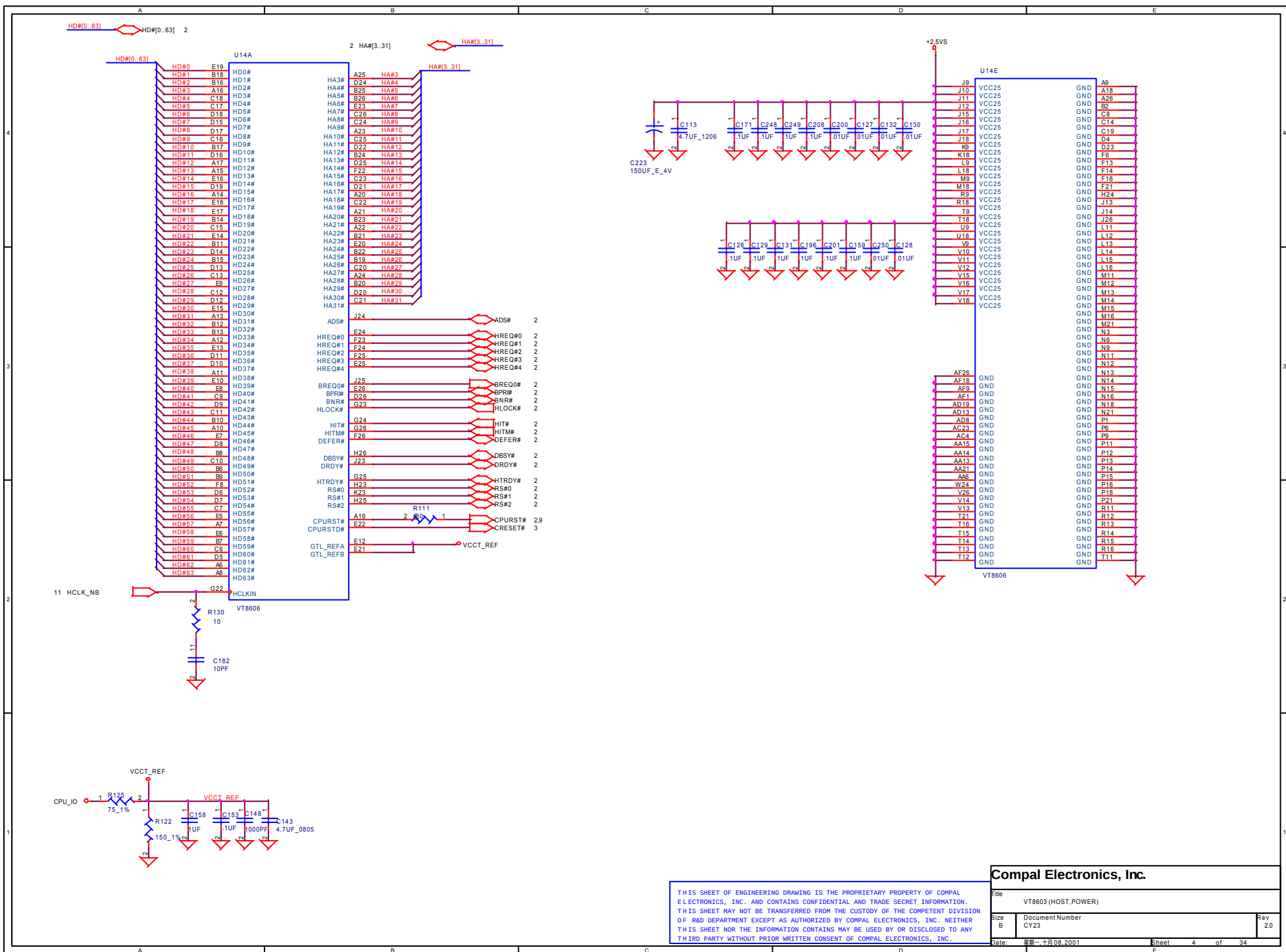
PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

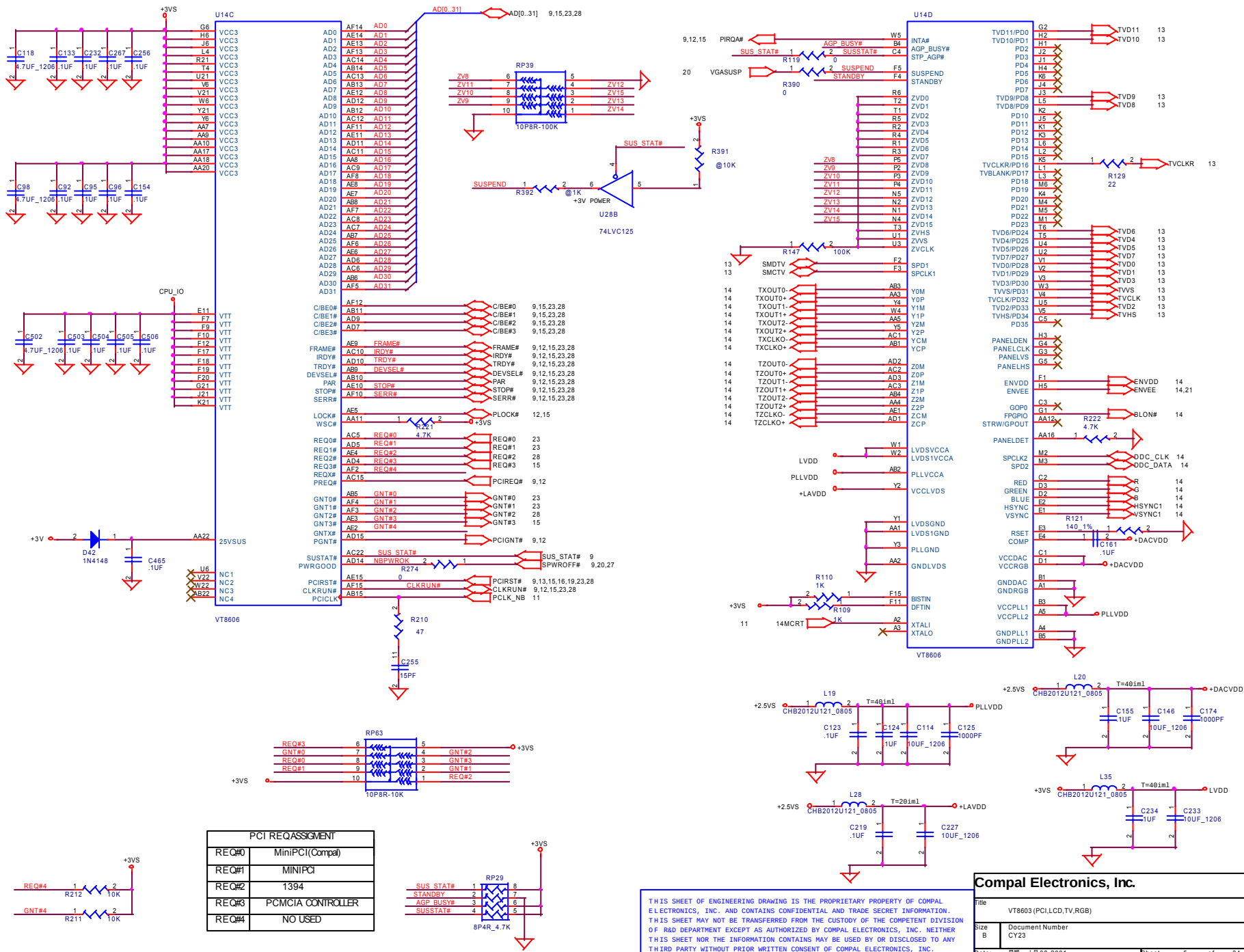
Compal Electronics, Inc.			
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Size	Document Number	Rev	
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PCI REQ ASSIGNMENT	
REQ#0	MiniPCI (Compal)
REQ#1	MINI PCI
REQ#2	1394
REQ#3	PCMCIA CONTROLLER
REQ#4	NO USED

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Compal Electronics, Inc.

File

VT8603 (PCI, LCD, TV, RGB)

Size

B

Document Number

CY23

Rev

2.0

Date

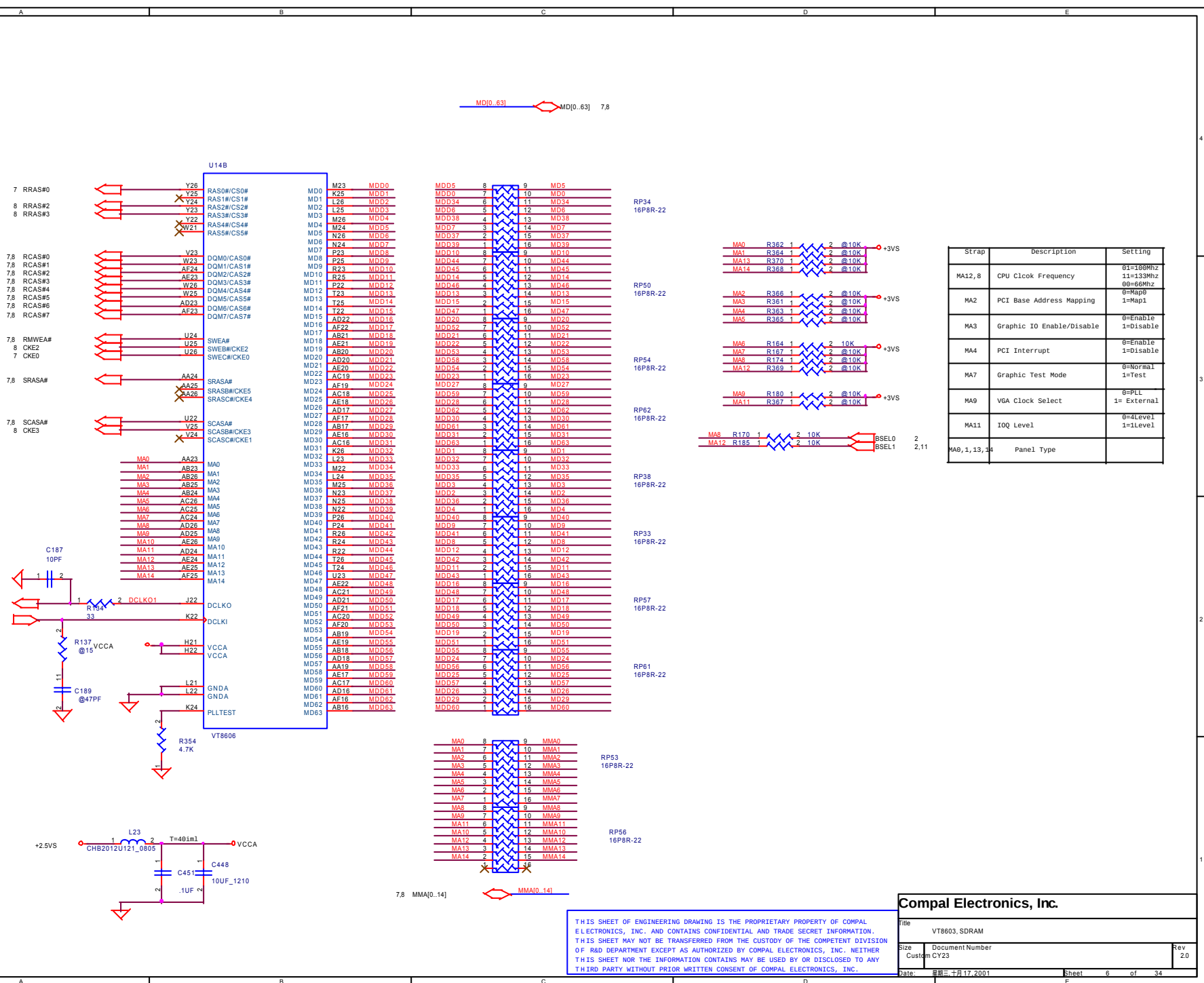
2001.08.10

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of

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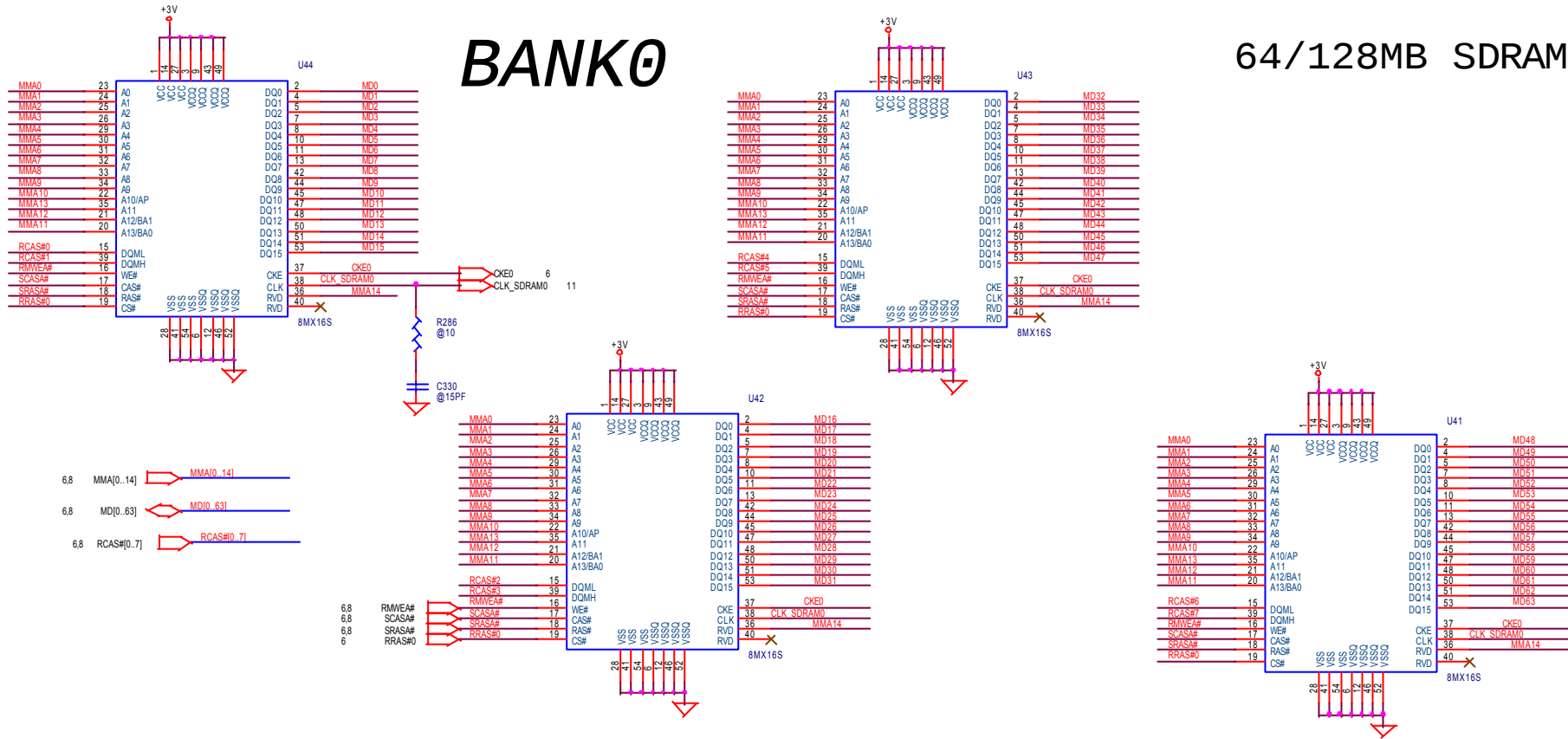
Strap	Description	Setting
MA12,8	CPU Clock Frequency	01=100MHz 11=133MHz 00=66MHz
MA2	PCI Base Address Mapping	0=Map0 1=Map1
MA3	Graphic IO Enable/Disable	0=Enable 1=Disable
MA4	PCI Interrupt	0=Enable 1=Disable
MA7	Graphic Test Mode	0=Normal 1=Test
MA9	VGA Clock Select	0=PLL 1= External
MA11	IOQ Level	0=1level 1=1level
MA8,1,13,14	Panel Type	

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File VT8603.SDRAM		
Size Custom CY23	Document Number	Rev 2.0
Date 2003.10.17	Sheet 6	of 34

BANK0

64/128MB SDRAM



Compal Electronics, Inc.

ON BOARD 64/128MB SDRAM

CY23

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Date: 2001.08.08 Sheet 7 of 34

BANK2/3



Compal Electronics, Inc.

Document Number
CY23

Rev	2.0
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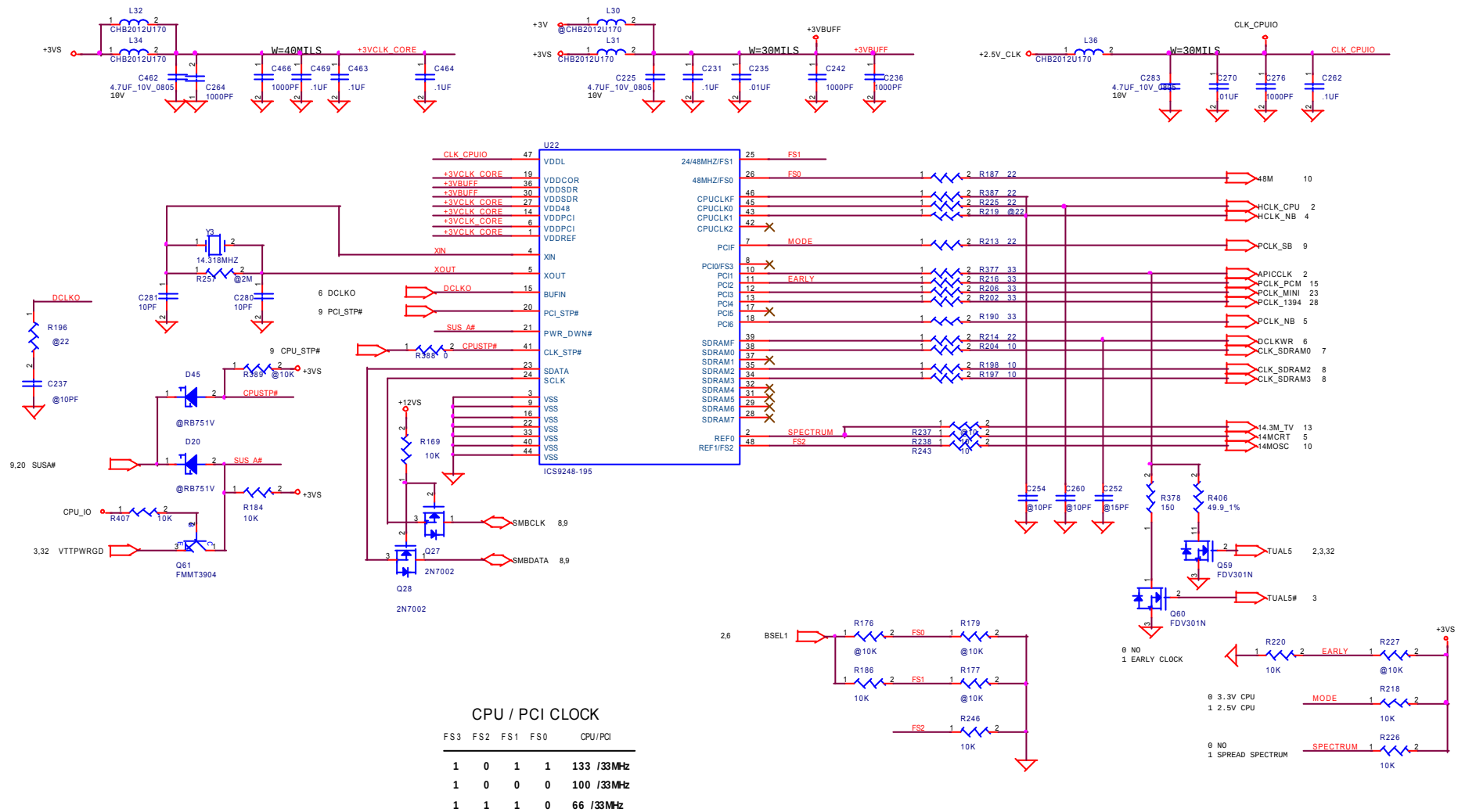
Date: 星期一, 十月 08, 2001 Sheet 8 of 34

Signals Pullup



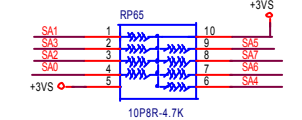
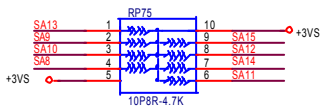
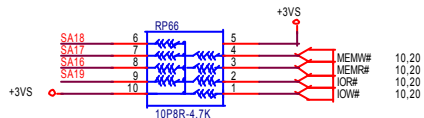
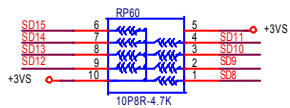
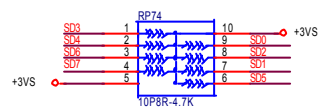
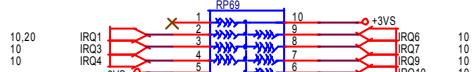
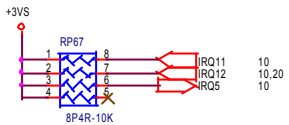
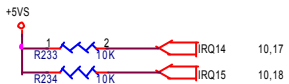
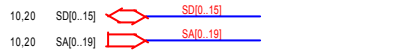
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VT82C686A 1			
Size	Document Number		Rev
B	CY23		2.0
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CLOCK GENERATOR & BUFFER

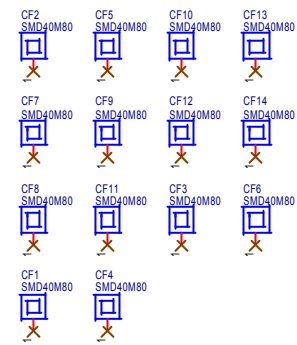
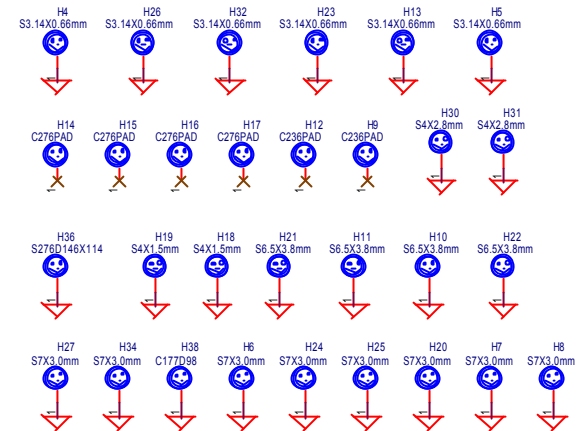
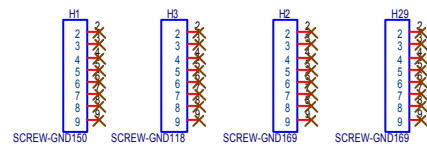
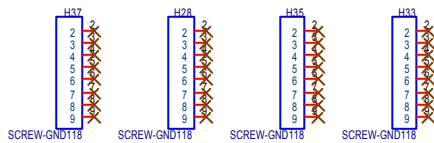
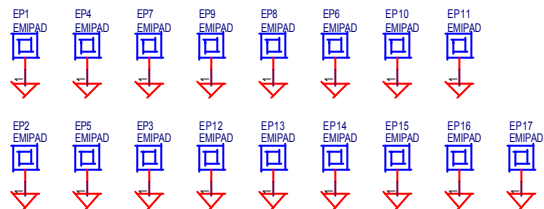
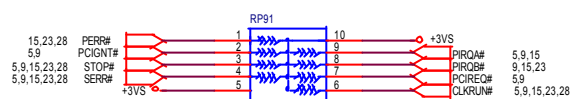
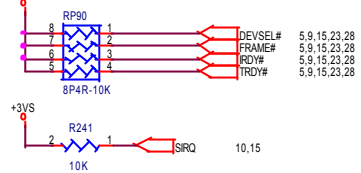
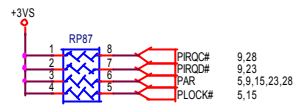


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ISA BUS Pullup



PCI BUS Pullup



Compal Electronics, Inc.

	Title	PULL UP/DOWN RESISTOR
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Size B	Document No. CY23
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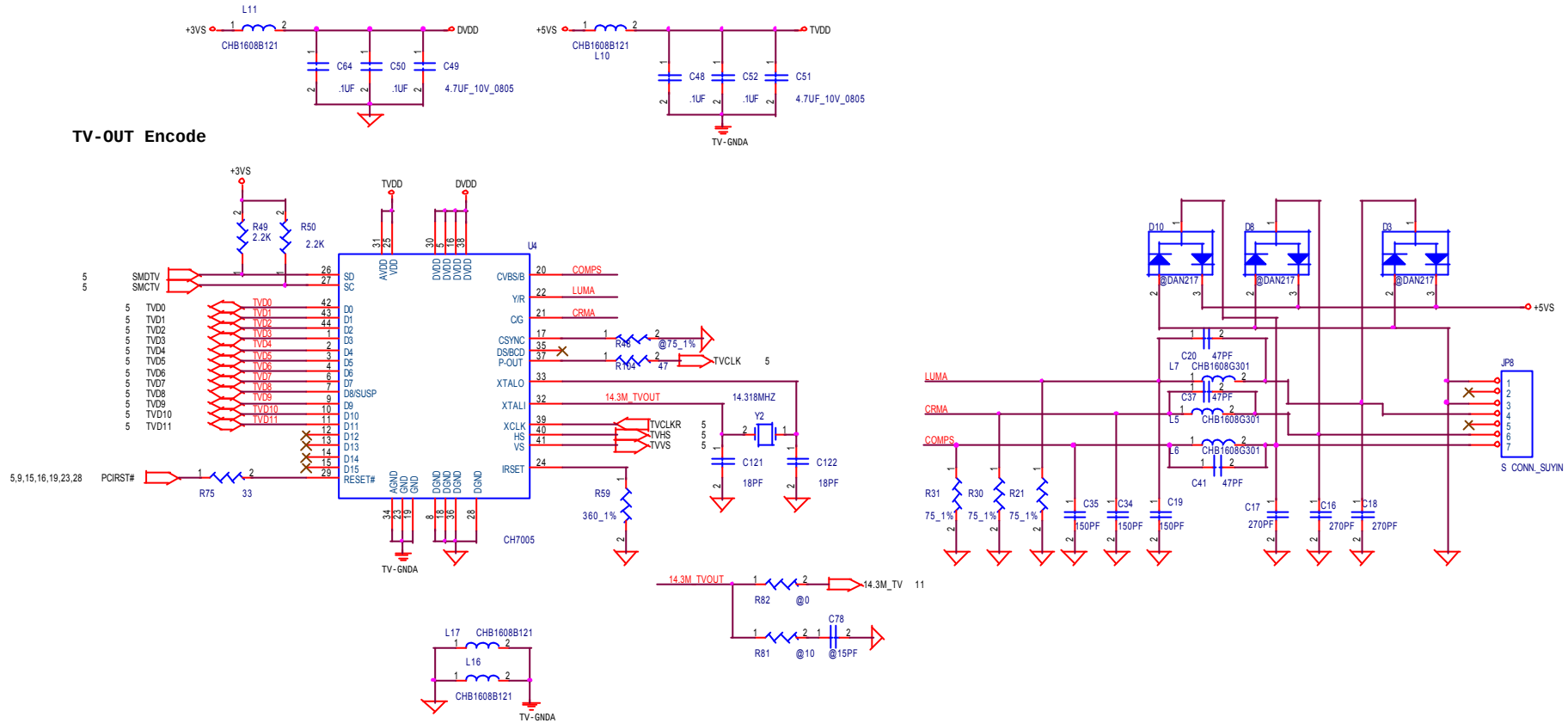
Date: 星期一, 十月 08, 2001

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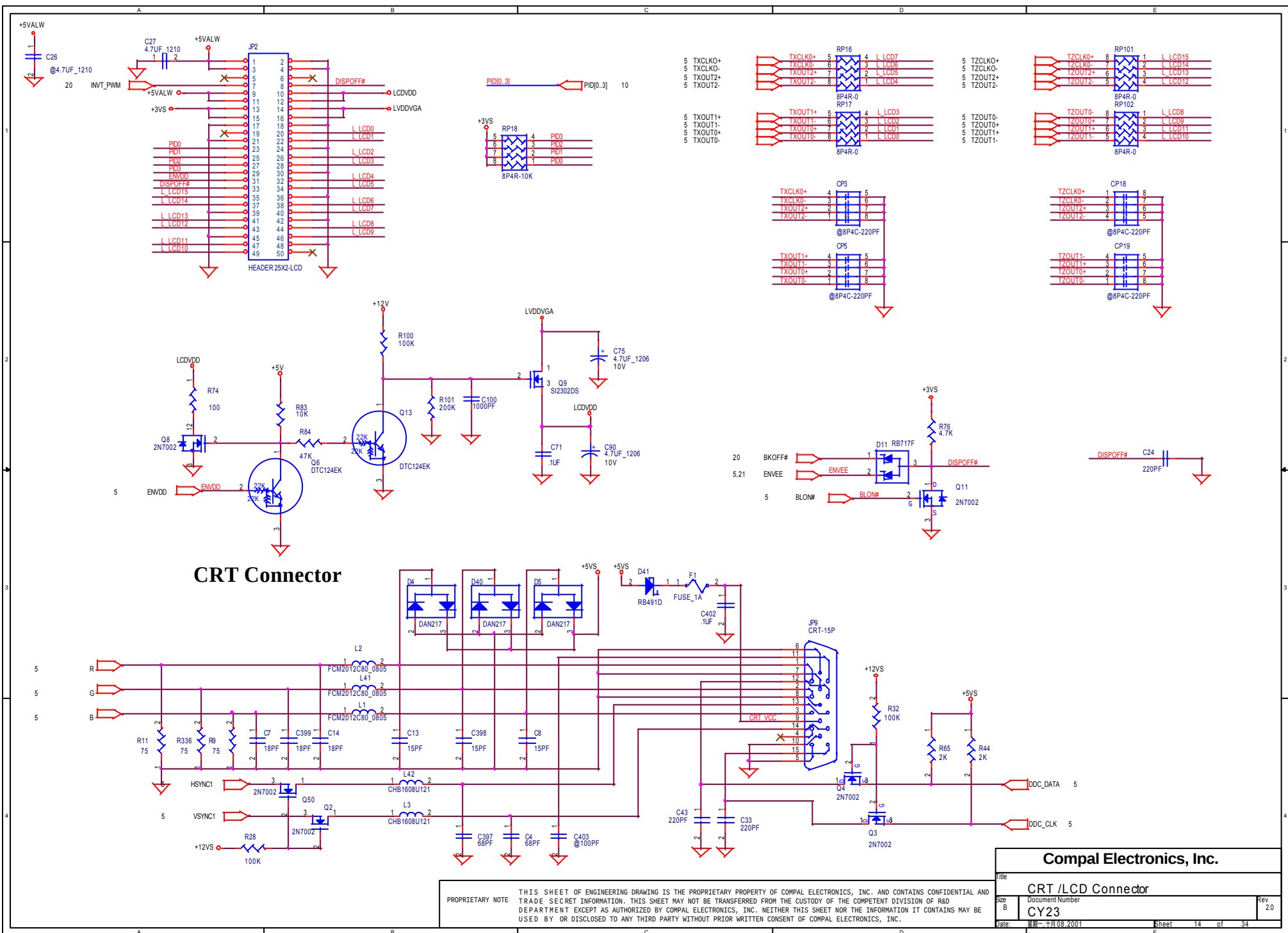
TV-OUT Encode



Compal Electronics, Inc.

Title		CH7005 TV-OUT
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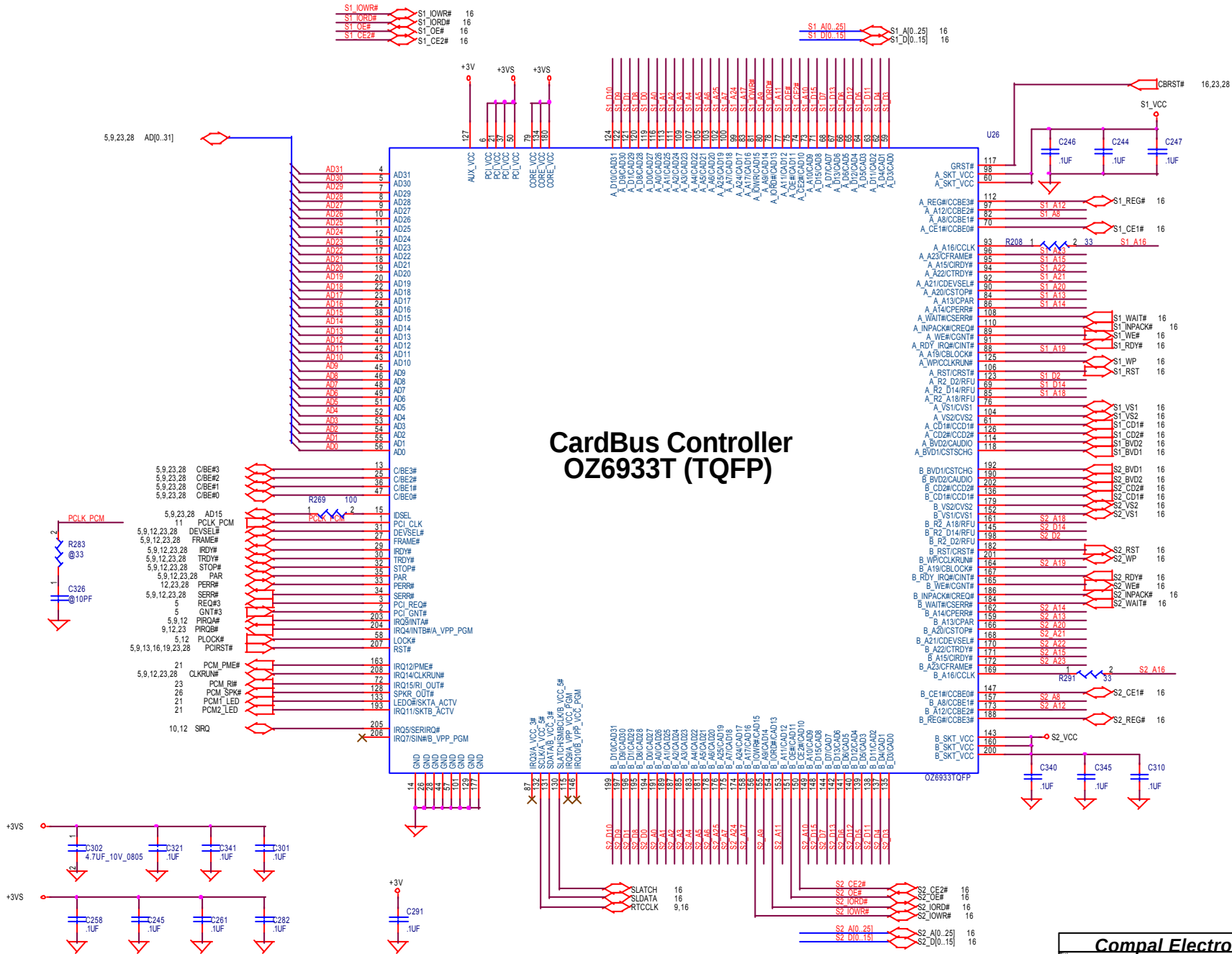


CRT Connector

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Compal Electronics, Inc.			
Title		CRT /LCD Connector	
Size	Document Number	Rev	
B	CY23	2.0	
Date:	日期: 十月08, 2001	Sheet	14 of 34

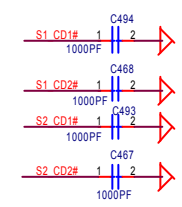
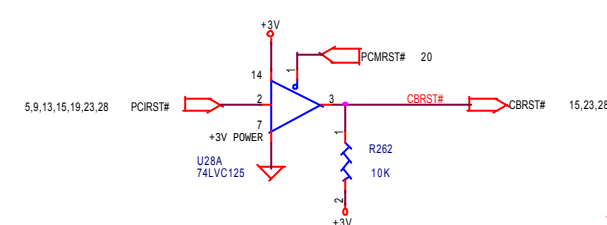
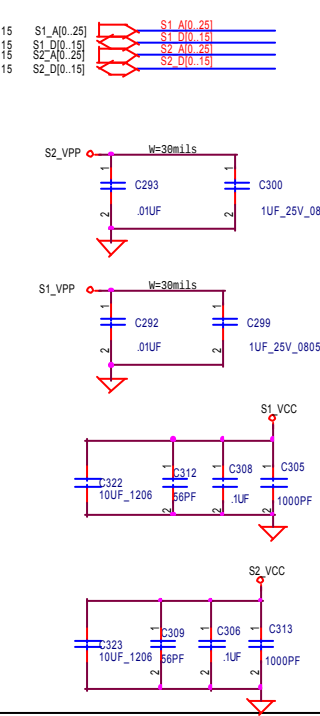
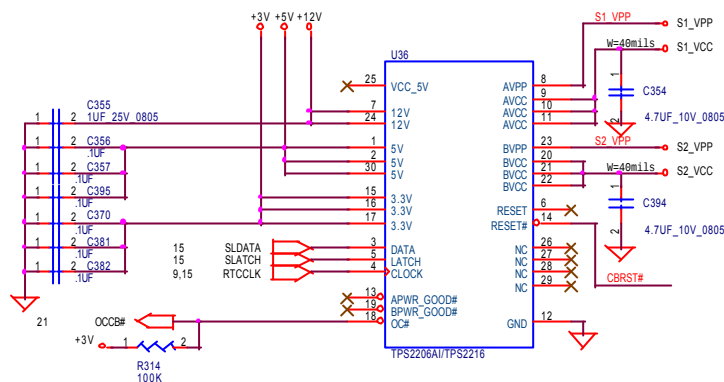
CardBus Controller OZ6933T (TQFP)



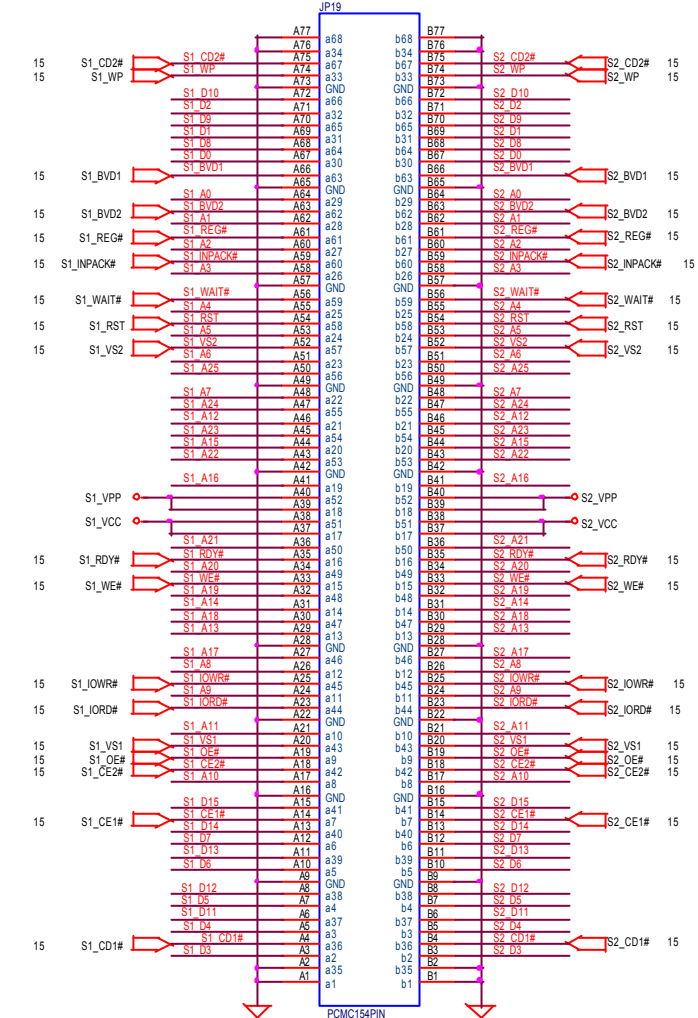
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Compal Electronics, Ltd.		
CardBus Controller(OZ6933T)		
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PCMCIA POWER CTRL. Wire ZV PORT to Slot A



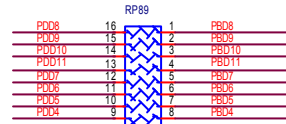
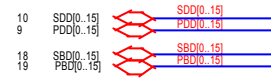
CARDBUS SOCKET



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Title: BERG PCMCIA SOCKET			
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IDE Series Resistor **Place them close SB**



16P8R-33

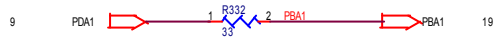


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RP95



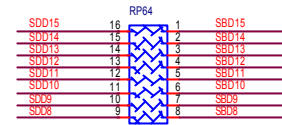
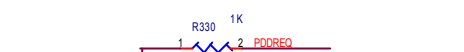
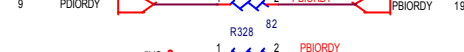
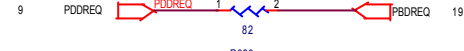
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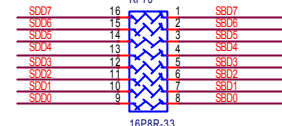
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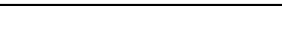
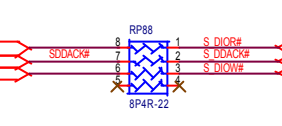
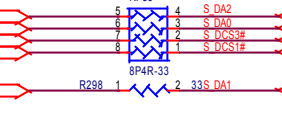
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16P8R-33



16P8R-33



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IDE Serial Resistors

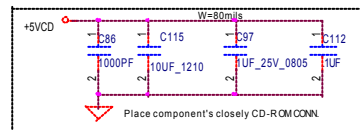
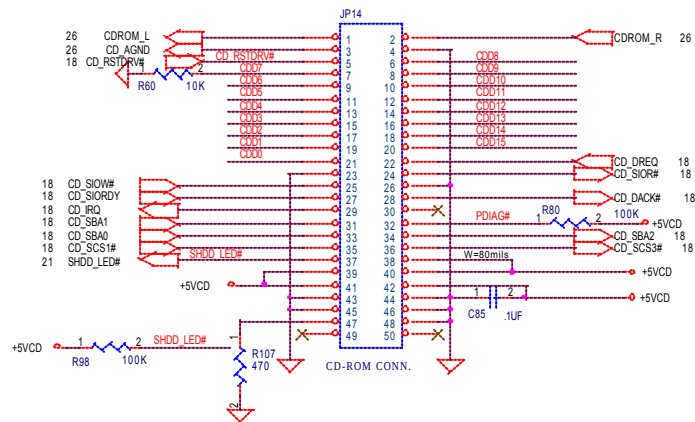
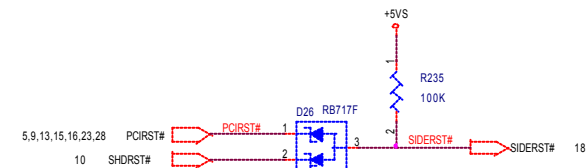
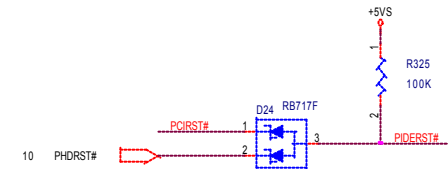
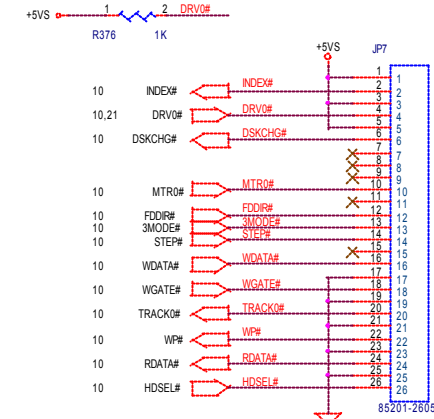
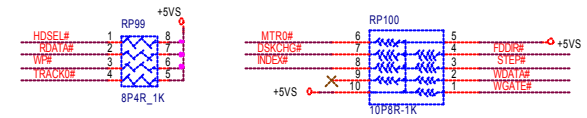
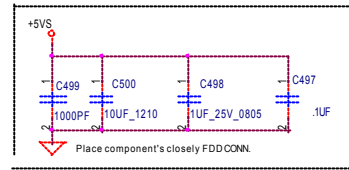
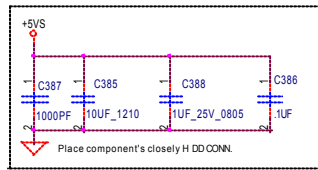
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IDE,CD-ROM & FDD Module CONN.



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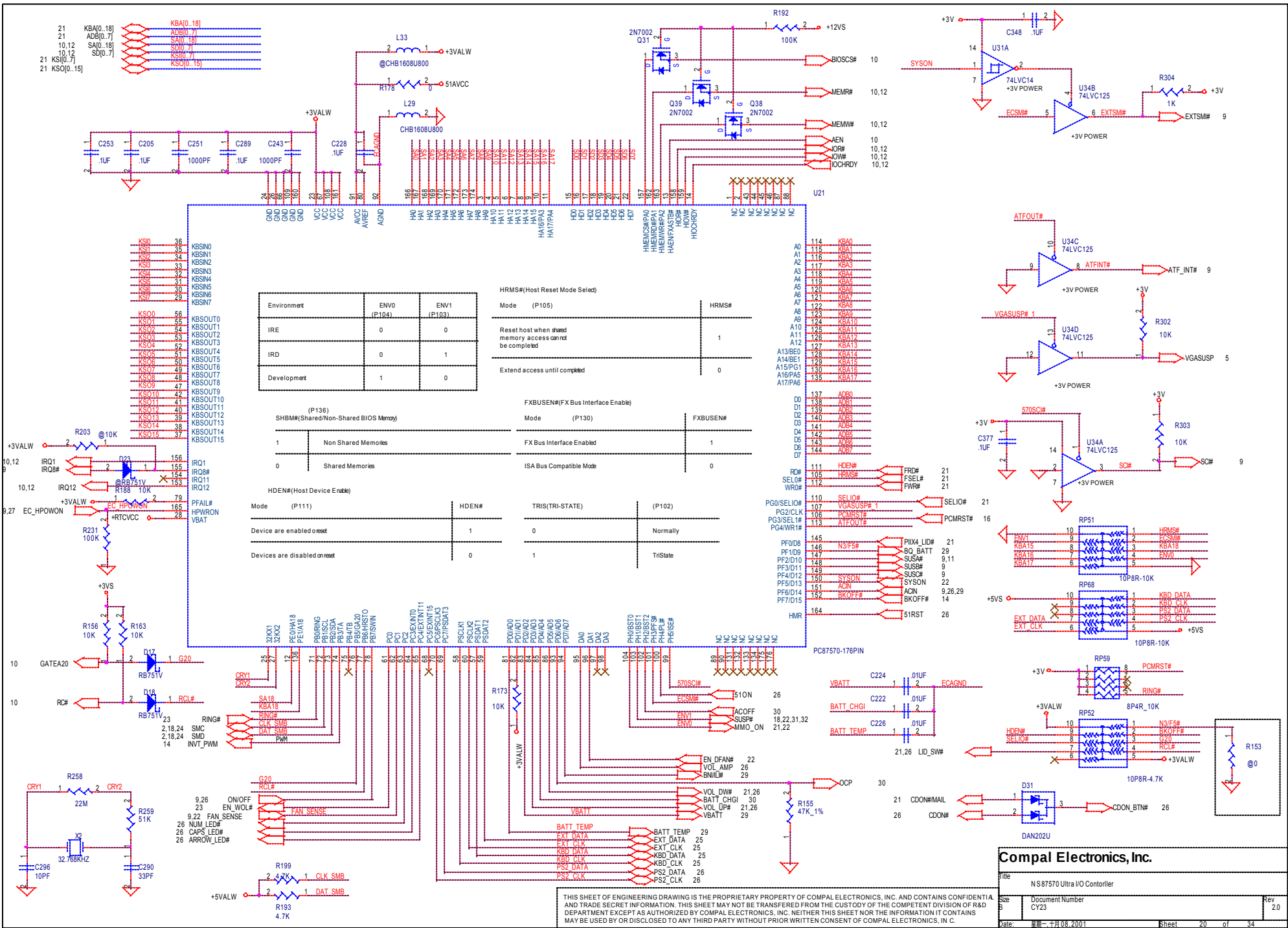
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B	CY23

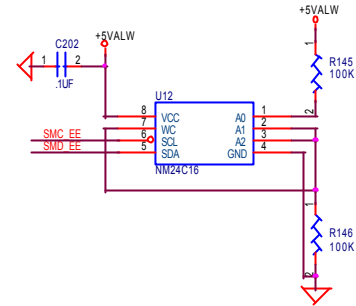
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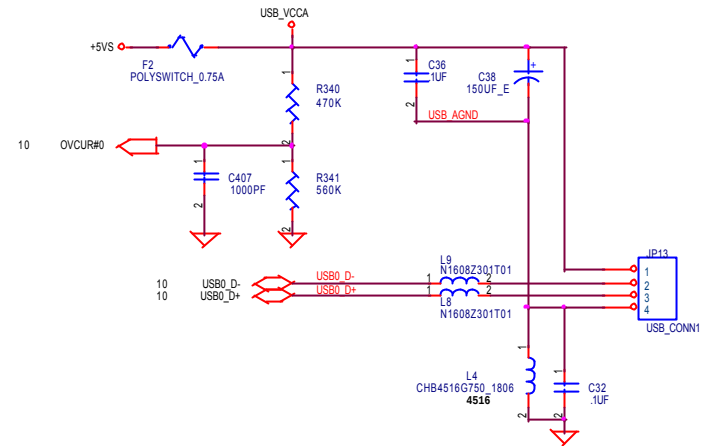
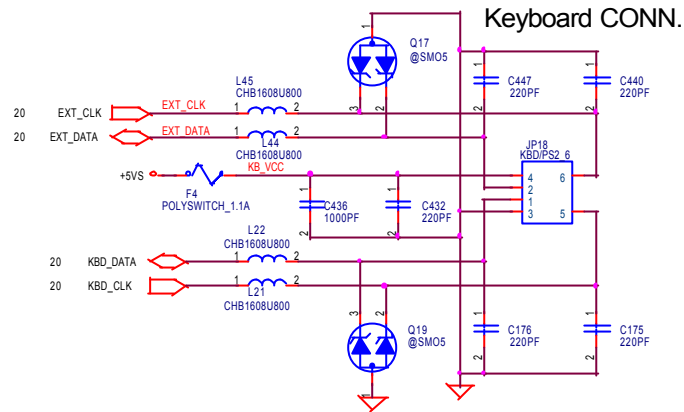
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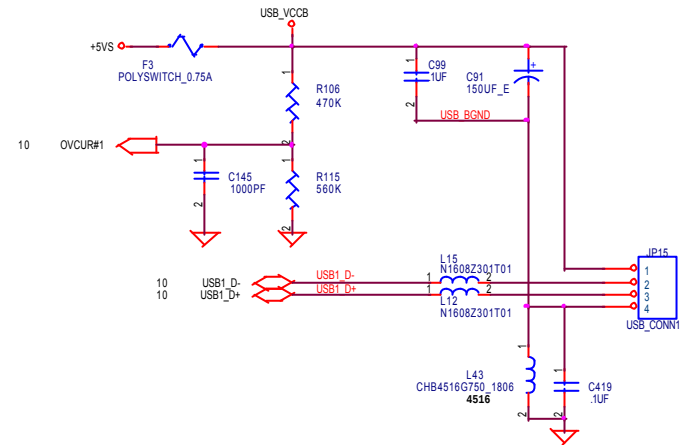
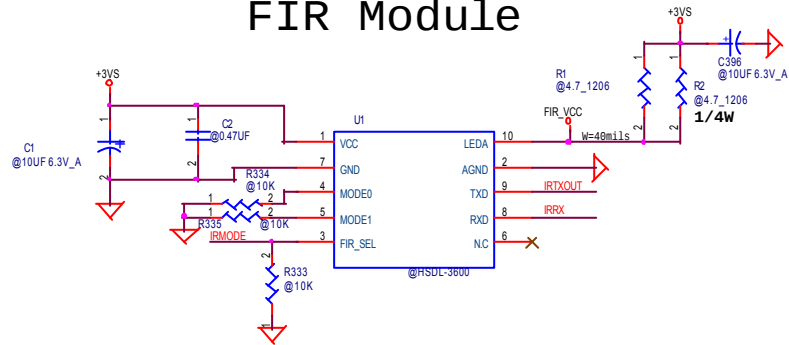


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FIR Module



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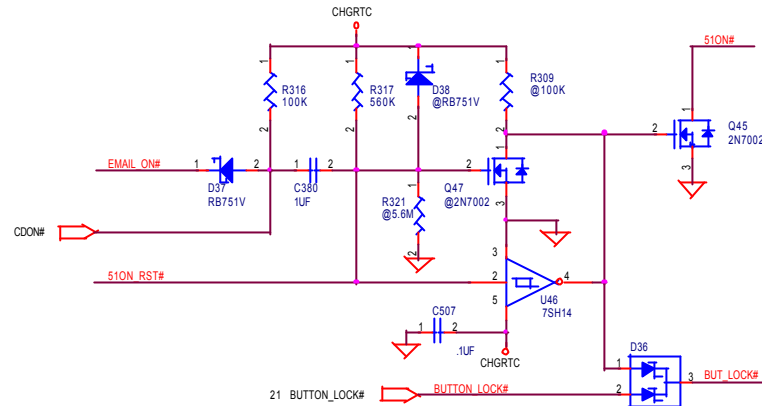
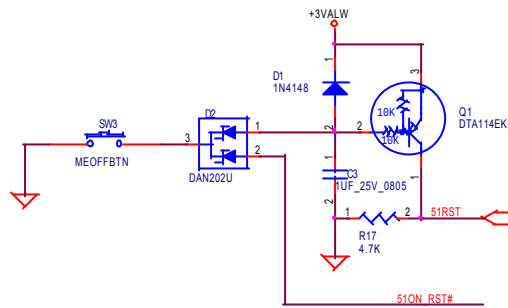
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Sub B Document Number CY23

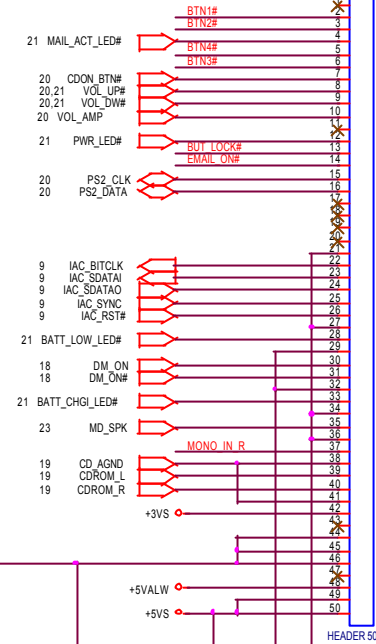
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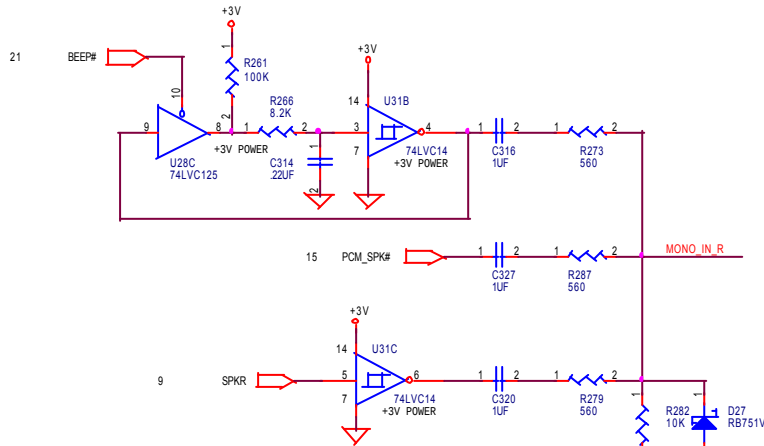
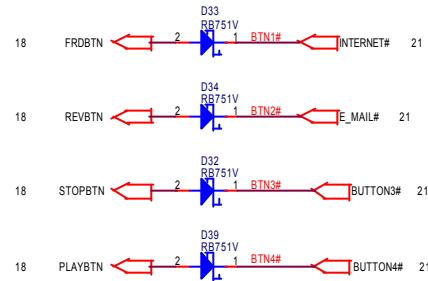
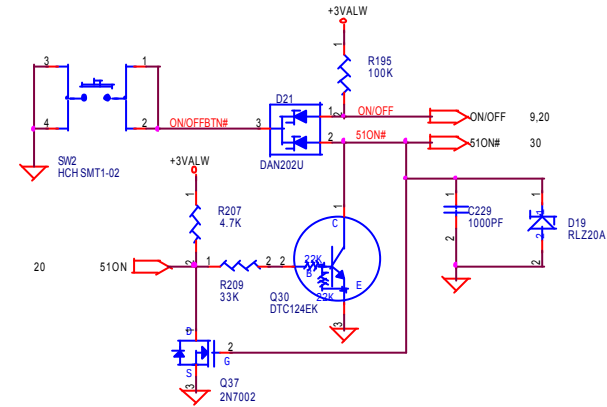
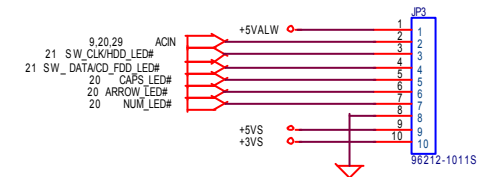
Reset Button



CDON_BTN# is Low, enable CD_PLAY
MAIL is High, enable
MAIL



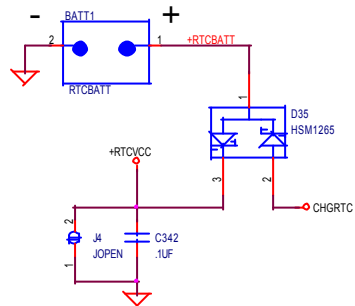
System Window Connector



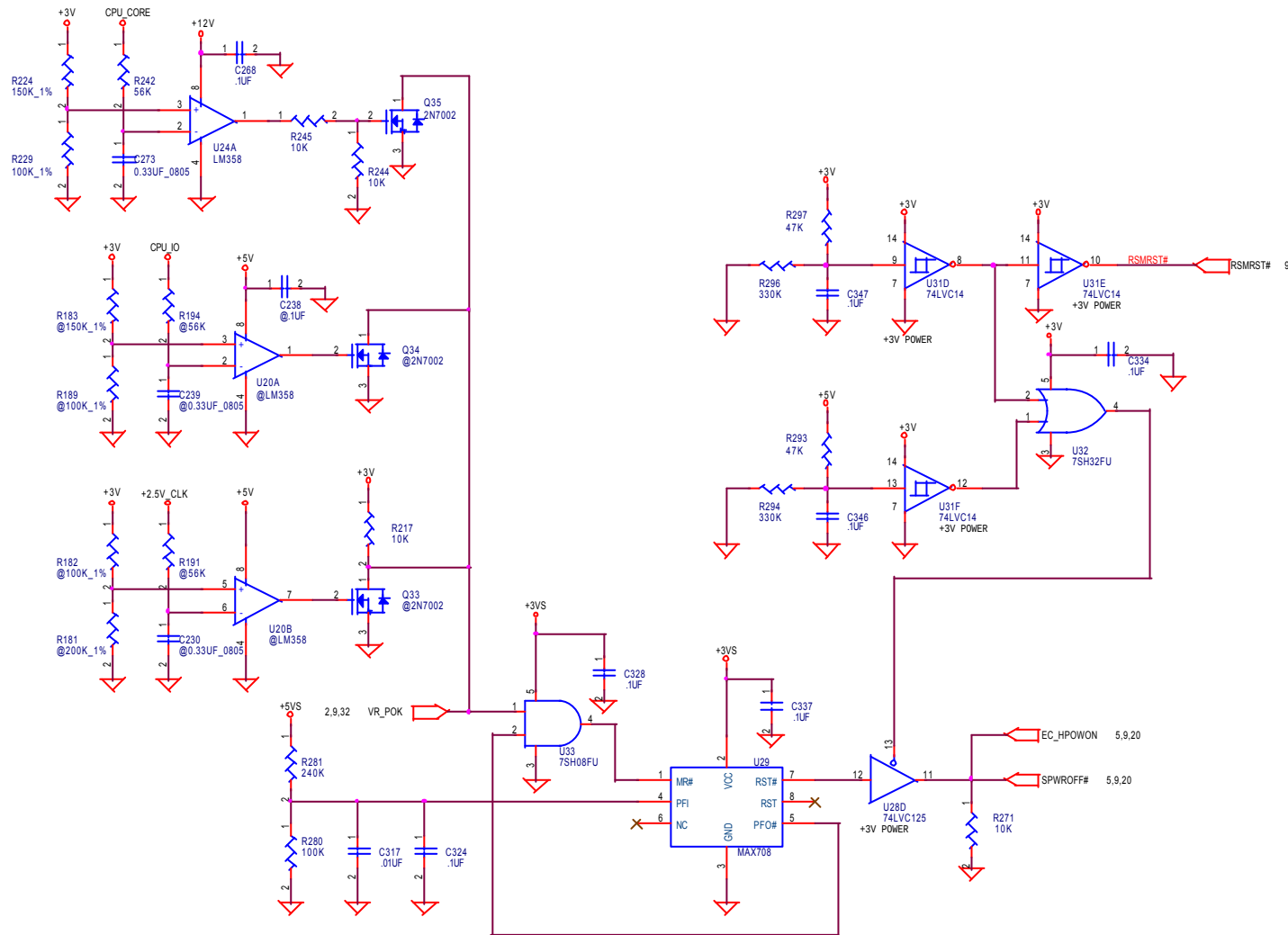
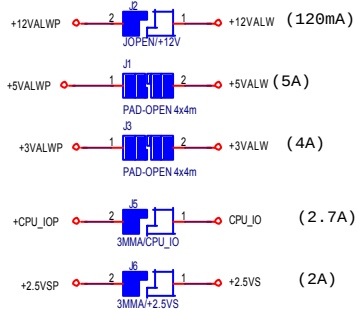
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RTC BATT



DC/DC INTERFACE

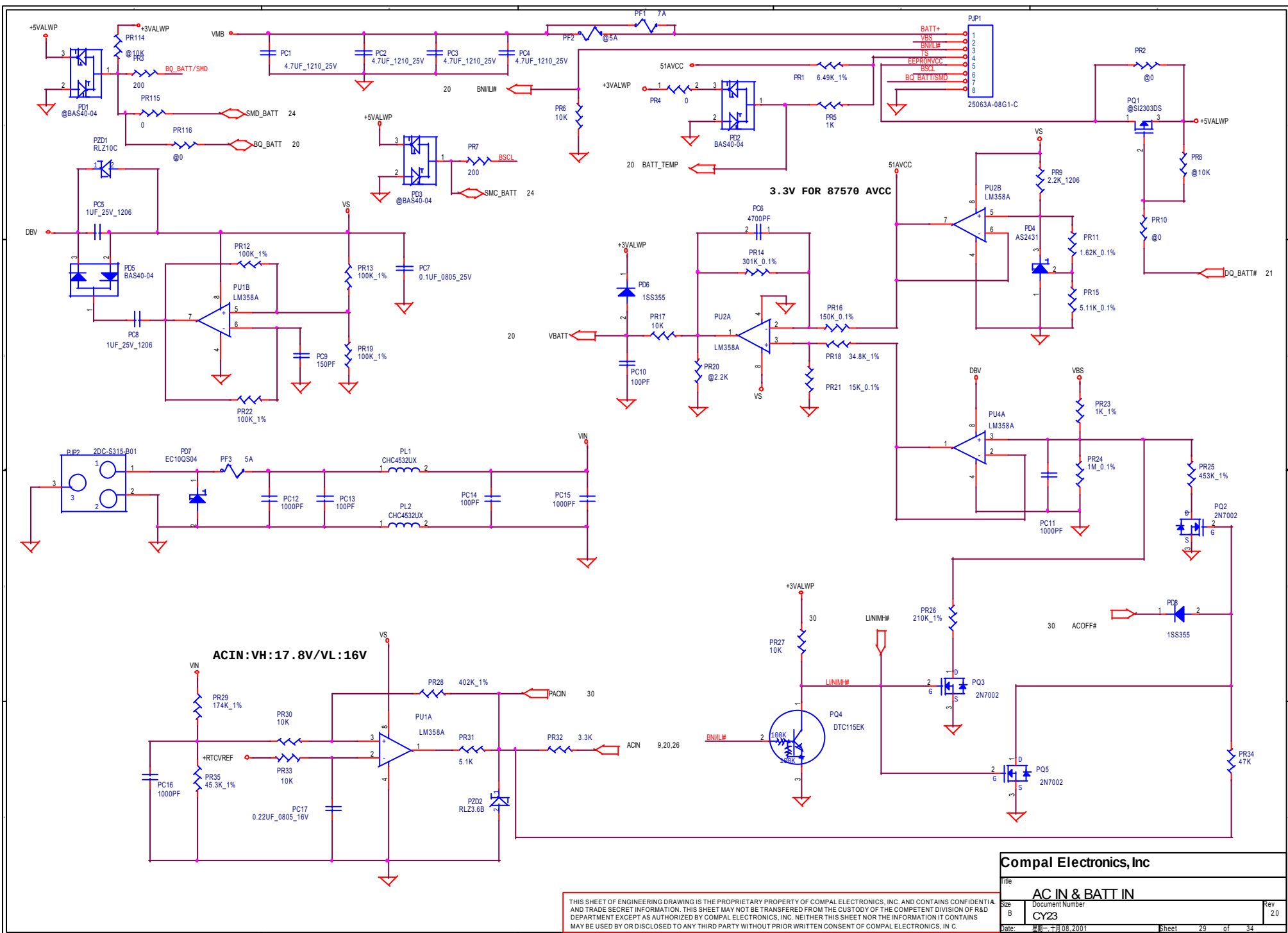


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Charger : CC:2.2A CV:16.8V for LI-ION/CV:14.4V for NI-MH

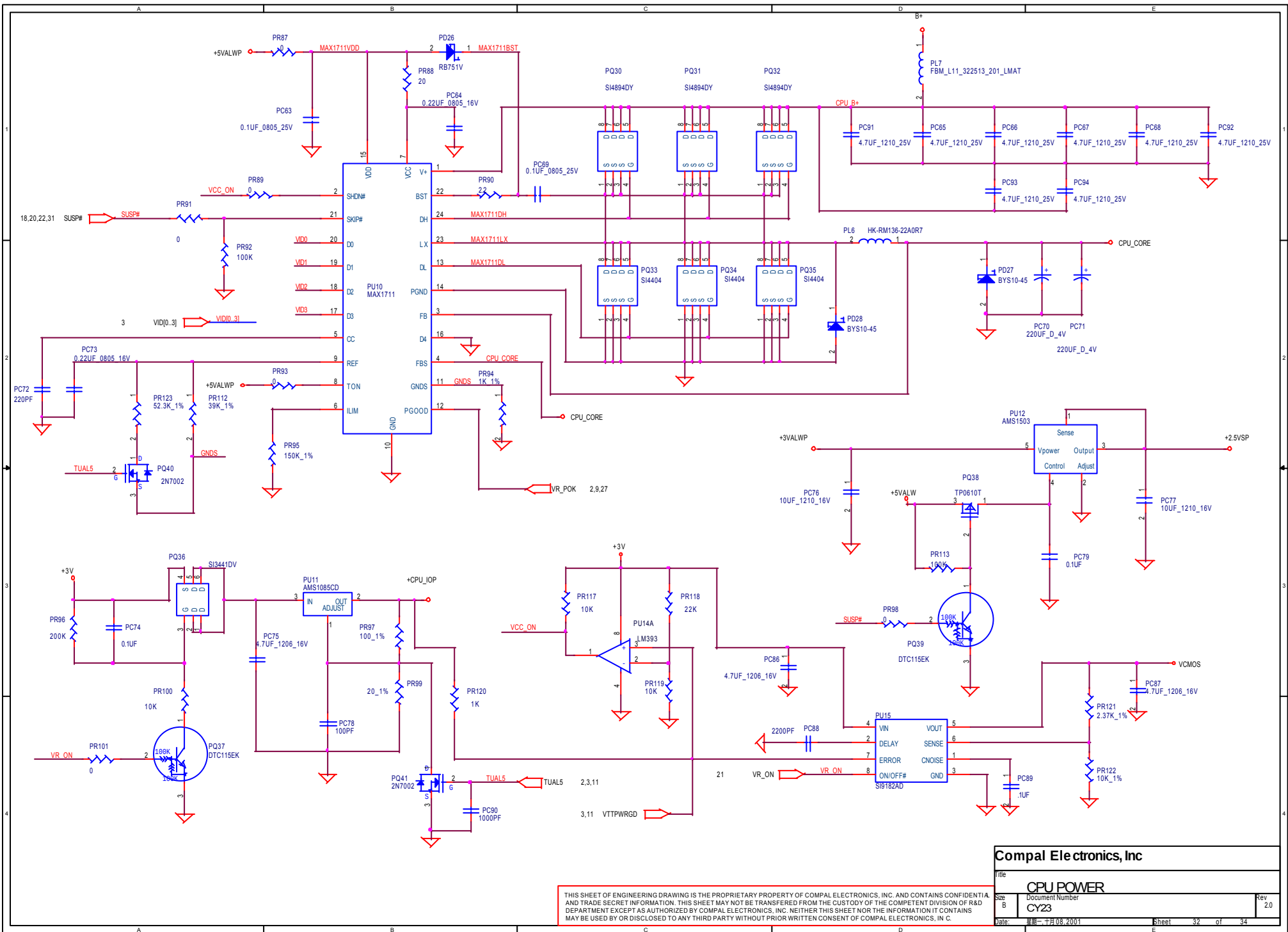
Charger : CC:2.2A CV:14.4V

RTC: 3.5V

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CHARGER		
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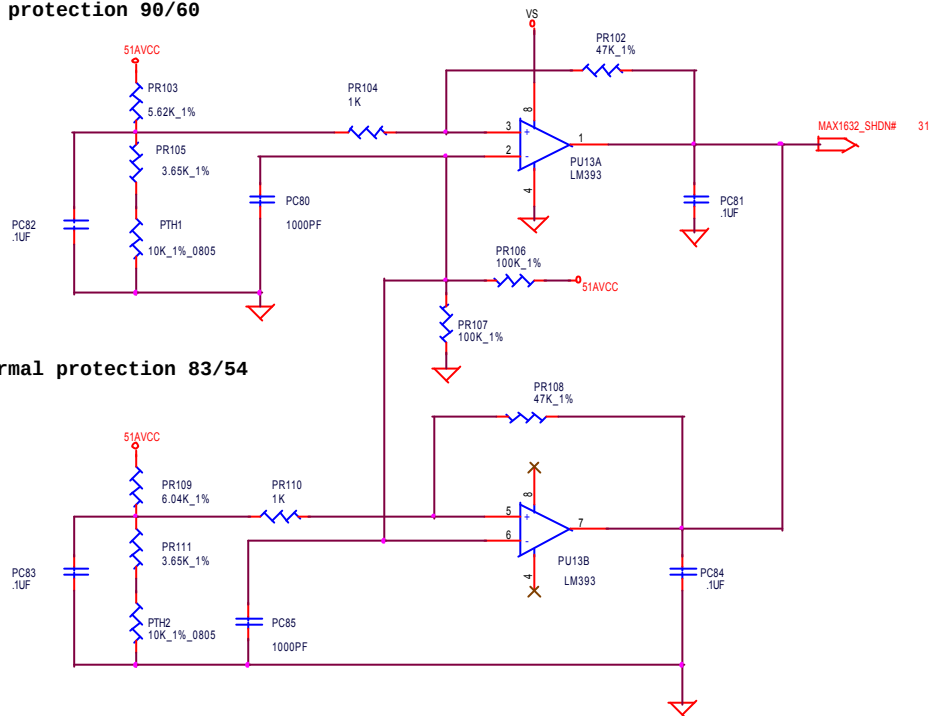
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Compal Ele ctronics, Inc			
Title			
CPU POWER			
Document Number			
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2.0			
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For CPU thermal protection 90/60

For Battery thermal protection 83/54



Title			Thermal
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CY23 PIR LIST

08/28/01 Written by Jerry

P11: Add Q61 and R407 for VTTPWRGD

***** Rev1.0 PIR List *****

09/10/01 Written by Jerry

P26: Add @ at R309 and Q47

Add C507 and U46

Change value of R317 to 510K

P27: Remove @ at R224, R229, R242, R245, R244, C273, and Q35

P32: Add PC91, PC92, PC93, and PC94.

09/12/01 Written by Jerry

P06: Change value of RP34, RP50, RP54, RP62, RP38, RP33, RP57,

RP61,RP53, and RP56 to 16P8R-22

P26: Change value of R317 to 560K

09/19/01 Written by Jerry

P29: Change value of PR29 to 174K_1%

P32: Change value of PR123 to 52.3K_1%

***** Rev2.0 PIR List *****

10/03/01 Written by Jerry

P02: Change value of R27 to 180

Delete R395

R395's Pin 1 tie to RP98's pin5

P03: Change value of R353 to 56.2 1%

P28: Add R408 between +3V and pin 87 of U3

10/08/01 Written by Jerry

P11, P13, P28: Change footprint of Y1, Y2, and Y3

P23: Change footprint of JP11

P28: Change footprint of JP12

P31: Change footprint of PL5

P26: Change footprint of SW1

10/17/01 Written by Jerry

P02: Add @ at RP25, RP24, RP28, RP27, RP31, RP30, RP 42, RP41,

RP40, RP43, RP44, RP45, RP46, RP47, RP48, RP49, RP7, RP1, RP6,

RP14, RP20, RP19, RP22, RP9, RP10, RP11, RP6, RP2, and R7

P06: Remove @ at R164

P11: Add @ at R176

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CY23 PIRLIST

Document Number
CY23

Rev
2.0

Date: 民國三十二年十月十七日

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