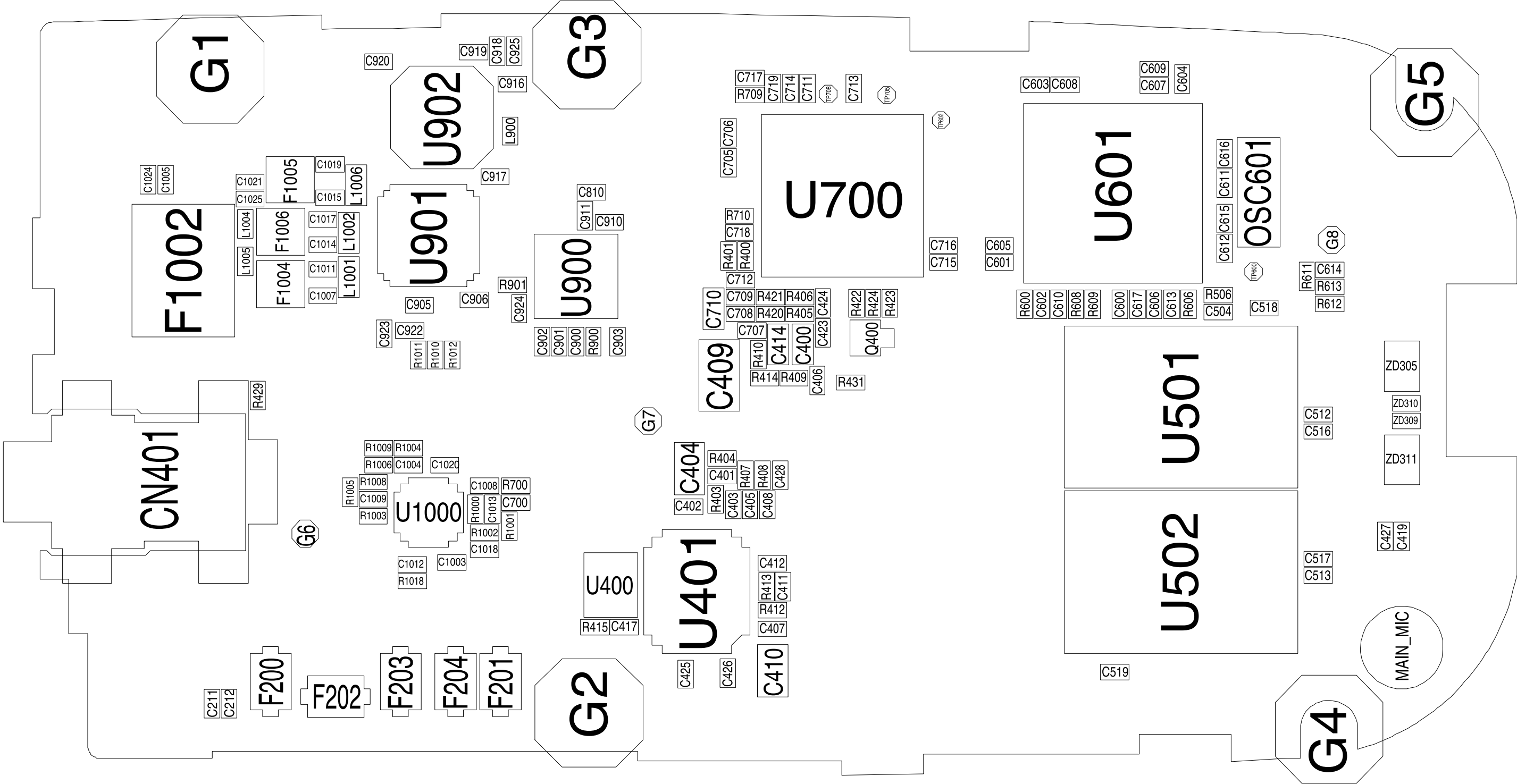
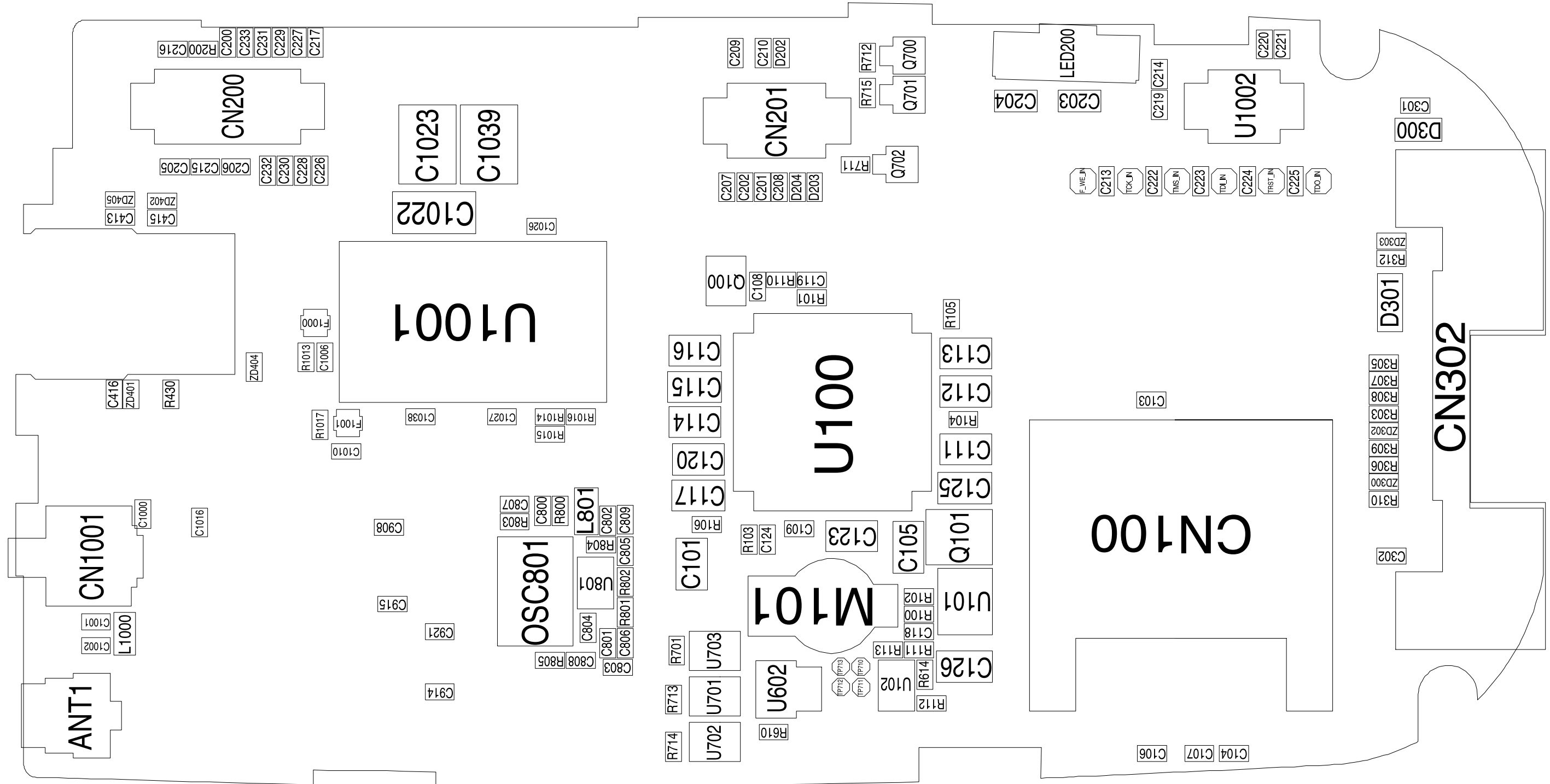


4. SGH-S100 PCB Diagrams

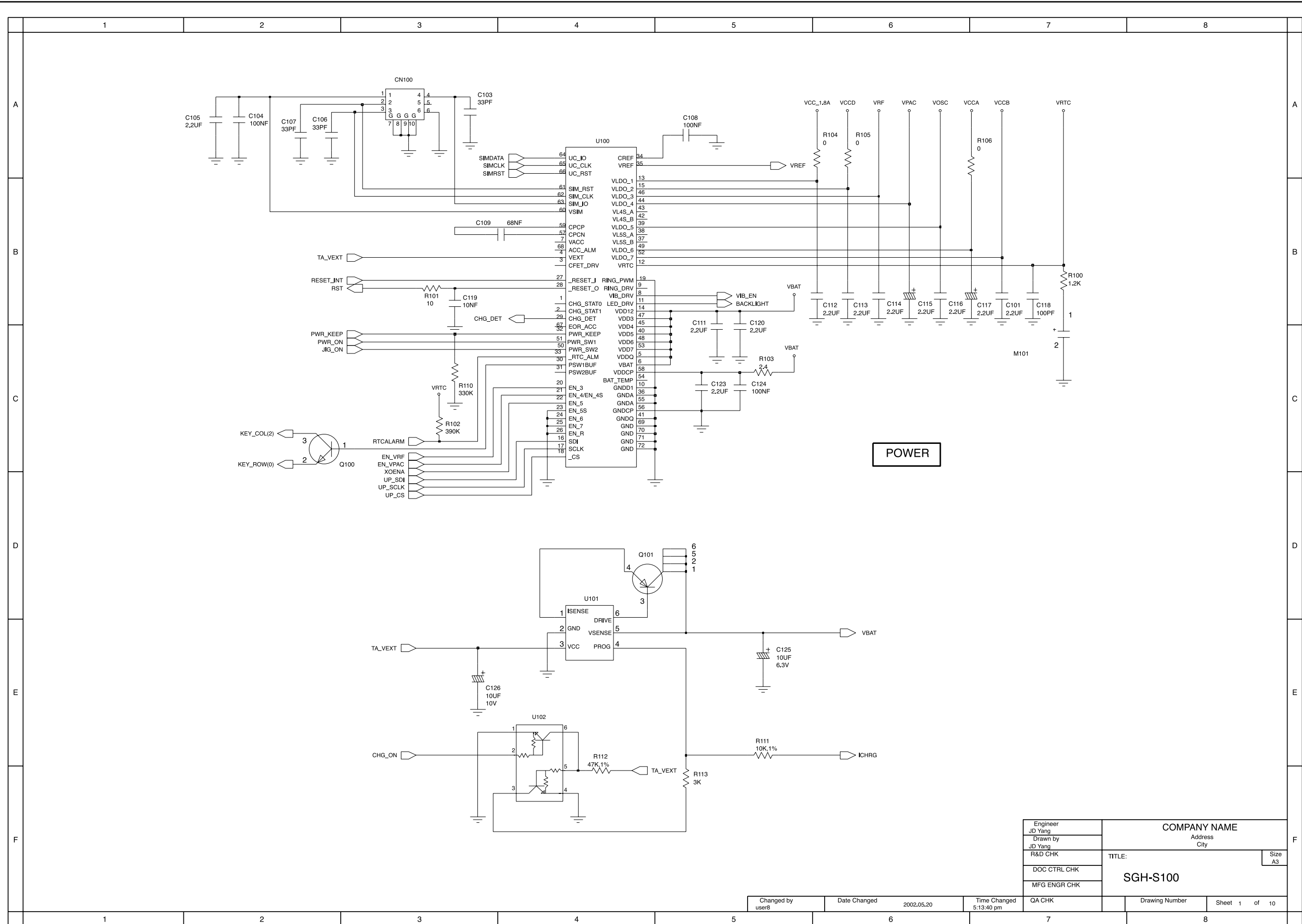
1. Main PCB Top Diagram



2. Main PCB Bottom Diagram



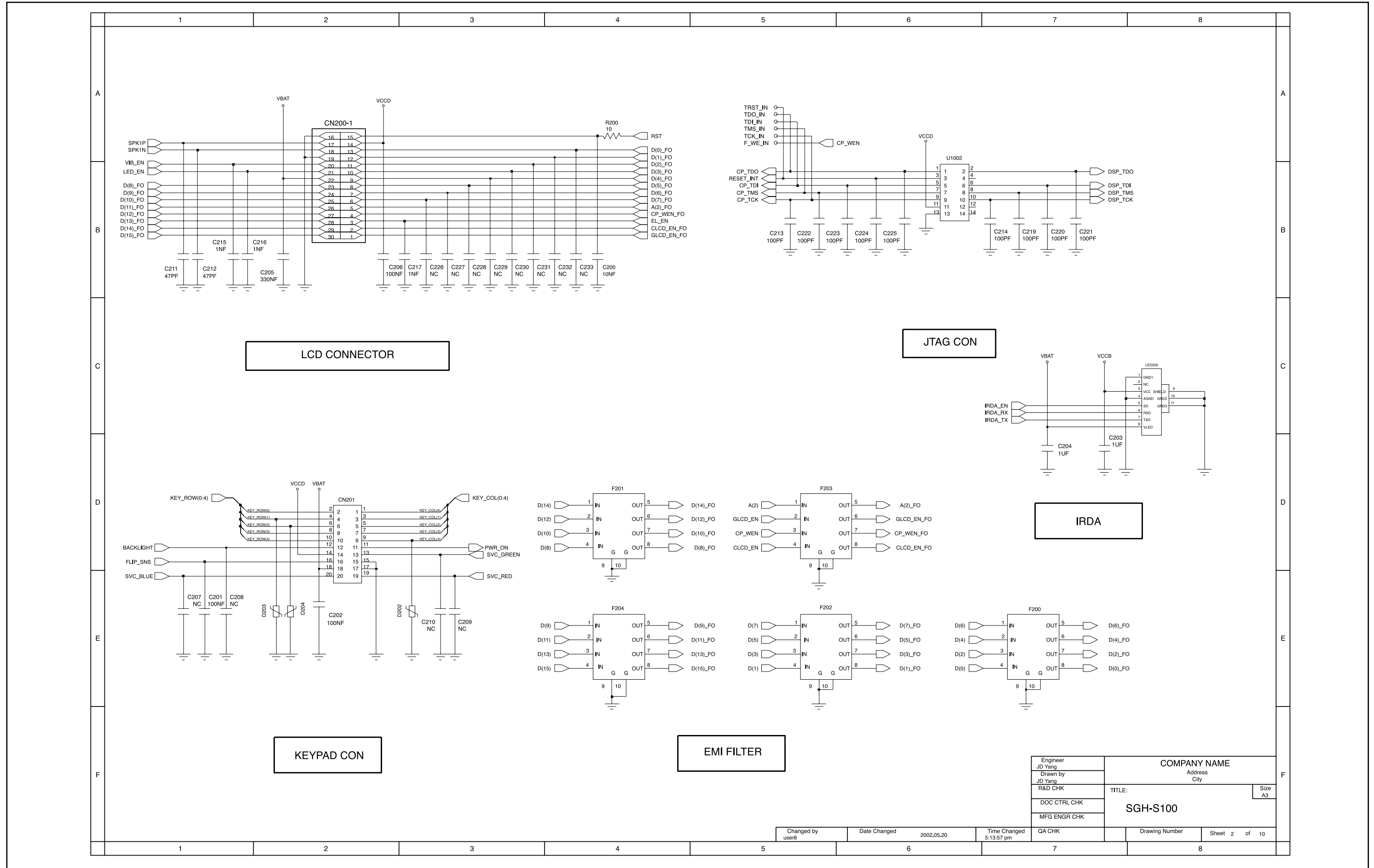
5-15. Power Circuit Diagram



Engineer JD Yang	COMPANY NAME	
Drawn by JD Yang	Address City	
R&D CHK	TITLE:	Size A3
DOC CTRL CHK	SGH-S100	
MFG ENGR CHK		

Changed by user8	Date Changed 2002.05.20	Time Changed 5:13:40 pm	QA CHK	Drawing Number	Sheet 1 of 10
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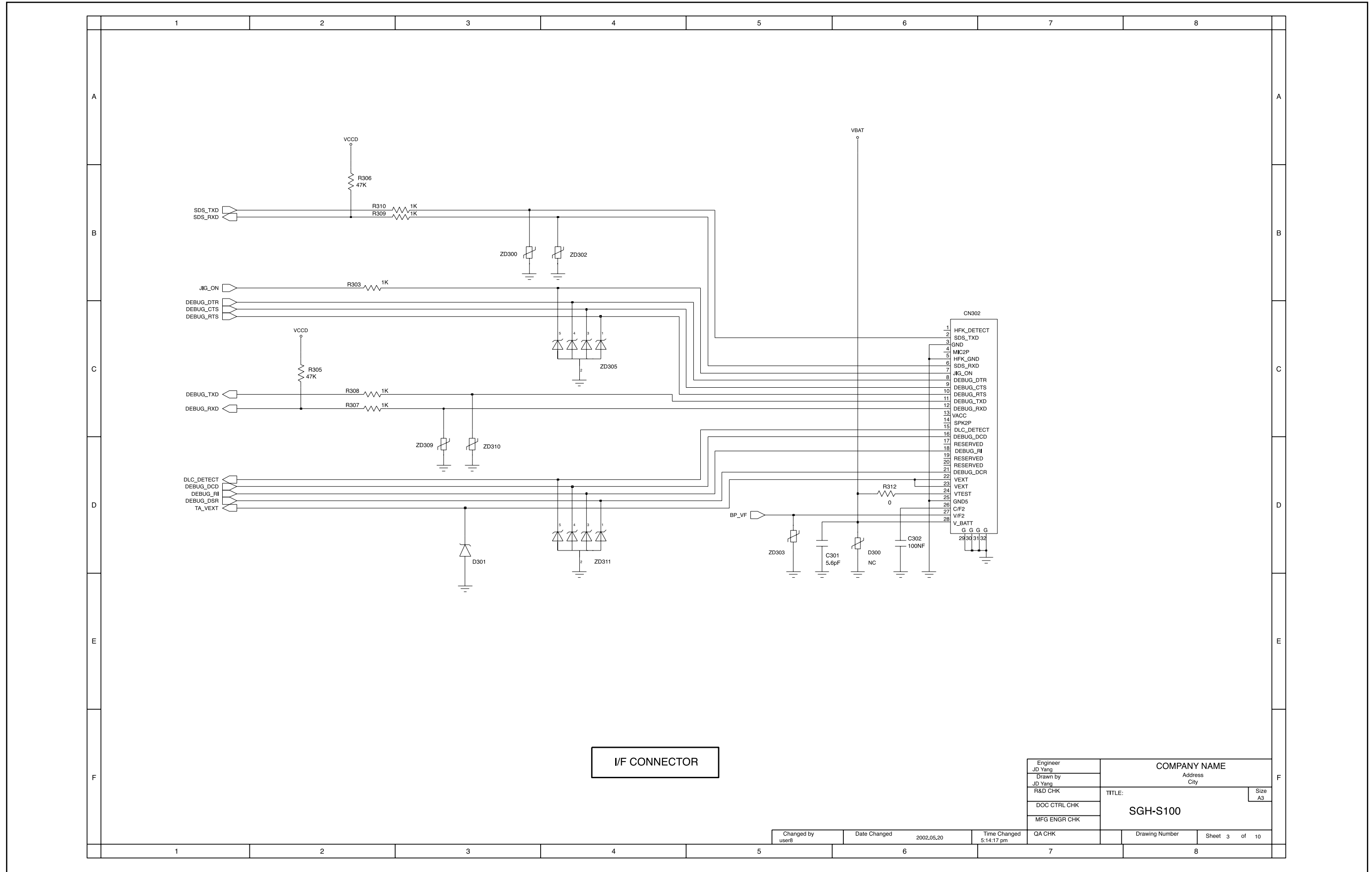
5-16. LCD & KEYPAD & EMI FILTER & JTAG & IRDA Connector Circuit Diagram



Engineer JD Yang	COMPANY NAME	
Drawn by JD Yang	Address City	
R&D CHK	TITLE:	Size A3
DOC CTRL CHK	SGH-S100	
MFG ENGR CHK	Drawing Number	Sheet 2 of 10

Changed by user8	Date Changed 2002,05,20	Time Changed 5:13:57 pm
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5-17. I/F Connector Circuit Diagram

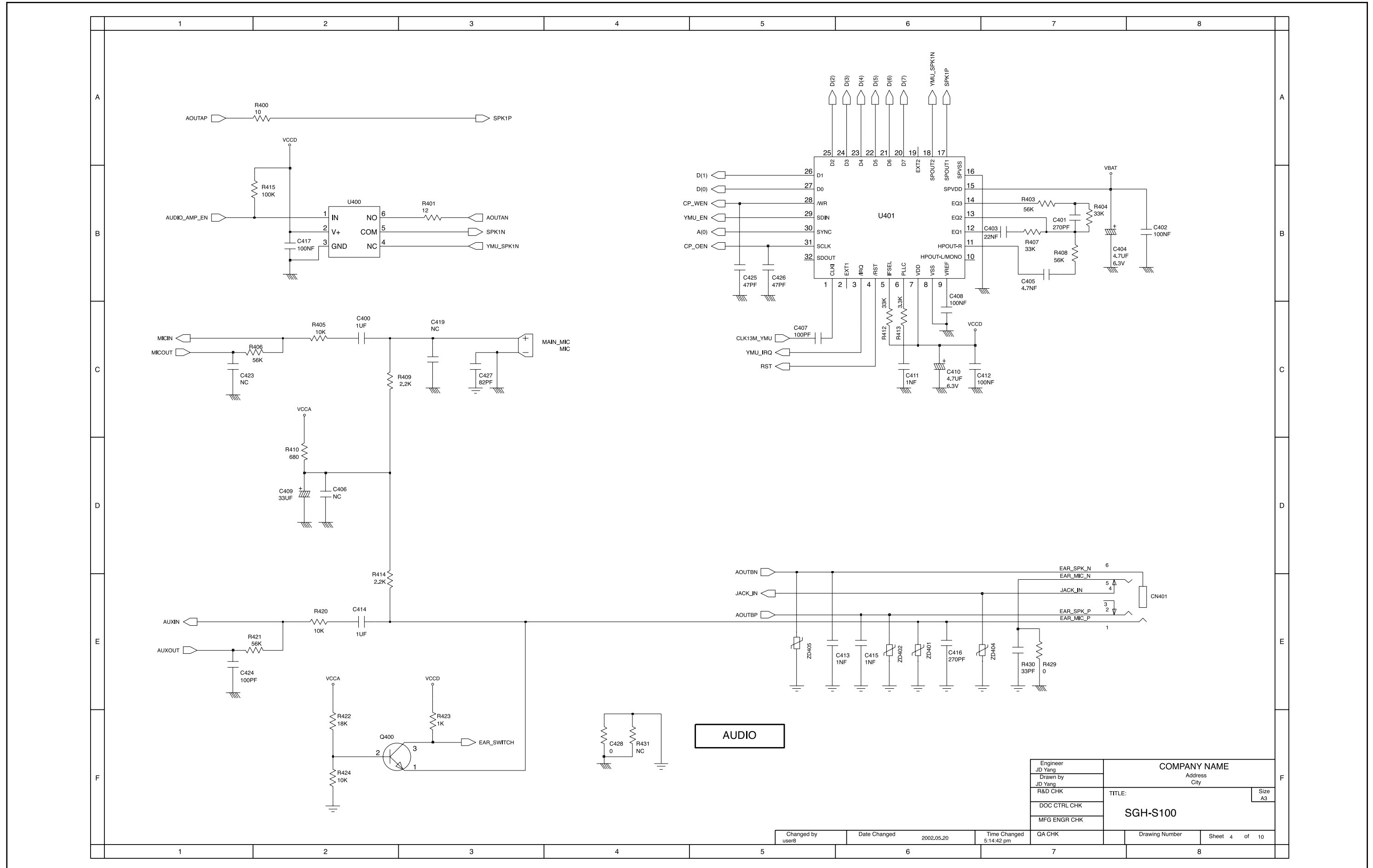


I/F CONNECTOR

Engineer JD Yang	COMPANY NAME	
Drawn by JD Yang	Address City	
R&D CHK	TITLE:	Size A3
DOC CTRL CHK	SGH-S100	
MFG ENGR CHK		

Changed by user8	Date Changed 2002.05.20	Time Changed 5:14:17 pm	QA CHK	Drawing Number	Sheet 3 of 10
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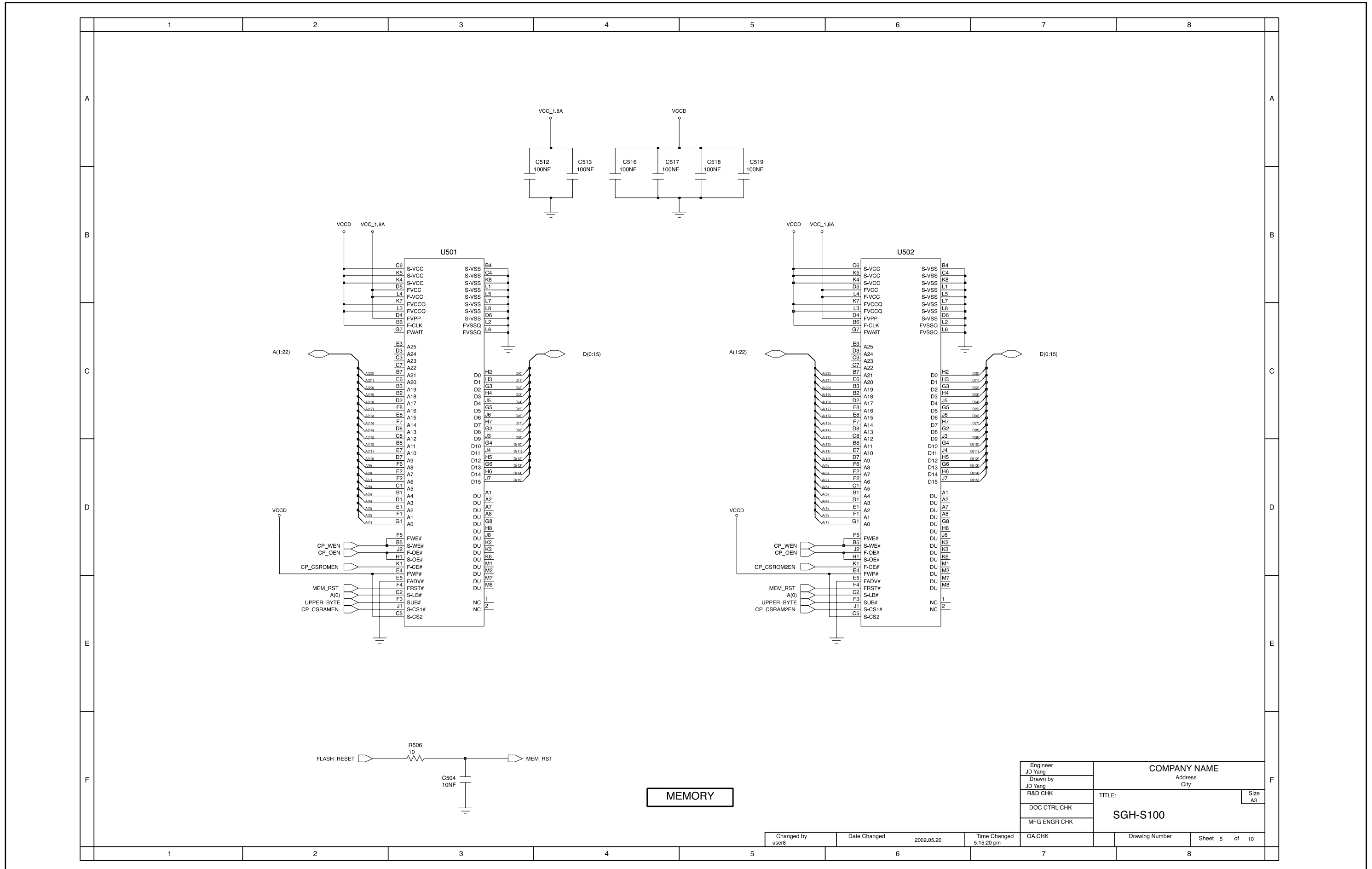
5-18. Audio Circuit Diagram



Engineer JD Yang	COMPANY NAME	
Drawn by JD Yang	Address City	
R&D CHK	TITLE:	Size A3
DOC CTRL CHK	SGH-S100	
MFG ENGR CHK	Drawing Number	Sheet 4 of 10

Changed by user8	Date Changed 2002.05.20	Time Changed 5:14:42 pm
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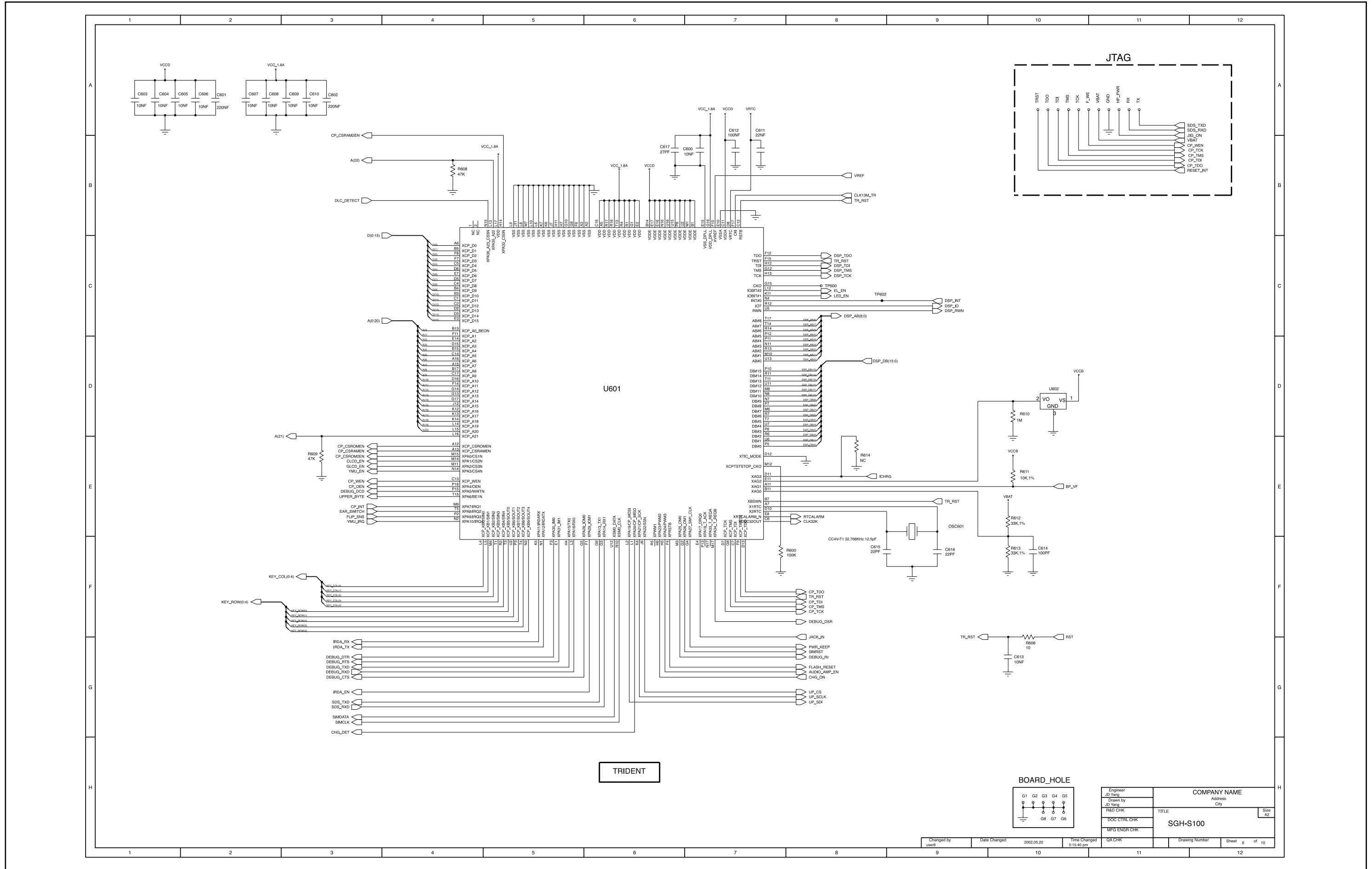
5-19. Memory & Buzzer Circuit Diagram



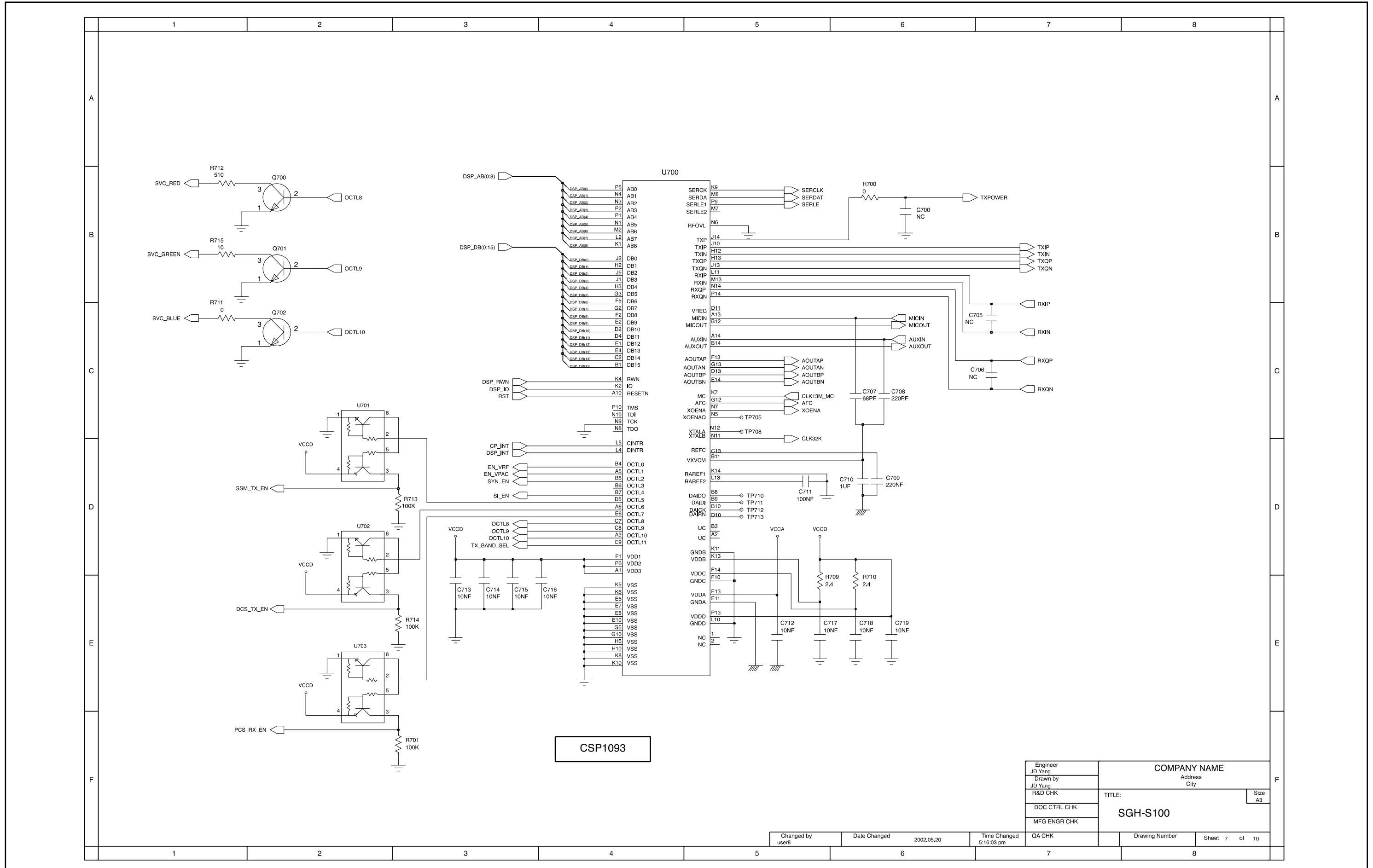
Engineer JD Yang	COMPANY NAME	
Drawn by JD Yang	Address City	
R&D CHK	TITLE:	Size A3
DOC CTRL CHK	SGH-S100	
MFG ENGR CHK	Drawing Number	Sheet 5 of 10
QA CHK		

Changed by user8	Date Changed 2002.05.20	Time Changed 5:15:20 pm
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5-20. Trident Circuit Diagram



5-21. CSP1089 Circuit Diagram

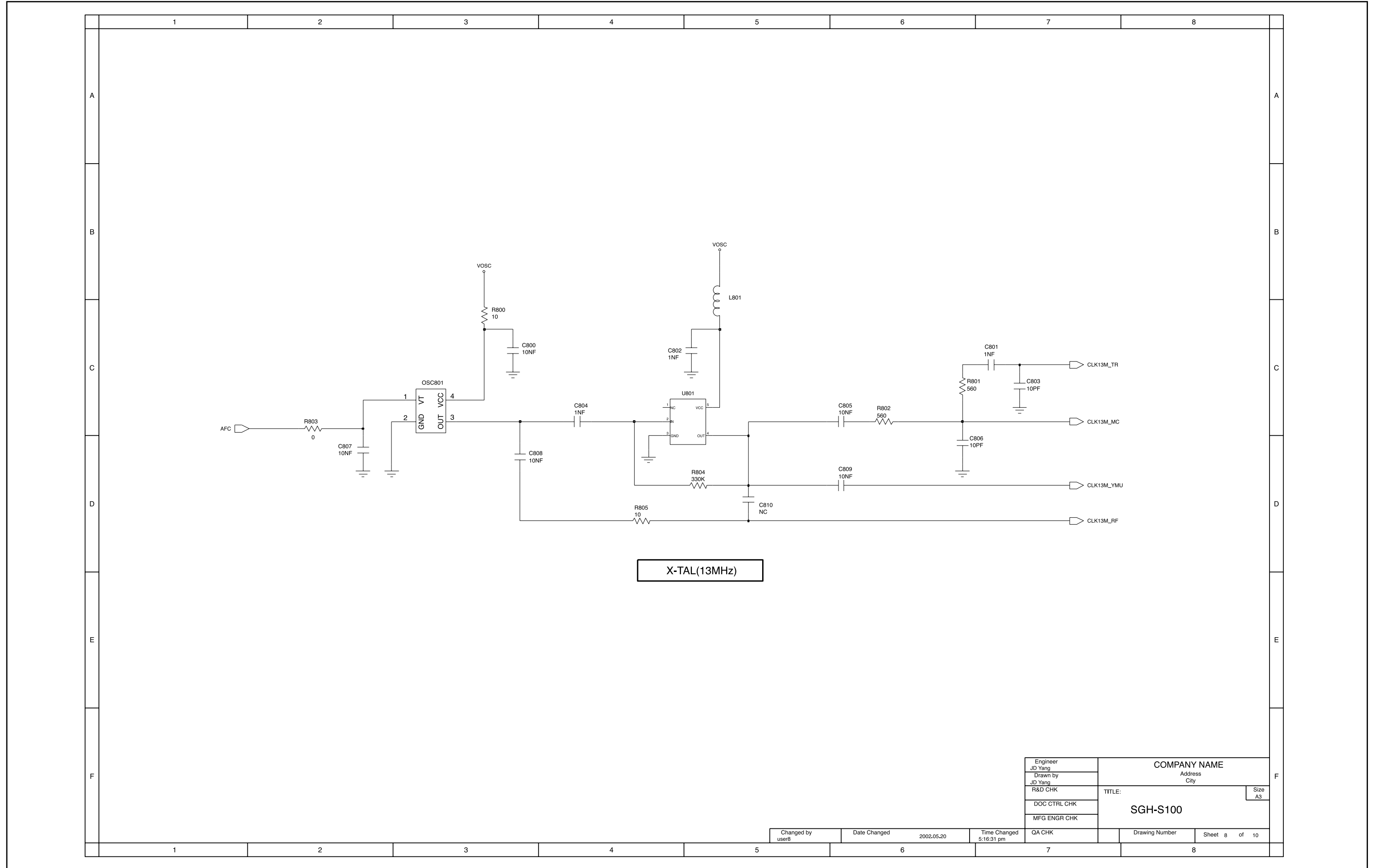


CSP1093

Engineer JD Yang	COMPANY NAME	
Drawn by JD Yang	Address City	
R&D CHK	TITLE: SGH-S100	Size A3
DOC CTRL CHK		
MFG ENGR CHK		
QA CHK	Drawing Number	Sheet 7 of 10

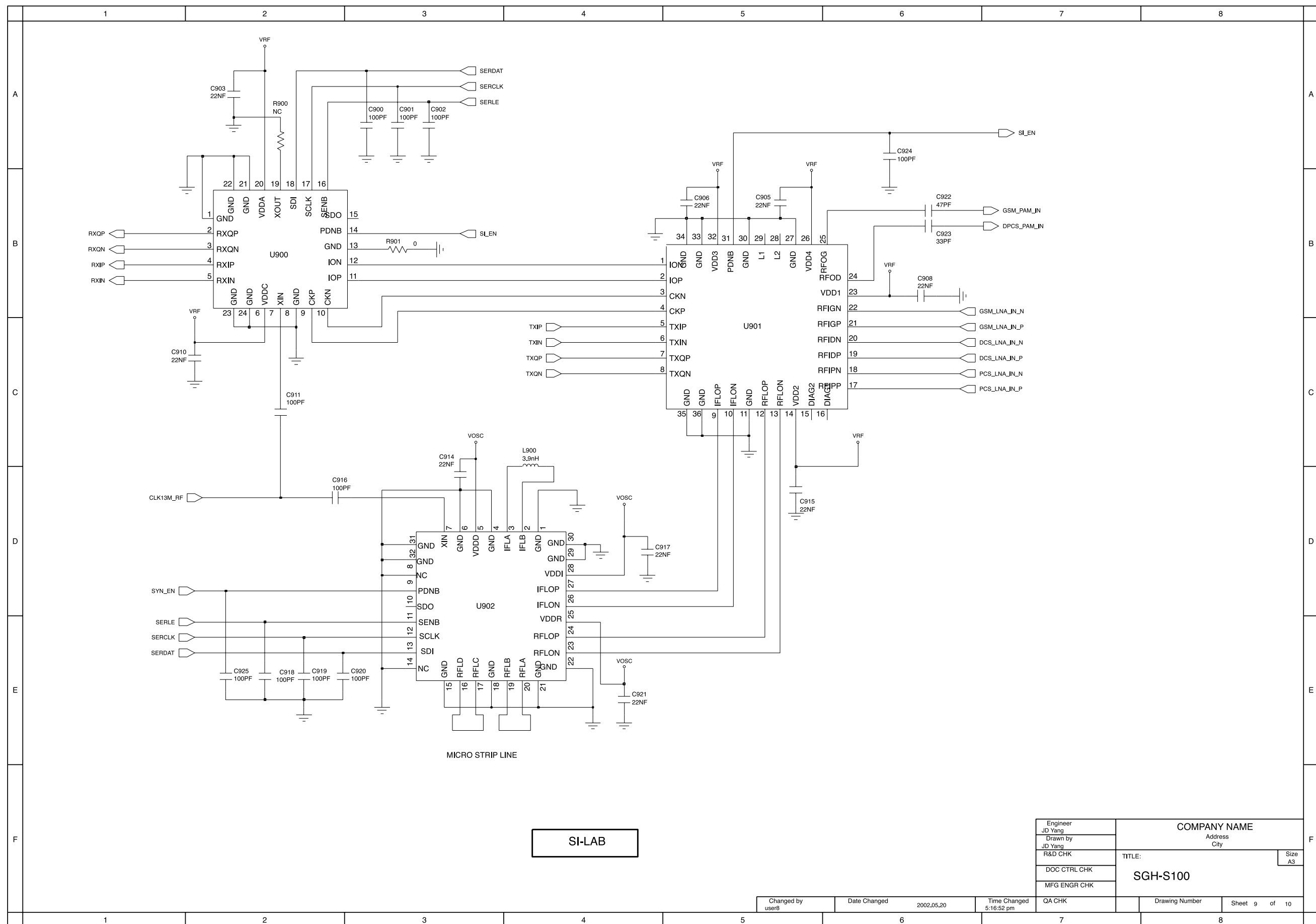
Changed by user6	Date Changed 2002.05.20	Time Changed 5:16:03 pm
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5-22. X-TAL(13 MHz) & SIM Circuit Diagram



Engineer JD Yang	COMPANY NAME	
Drawn by JD Yang	Address City	
R&D CHK	TITLE:	Size A3
DOC CTRL CHK	SGH-S100	
MFG ENGR CHK	QA CHK	Drawing Number
Changed by user8	Date Changed 2002.05.20	Time Changed 5:16:31 pm
Sheet 8 of 10		

5-23. Bright3 Circuit Diagram



SI-LAB

Engineer JD Yang	COMPANY NAME	
Drawn by JD Yang	Address City	
R&D CHK	TITLE:	Size A3
DOC CTRL CHK	SGH-S100	
MFG ENGR CHK	QA CHK	Drawing Number
		Sheet 9 of 10

Changed by user8	Date Changed 2002.05.20	Time Changed 5:16:52 pm
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5-24. Transmitter & Receiver Circuit Diagram

