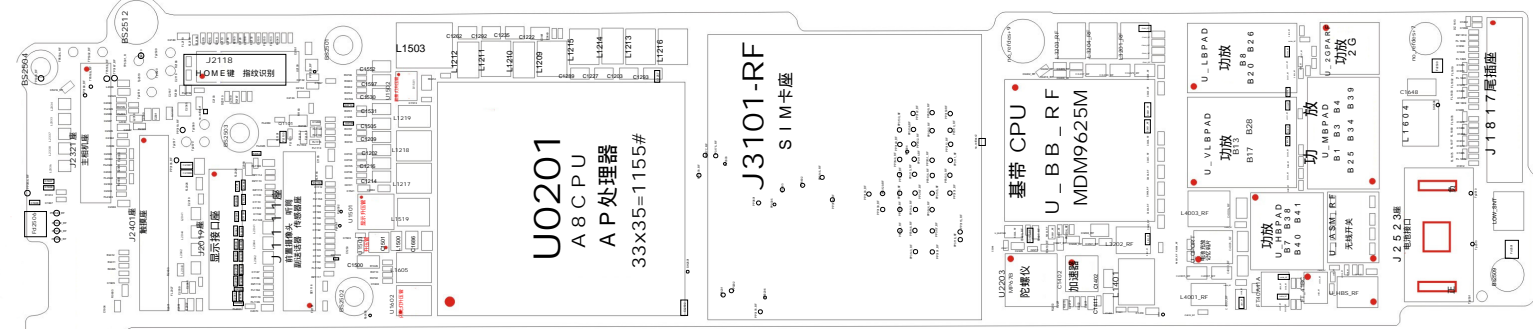
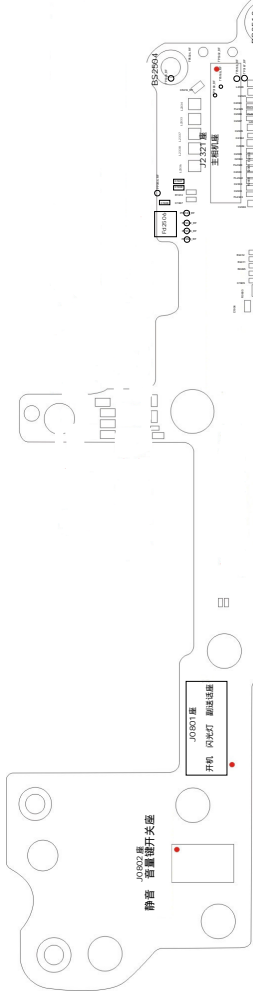


iPhone6元件分布位置图
(支持搜索放大)



borneoflasher
PROFESSIONAL TRAINING CENTRE INDONESIA



borneoflasher
 PROFESSIONAL TRAINING CENTRE INDONESIA

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

Thu Apr 17 17:11:44 2015

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
7	0002727241	ENGINEERING RELEASED		2014-04-18

N61 CARRIER BUILD - IPHONE 6

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47	MID BAND PAD	N61_RADIO_MLB	03/24/2014
48	HIGH BAND PAD	N61_RADIO_MLB	03/24/2014
49	ANTENNA SWITCH	N61_RADIO_MLB	03/24/2014
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57	ON BOARD JUMPER FLEX	N61_RADIO_MLB	03/24/2014
58	DSSS	N61_RADIO_MLB	03/24/2014

NAND BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S0998	1	NAND,19NM,16GX8,MLC,PPN1.5	U0604	CRITICAL	NAND_16G
335S0993	1	NAND,19NM,32GX8,MLC,PPN1.5	U0604	CRITICAL	NAND_32G
335S0994	1	NAND,19NM,64GX8,MLC,PPN1.5	U0604	CRITICAL	NAND_64G
335S00010	1	NAND,19NM,128GX8,TLC,PPN1.5	U0604	CRITICAL	NAND_128G
138S0867	1	CAP,XSR,10UF,20%,6.3V,0.65MM,HRTZ,0402	C0610,C0611,C0614,C0634	CRITICAL	NAND_16G
138S0867	1	CAP,XSR,10UF,20%,6.3V,0.65MM,HRTZ,0402	C0613,C0633,C0610,C0611,C0614,C0634	CRITICAL	NAND_32G & NAND_64G
138S00003	1	CAP,XSR,15UF,20%,6.3V,0.65MM,HRTZ,0402	C0613,C0633,C0610,C0611,C0614,C0634	CRITICAL	NAND_128G

ALTERNATE NAND BOM OPTIONS

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS
335S0992	335S0998	ALTERNATE	J0604	TOSHIBA,NAND,16GB
335S1038	335S0998	ALTERNATE	J0604	HYNIX,NAND,16GB
335S1040	335S0994	ALTERNATE	J0604	HYNIX,NAND,64GB
335S00014	335S0994	ALTERNATE	J0604	TOSHIBA,NAND,64GB
335S00015	335S00010	ALTERNATE	J0604	TOSHIBA,NAND,128GB
335S00009	335S0994	ALTERNATE	J0604	SANDISK,NAND,64GB,TLC

SHIELD BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
604-00241	1	SUBASSY, SHIELD, UPPER FRONT, N61	SH2501	CRITICAL	COMMON
604-00242	1	SUBASSY, SHIELD, LOWER FRONT, N61	SH2502	CRITICAL	COMMON
604-00243	1	SUBASSY, SHIELD, LOWER BACK, N61	SH2504	CRITICAL	COMMON
604-00244	1	SUBASSY, SA SHIELD, N61	SH2506	CRITICAL	COMMON

N61 BOM CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-9903	1	SCH, MLB, N61	SCH	CRITICAL	?
820-3486	1	PCBF, MLB, N61	PCB	CRITICAL	?
825-6838	1	EEEE FOR 639-4237 16GB	EEEE_G16T	CRITICAL	EEEE_16G
825-6838	1	EEEE FOR 639-5838 32GB	EEEE_G16R	CRITICAL	EEEE_32G
825-6838	1	EEEE FOR 639-5839 64GB	EEEE_G16Q	CRITICAL	EEEE_64G
825-6838	1	EEEE FOR 639-00025 128GB	EEEE_G16N	CRITICAL	EEEE_128G
825-6838	1	EEEE FOR 639-00208 16GB	EEEE_F98F	CRITICAL	EEEE_16G_TDDLTE
825-6838	1	EEEE FOR 639-00209 32GB	EEEE_FQKQ	CRITICAL	EEEE_32G_TDDLTE
825-6838	1	EEEE FOR 639-00210 64GB	EEEE_FQJY	CRITICAL	EEEE_64G_TDDLTE
825-6838	1	EEEE FOR 639-00212 128GB	EEEE_FY9W	CRITICAL	EEEE_128G_TLC_TDDLTE

ALTERNATE BOM OPTIONS

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS
152S1844	152S1836	ALTERNATE	L1604	TY ALT INDUCTOR
152S1842	152S1849	ALTERNATE	L1519	TY ALT INDUCTOR
197S0392	197S0369	ALTERNATE	Y1200	ESPON ALT XTAL
197S0399	197S0369	ALTERNATE	Y1200	NDK ALT XTAL
338S1285	338S1202	ALTERNATE	U1601	L21 SPKAMP
152S2034	152S2033	ALTERNATE	L1209,L1211,L1213	1.2MM 1.0UH, CYNTEC
152S00004	152S2049	ALTERNATE	L1210,L1212,L1214	1.2MM 0.47UH, CYNTEC
339S00005	339S0246	ALTERNATE	U0201	FIJI, B0, SAMSUNG
339S0247	339S0246	ALTERNATE	U0201	FIJI, B0, HYNIX
339S00006	339S0246	ALTERNATE	U0201	FIJI, B1, E
339S00007	339S0246	ALTERNATE	U0201	FIJI, B1, H
339S00008	339S0246	ALTERNATE	U0201	FIJI, B1, S
155S0773	155S0453	ALTERNATE		TY 1200HM FERRITE
118S0764	118S0717	ALTERNATE	R1309	3.92KOHM, 01005
343S0688	343S0638	ALTERNATE	U2401	CUMULUS C1, FAB4
138S00005	138S00003	ALTERNATE	C1290	15UF,0402,HRTZL CAP
155S00011	155S00008	ALTERNATE	L1135	CMC,900HM,MURATA
377S0168	377S0140	ALTERNATE	DZ1113	SUPPL,TRANS,VARIABLE,AMOTEC
155S0895	155S0610	ALTERNATE	FL1802,FL1809	FERROXID,1500NM,200NM,01005
138S0648	138S0652	ALTERNATE	C1018	CAP,4.7UF,20%,6.3V,0402,040-65MM
138S0657	138S0702	ALTERNATE	C1106	CAP,4.7UF,20%,4V,0402
338S00028	338S00017	ALTERNATE	J2203	CARBON,BOBCH,BMI162BC
338S00029	338S00017	ALTERNATE	J2203	CARBON,ST,AP60S2AA
335S00013	335S0894	ALTERNATE	J0301	ST BK EPRROM

SCH 051-9903
BRD 820-3486
MCO 056-6825

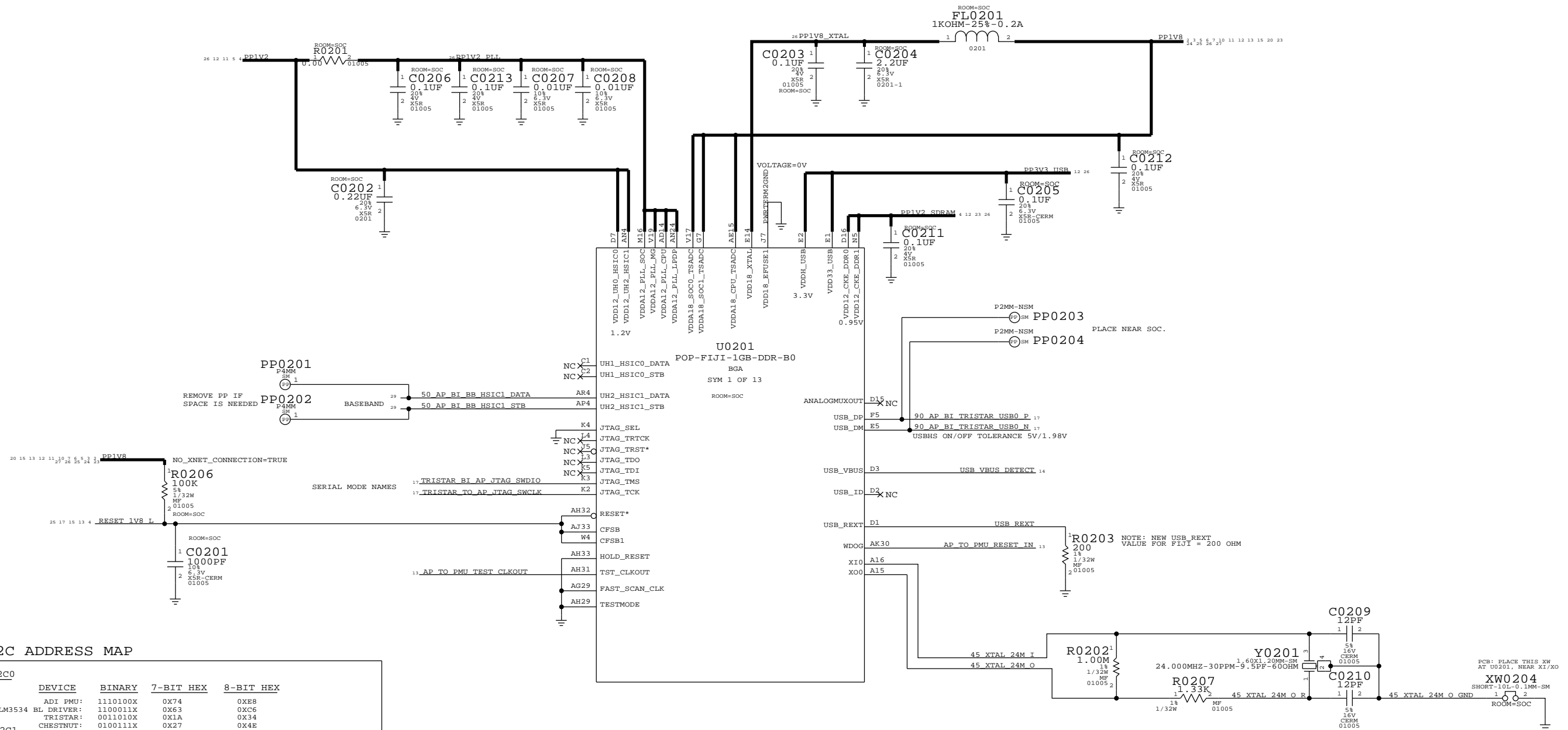
BOM 639-4237 (16GB,BETTER)
BOM 639-5838 (32GB,BEST)
BOM 639-5839 (64GB,ULTRA)

BOM 639-00208 (16GB,BETTER,DTD)
BOM 639-00209 (32GB,BEST,DTD)
BOM 639-00210 (64GB,ULTRA,DTD)

BOM 639-00025 (128GB,SUPREME,TLC) BOM 639-00212 (128GB,SUPREME,TLC,DTD)

DRAWING TITLE		SCHEM,MLB,N61	
Apple Inc.	DRAWING NUMBER	051-9903	SIZE D
	REVISION	7.0.0	
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FIJI: JTAG, USB, HSIC, XTAL



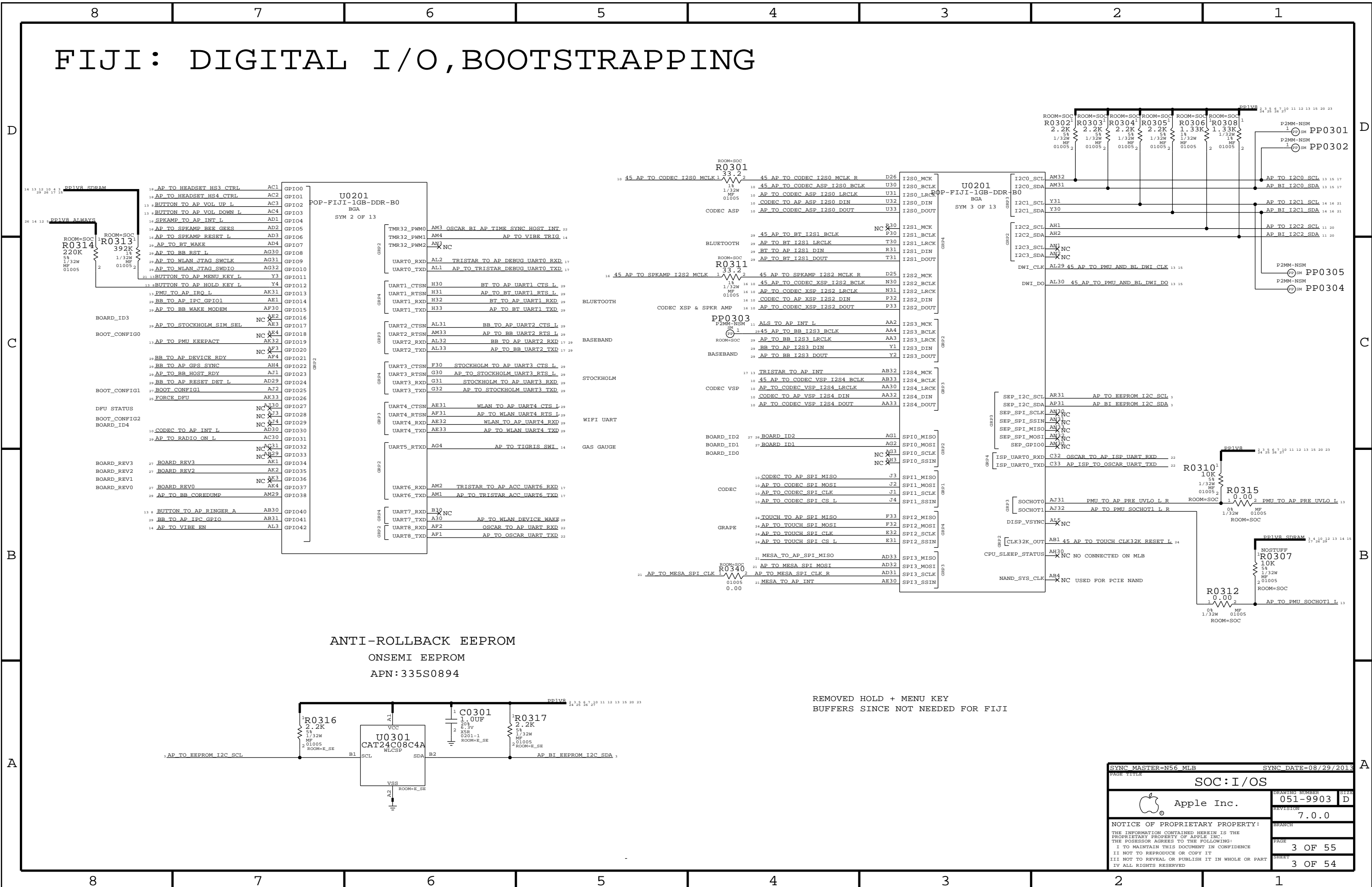
I2C ADDRESS MAP

I2C0	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
I2C0	ADI PMU:	1110100X	0X74	0XE8
	LM3534 BL DRIVER:	1100011X	0X63	0XC6
	TRISTAR:	0011010X	0X1A	0X34
	CHESTNUT:	0100111X	0X27	0X4E
I2C1	TIGRIS CHARGER:	1110101X	0X75	0XEA
	LINEAR VIBE:	1011010X	0X5A	0XB4
	CS35L19B AMP:	1000000X	0X40	0X80
	MESA EEPROM (MEMORY):	1010110X	0X56	0XAC
	MESA EEPROM (ID):	1011110X	0X5E	0XBC
I2C2	CT814 ALS:	0101001X	0X29	0X52
	DISPLAY EEPROM:	1010001X	0X51	0XA2
	RCAM I2C			
RCAM I2C	OPEL STROBE DRIVER:	1100011X	0X63	0XC6
	REAR FACING CAM:	0010000X	0X10	0X20
	VCM AF DRIVER:	0001100X	0X0C	0X18
FCAM I2C	FCAM I2C			
	FRONT FACING CAM:	0010000X	0X10	0X20

NOTE: ACCEL, GYRO, COMPASS ALL USING SPI (VIA OSCAR) FOR AP COMMUNICATION.

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
SOC:MAIN			
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FIJI: DIGITAL I/O, BOOTSTRAPPING



ANTI-ROLLBACK EEPROM
 ONSEMI EEPROM
 APN: 335S0894

REMOVED HOLD + MENU KEY
 BUFFERS SINCE NOT NEEDED FOR FIJI

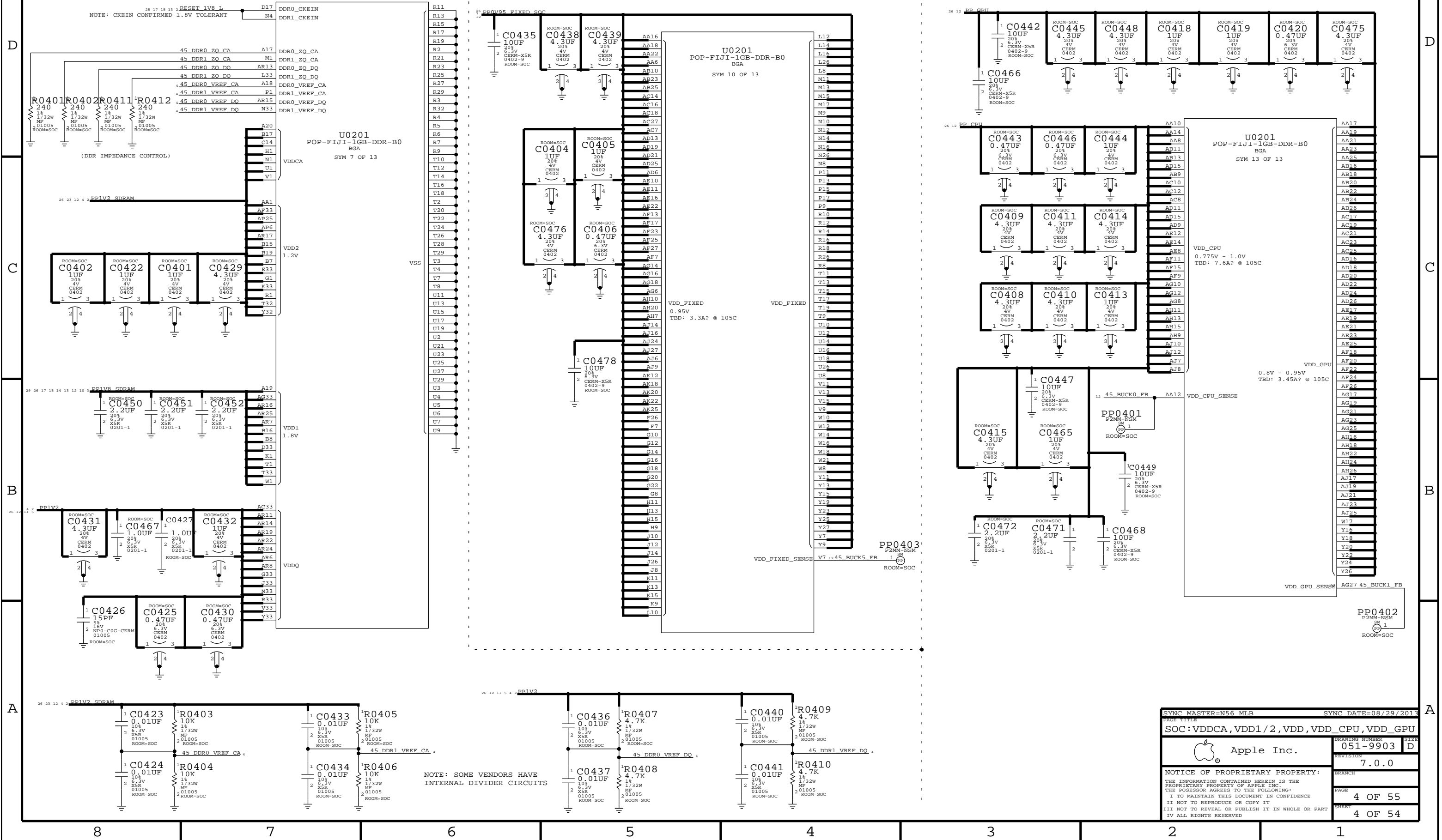
SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
SOC: I/O/S		DRAWING NUMBER	SIZE
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FIJI: VDDCA, VDD1/2, VDDQ, VDD, VDD_FIXED, VDD_CPU, VDD_GPU

VDDCA, VDD1/2, VDDQ

VDD

VDD_CPU, VDD_GPU

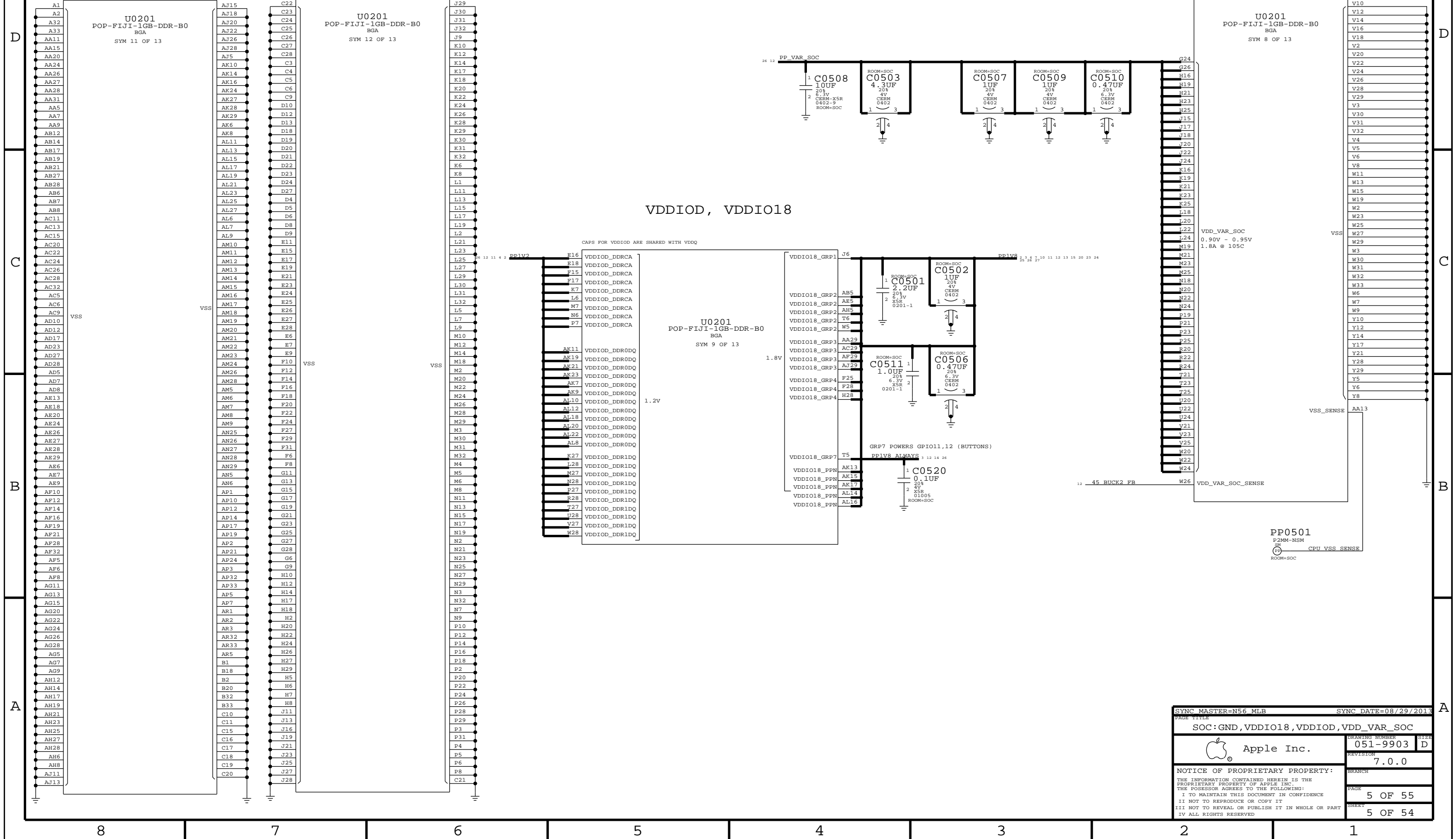


SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE SOC:VDDCA,VDD1/2,VDD,VDD_CPU,VDD_GPU			
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		PAGE	4 OF 55
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FIJI: VDDIOD, VDDIO18, VDD_VAR_SOC

JUST A FEW GNDS

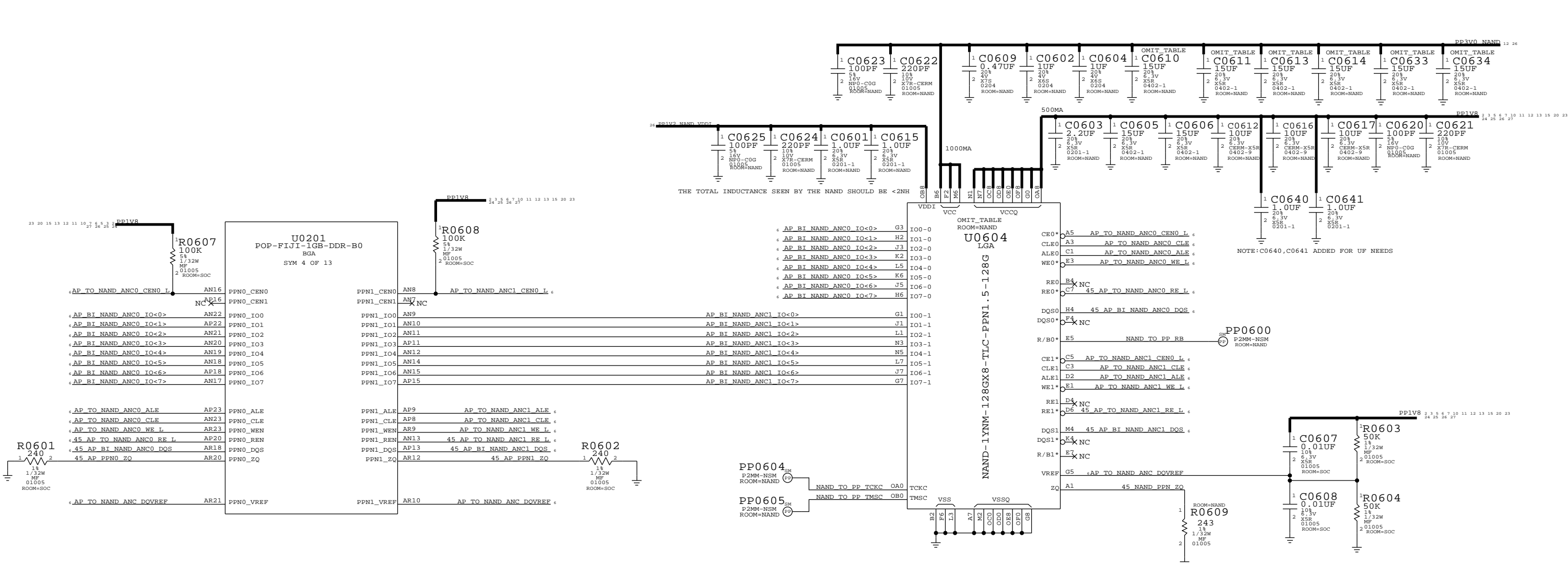
VDD_SRAM, VDD_SOC



SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE SOC: GND, VDDIO18, VDDIOD, VDD_VAR_SOC			
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PAGE 5 OF 55		SHEET 5 OF 54	

FIJI: NAND + 12X17 NAND PKG

SUPPORT FOR PPN1.5 (1.8V IO) ONLY



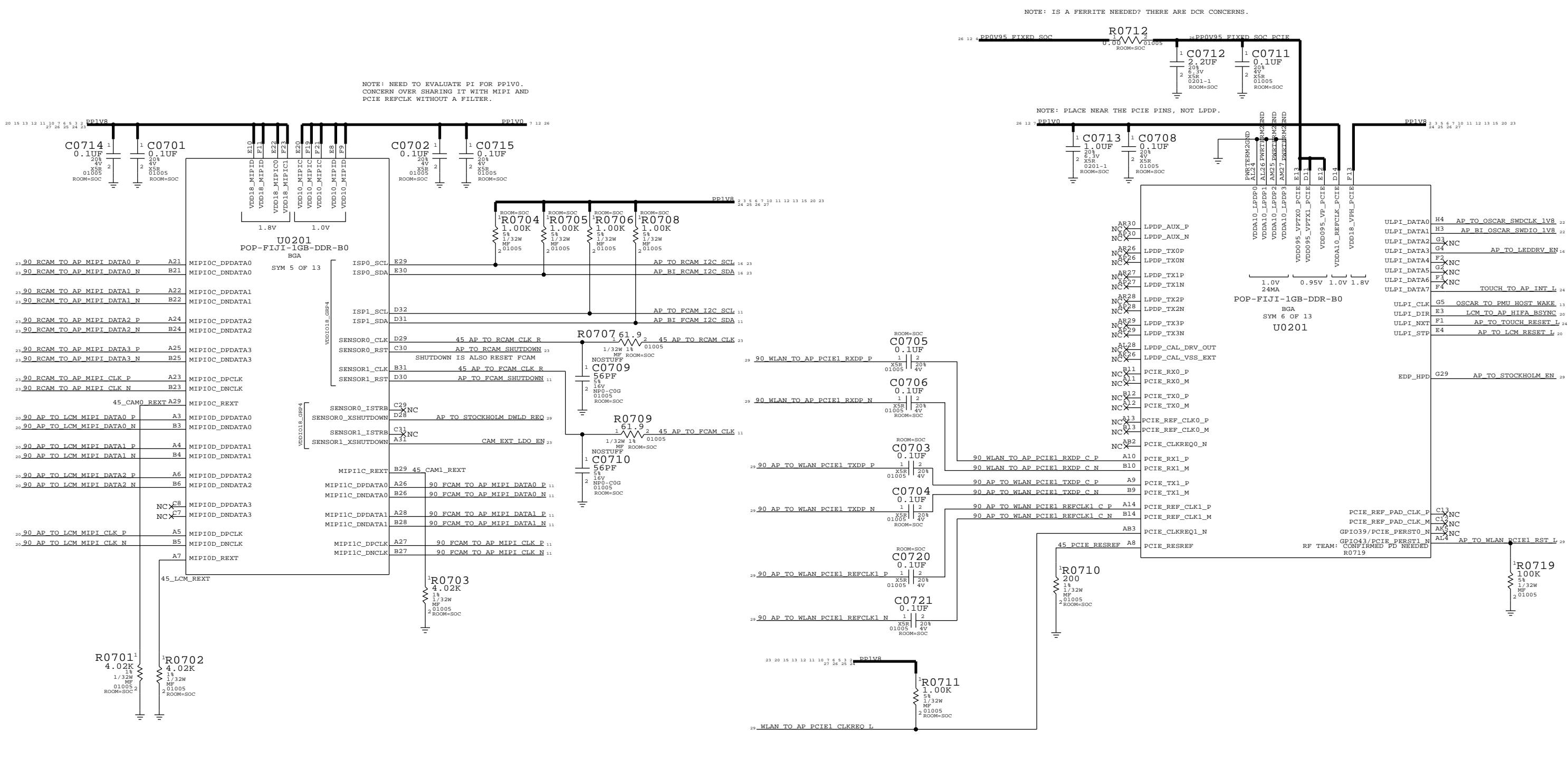
THE TOTAL INDUCTANCE SEEN BY THE NAND SHOULD BE <2NH

NOTE: NAND PADS SHOULD BE SHIELDED FROM TRACES WITH A GROUND PLANE

- PP0601 P4MM 1 AP BI NAND ANCO IO<6> NOTE: IO<6> PREFERRED BY MATT BYOM (N51) (IS A STATUS READY BIT)
- PP0602 P4MM 1 45 AP TO NAND ANCO RE L
- PP0603 P4MM 1 45 AP BI NAND ANCO DQS

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
SOC: NAND		DRAWING NUMBER	SIZE
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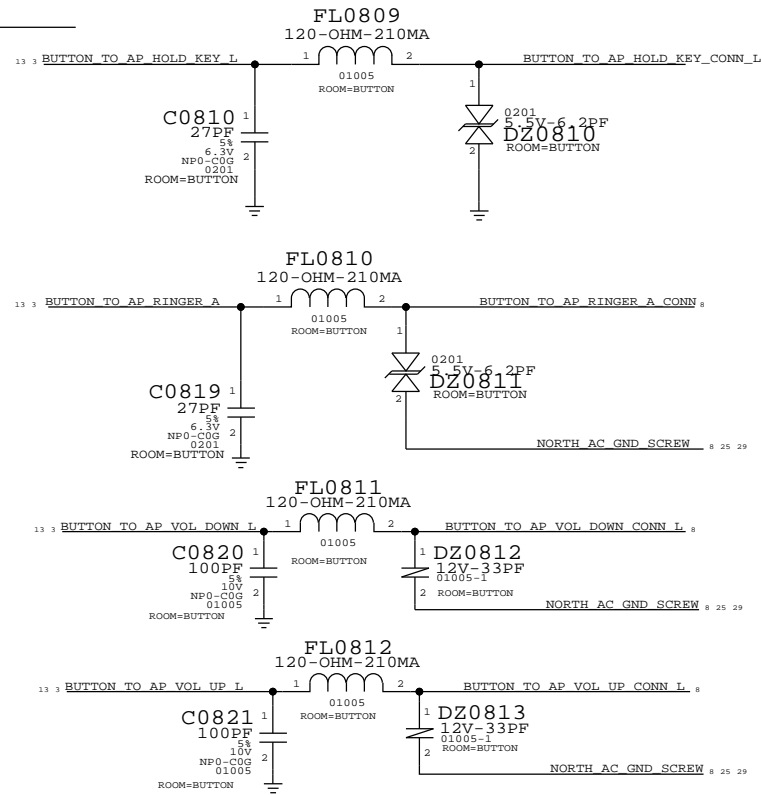
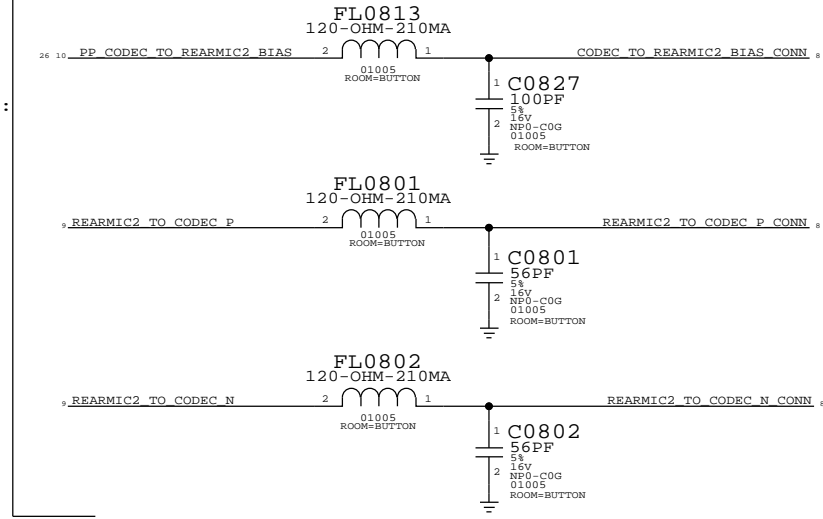
FIJI: HIGH SPEED DIG (CAM, LCD, LPDP, PCIE)



SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
SOC: CAM, LCD, LPDP, PCIE			
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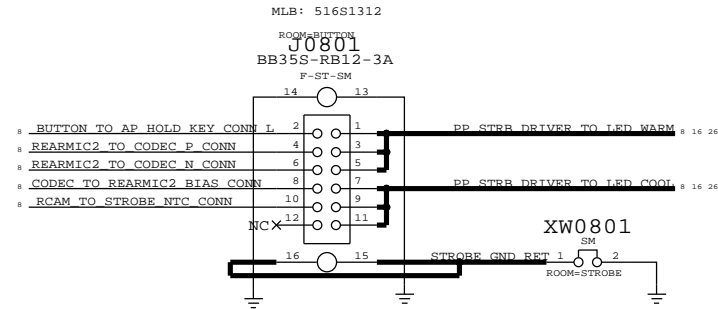
BUTTON FLEX (BUTTONS, ANC REF MIC, STROBE, STROBE_NTC, WIFI FLEX PAC)

MIC2 (ANC REF MIC):
MIC2_4 BIAS,
MIC2_P,_N

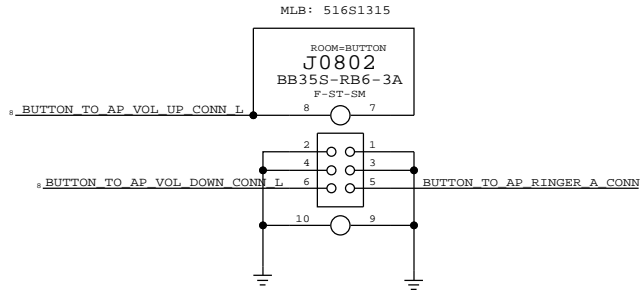


BUTTONS:
RINGER, HOLD,
VOL_UP/DOWN,

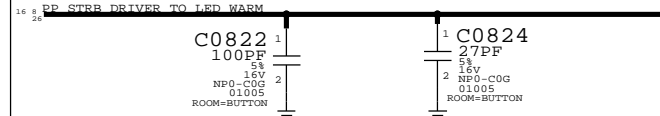
RIGHT BUTTON B2B



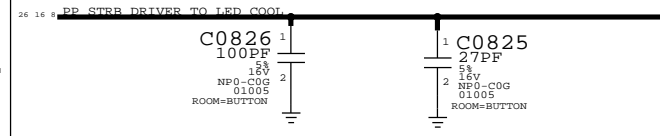
LEFT BUTTON B2B



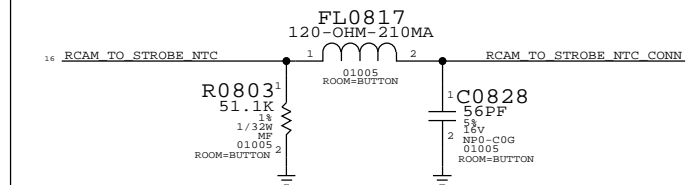
STROBE:
LED WARM



STROBE:
LED COOL



STROBE:
NTC

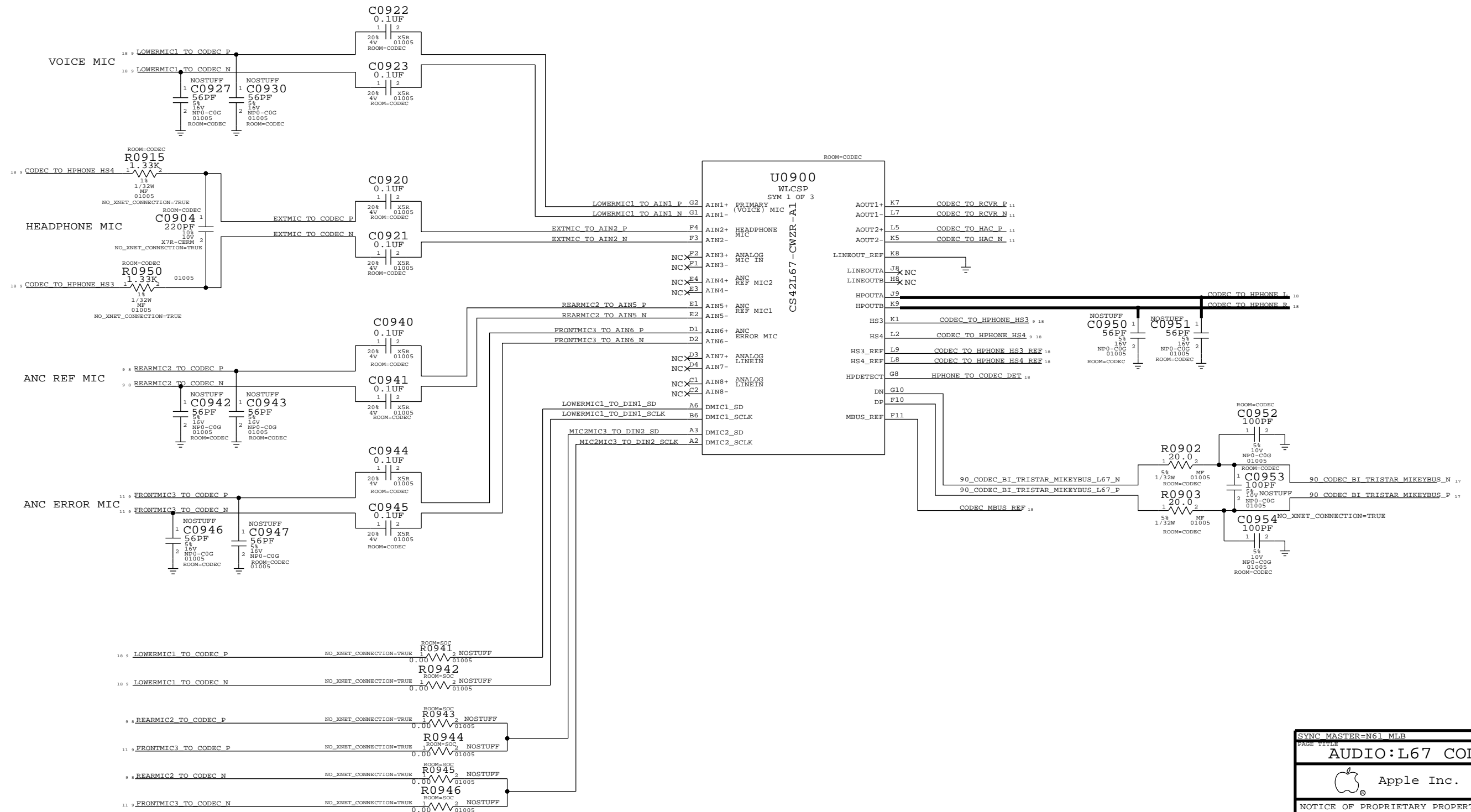


SYNC MASTER=N61_MLB		SYNC DATE=08/26/2013	
PAGE TITLE IO:BUTTON FLEX CONN			
Apple Inc.	DRAWING NUMBER	051-9903	SIZE D
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L67 AUDIO CODEC

AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)



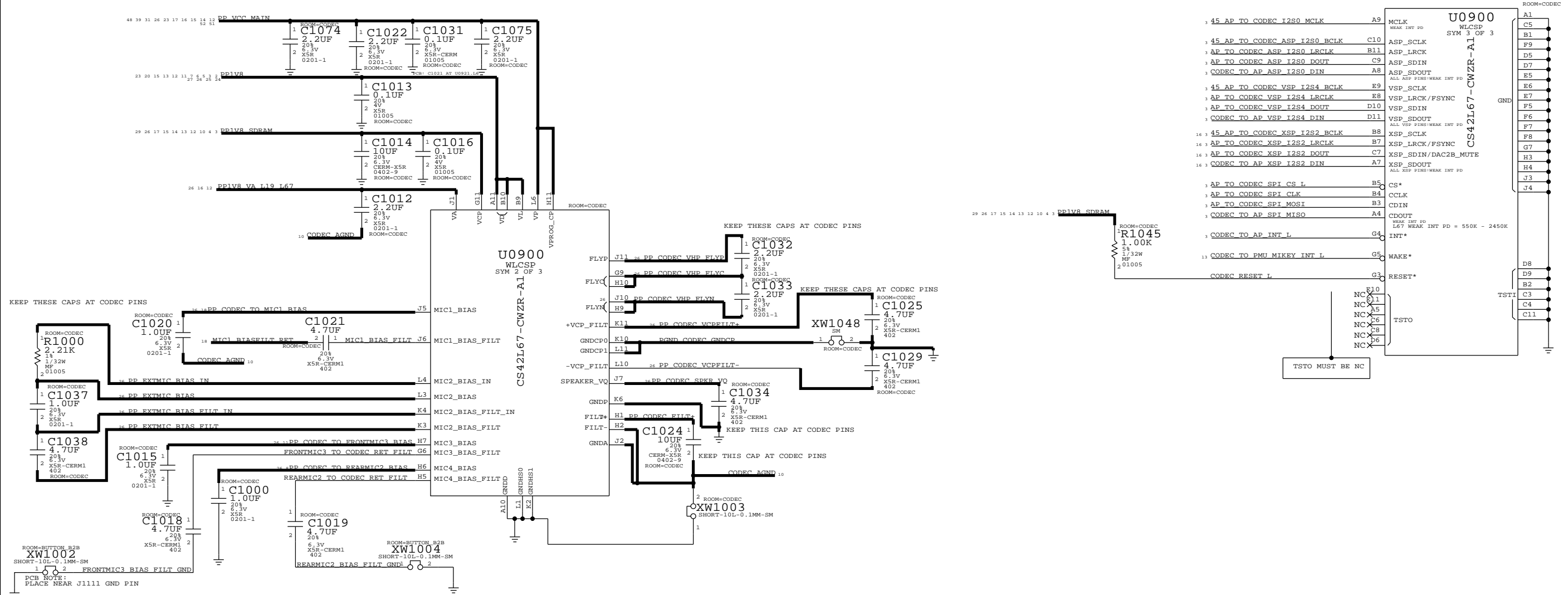
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PAGE TITLE			
AUDIO:L67 CODEC (1/2)			
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		REVISION	7.0.0
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L67 AUDIO CODEC

POWER, MICBIAS

DIGITAL SYSTEM I/O

NOTE: C1022 WAS REDUCED TO 2.2UF BECAUSE OF ADDITIONAL NEARBY VCC MAIN CAPS

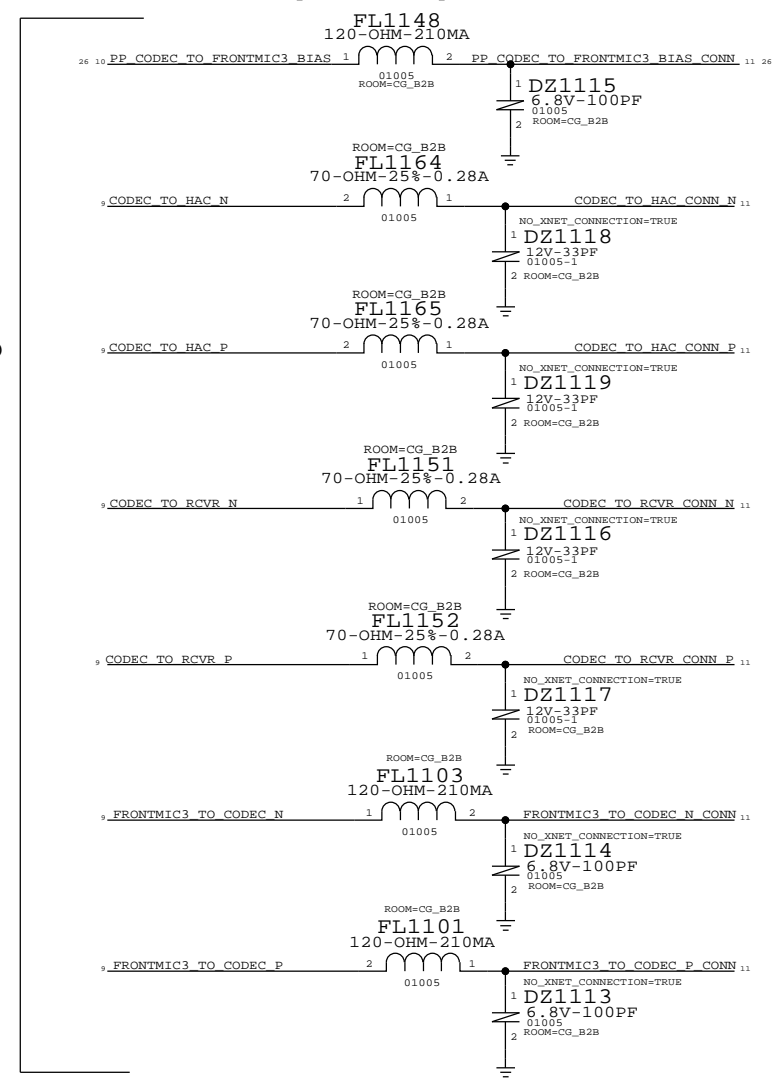
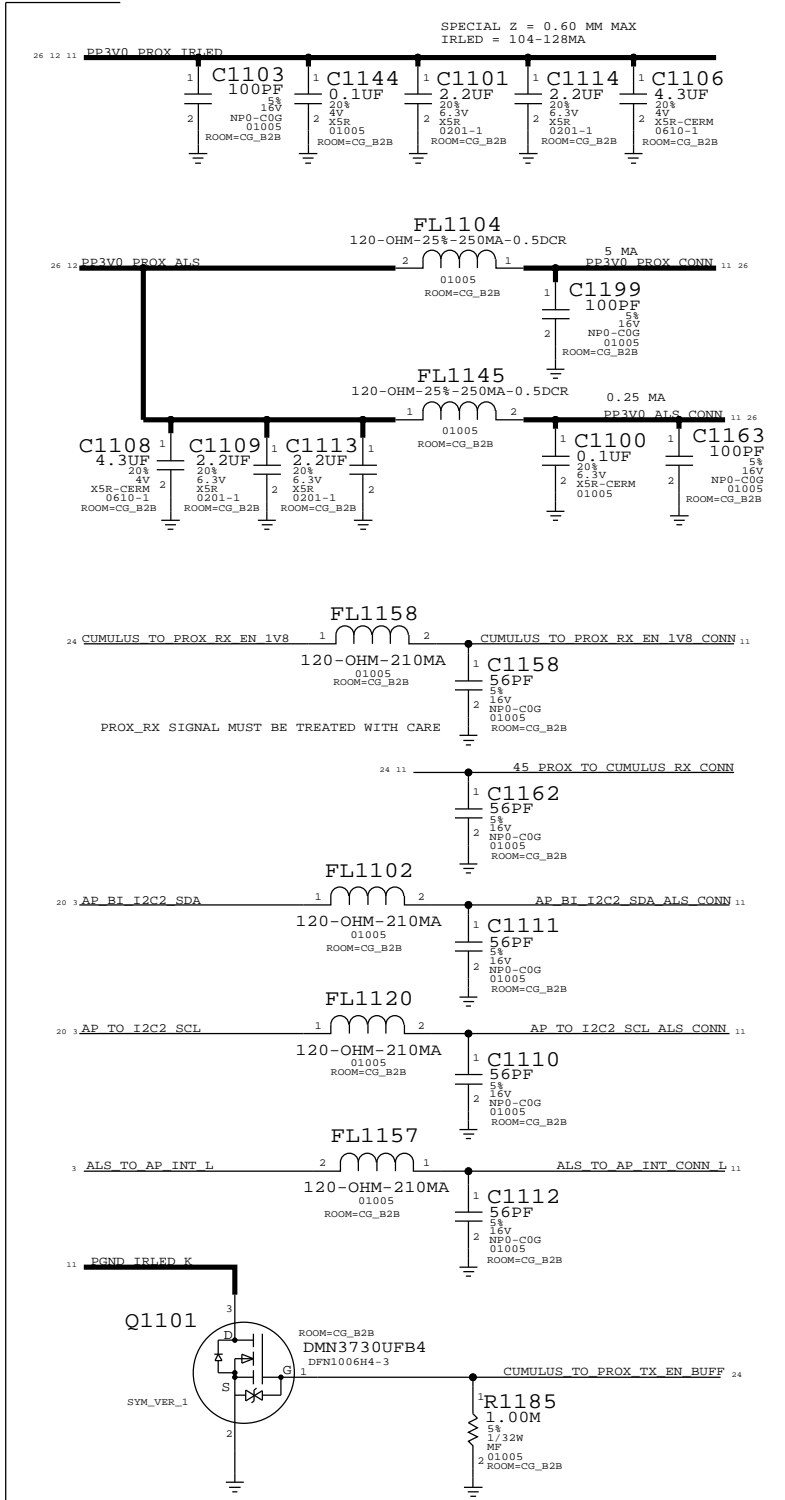
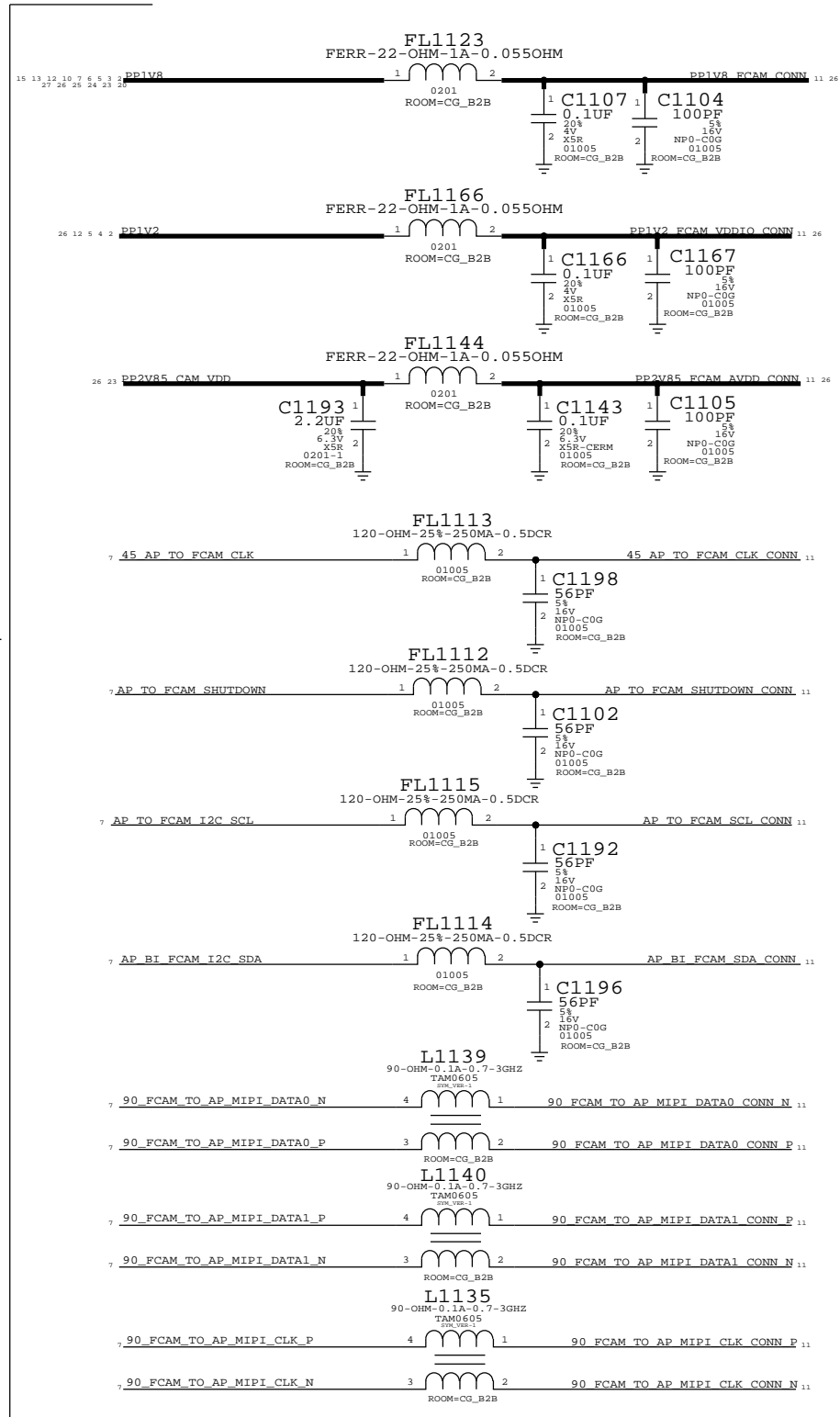
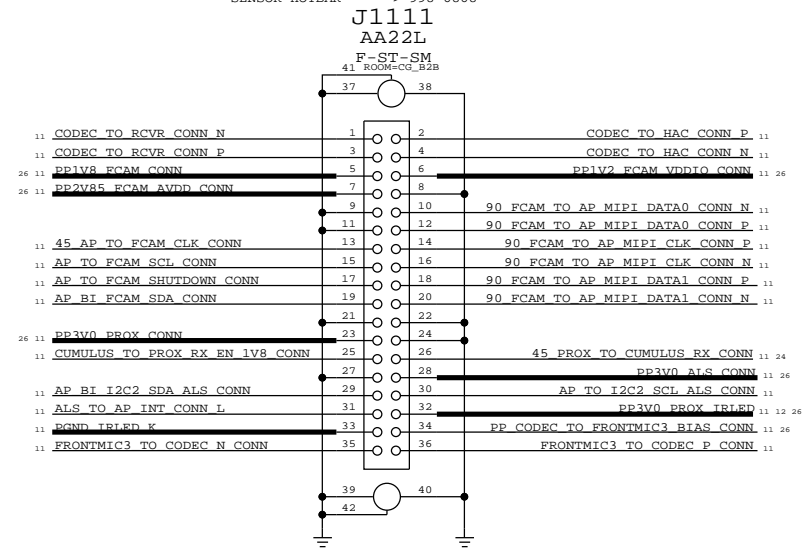


SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE AUDIO:L67 CODEC (2/2)			
Apple Inc.		DRAWING NUMBER 051-9903	SIZE D
		REVISION 7.0.0	
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		SHEET	10 OF 54

FRONT CAM FLEX B2B

(FCAM, PROX, ALS, RECEIVER, ANC ERROR MIC)

THIS ON ONE MLB ---> 516S1081 RECEPTACLE
SENSOR HOTBAR ---> 998-6868



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
CAMERA: FRONT FLEX CONN			
Apple Inc.		DRAWING NUMBER	051-9903
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		PAGE	11 OF 55
		SHEET	11 OF 54

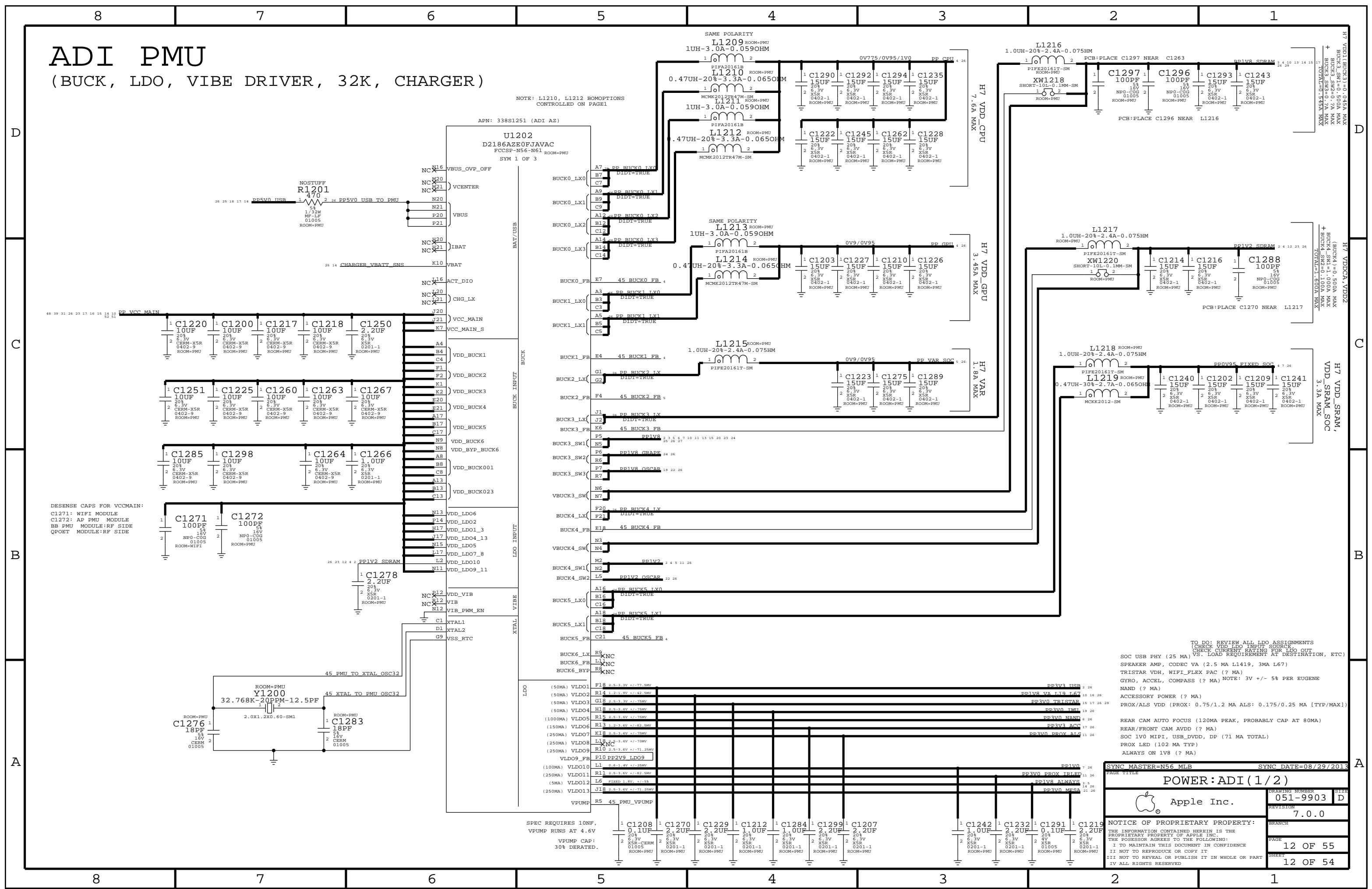
ADI PMU

(BUCK, LDO, VIBE DRIVER, 32K, CHARGER)

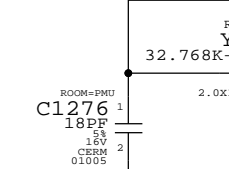
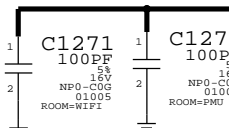
NOTE: L1210, L1212 BOMPTIONS CONTROLLED ON PAGE1

APN: 338S1251 (ADI AZ)

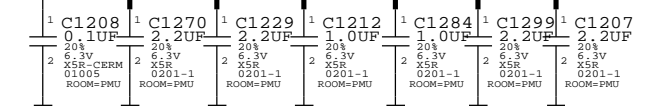
U1202
D2186AZE0FJAVAC
FCCSP-N56-N61 ROOM=PMU
SYM 1 OF 3



DESENSE CAPS FOR VCCMAIN:
C1271: WIFI MODULE
C1272: AP PMU MODULE
EB PMU MODULE:RF SIDE
QPOET MODULE:RF SIDE



SPEC REQUIRES 10NF, VPUMP RUNS AT 4.6V
VPUMP CAP: 30% DERATED.

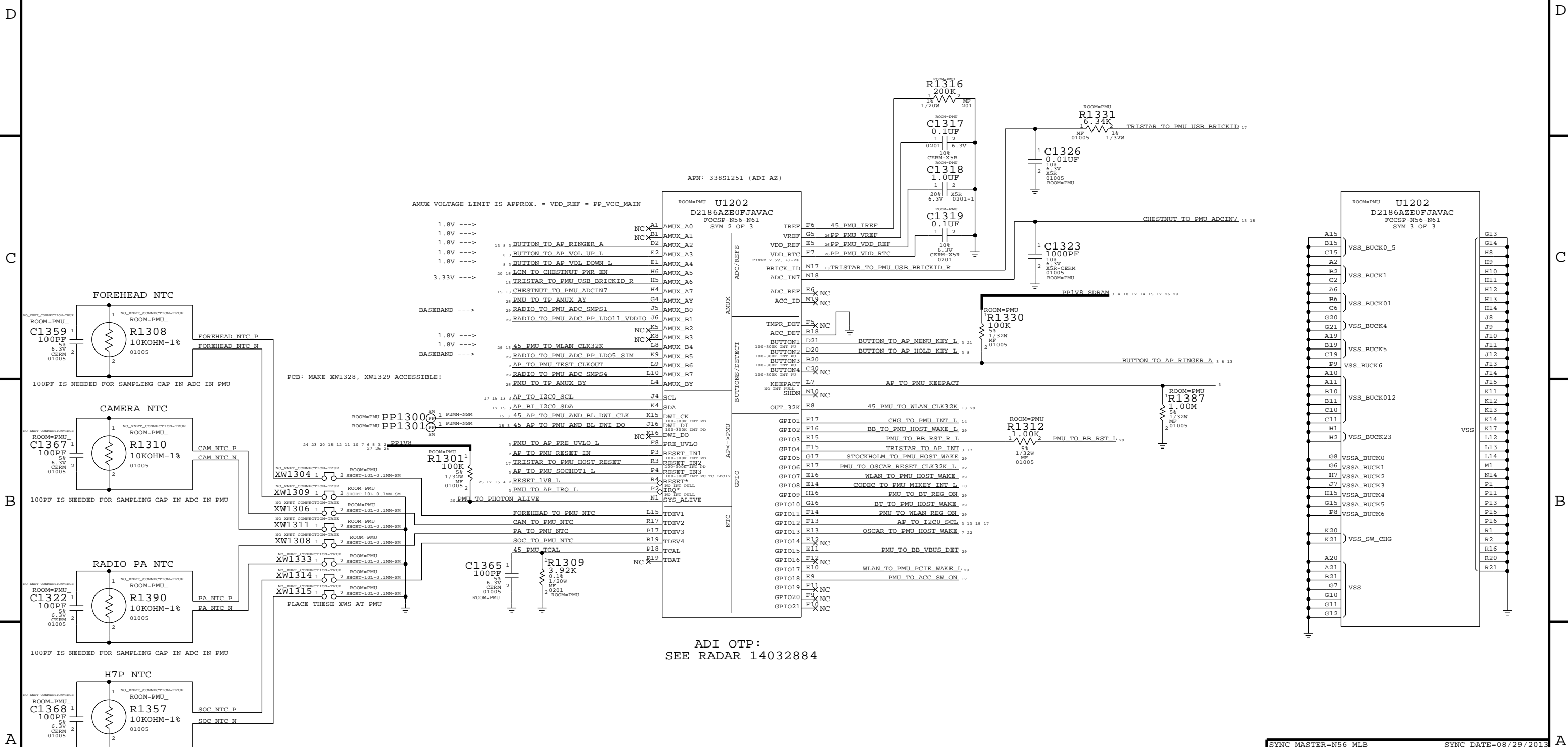


TO DO: REVIEW ALL LDO ASSIGNMENTS
(CHECK VDD LDO INPUT SOURCE)
CHECK CURRENT RATING FOR LDO OUT
CHECK CURRENT RATING FOR LDO OUT
SPEAKER AMP, CODEC VA (2.5 MA L1419, 3MA L67)
TRISTAR VDH, WIFI_FLEX PAC (? MA)
GYRO, ACCEL, COMPASS (? MA) NOTE: 3V +/- 5% PER EUGENE
NAND (? MA)
ACCESSORY POWER (? MA)
PROX/ALS VDD (PROX: 0.75/1.2 MA ALS: 0.175/0.25 MA [TYP/MAX])
REAR CAM AUTO FOCUS (120MA PEAK, PROBABLY CAP AT 80MA)
REAR/FRONT CAM AVDD (? MA)
SPEAKER AMP, CODEC VA (2.5 MA L1419, 3MA L67)
TRISTAR VDH, WIFI_FLEX PAC (? MA)
GYRO, ACCEL, COMPASS (? MA) NOTE: 3V +/- 5% PER EUGENE
NAND (? MA)
ACCESSORY POWER (? MA)
PROX/ALS VDD (PROX: 0.75/1.2 MA ALS: 0.175/0.25 MA [TYP/MAX])
REAR CAM AUTO FOCUS (120MA PEAK, PROBABLY CAP AT 80MA)
REAR/FRONT CAM AVDD (? MA)
SPEAKER AMP, CODEC VA (2.5 MA L1419, 3MA L67)
TRISTAR VDH, WIFI_FLEX PAC (? MA)
GYRO, ACCEL, COMPASS (? MA) NOTE: 3V +/- 5% PER EUGENE
NAND (? MA)
ACCESSORY POWER (? MA)
PROX/ALS VDD (PROX: 0.75/1.2 MA ALS: 0.175/0.25 MA [TYP/MAX])

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
POWER:ADI (1 / 2)			
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ADI PMU

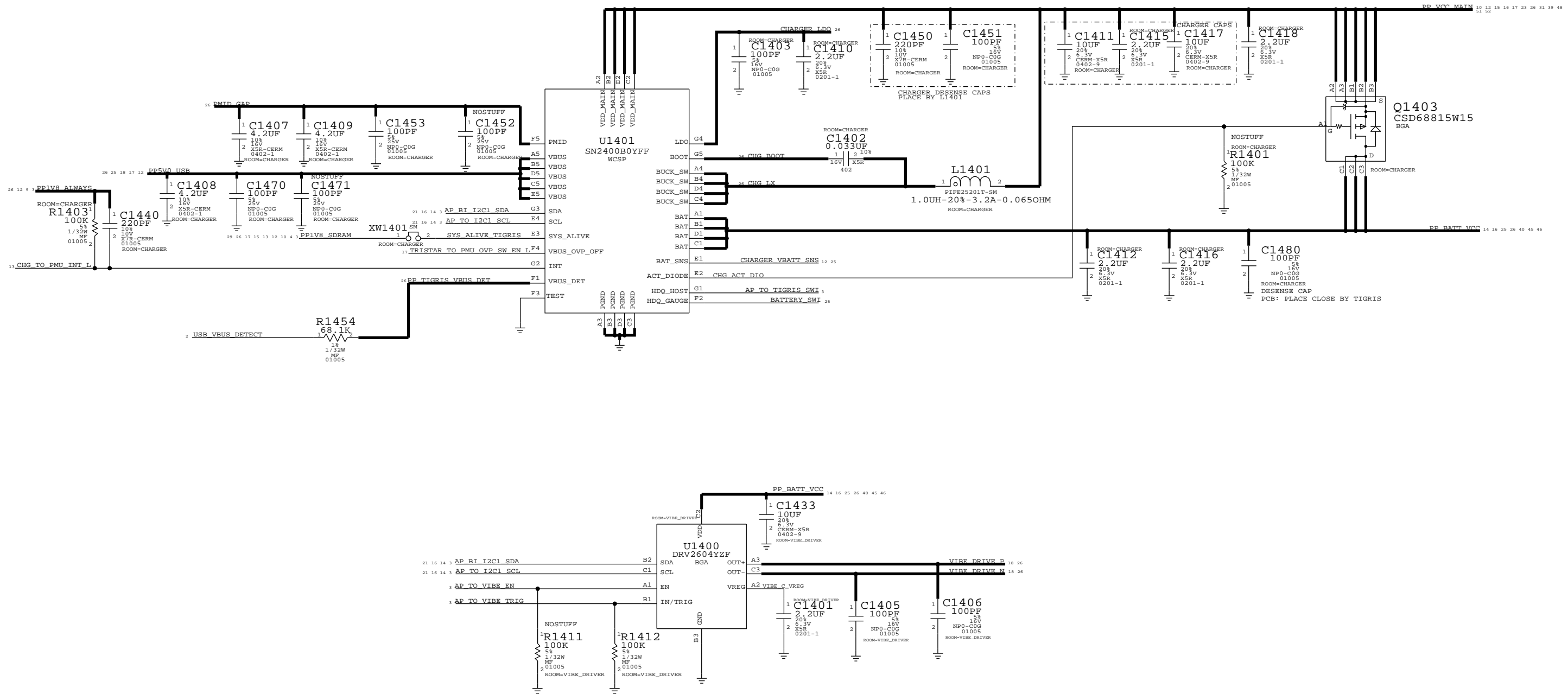
(AMUX, GPIO, BUTTONS, ADC, THERMISTORS, SYSTEM I/F, GND)



ADI OTP:
SEE RADAR 14032884

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
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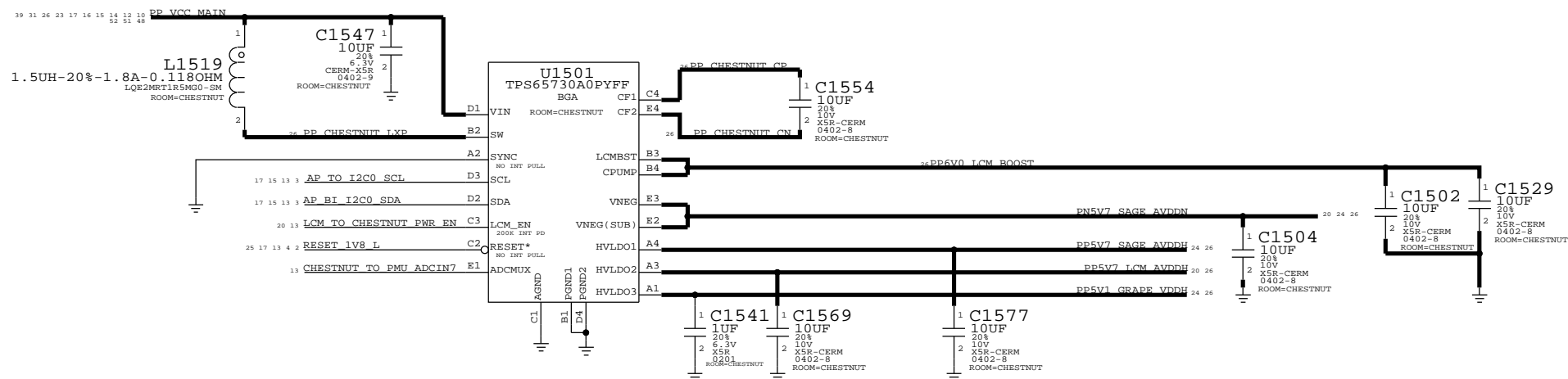
TIGRIS CHARGER & VIBE DRIVER



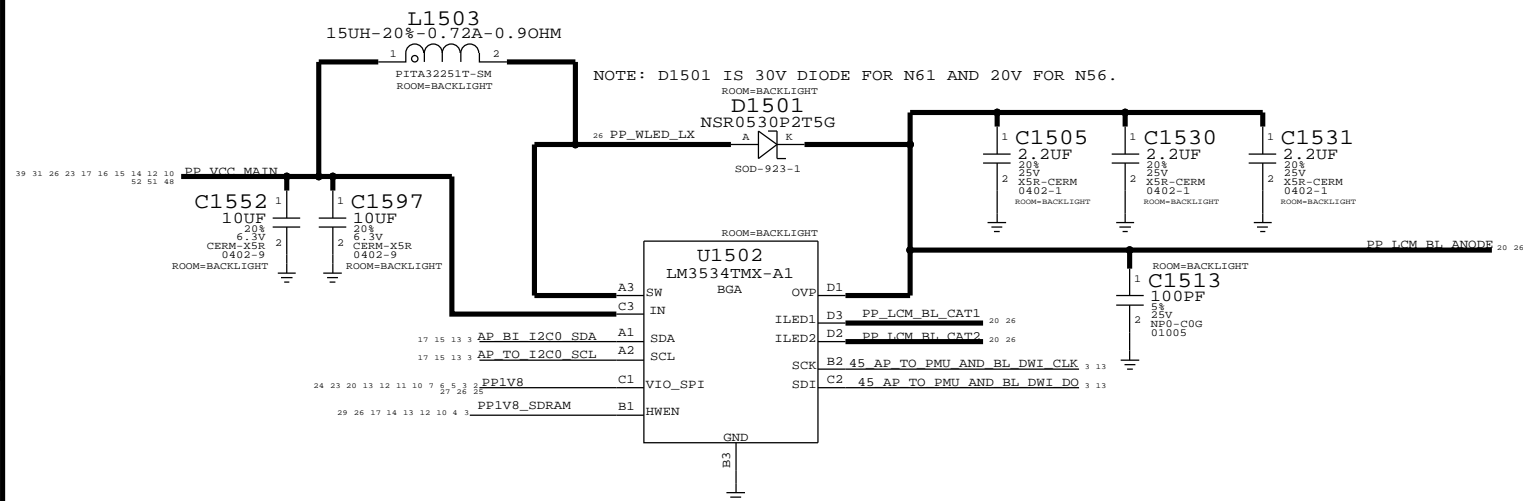
PAGE TITLE POWER:TIGRISR,VIBE DRIVER		
Apple Inc.	DRAWING NUMBER 051-9903	SIZE D
	REVISION 7.0.0	
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PAGE 14 OF 55		SHEET 14 OF 54

CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

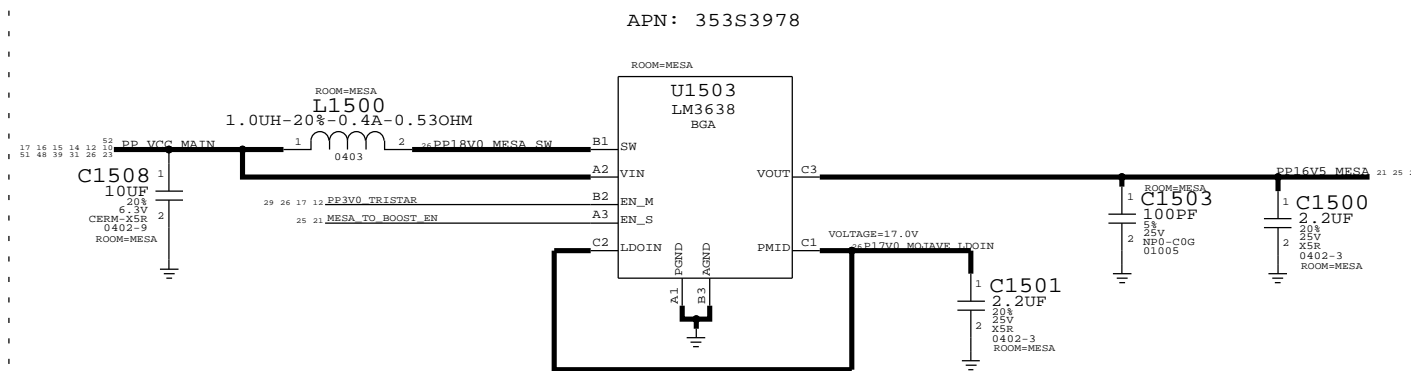
D500 DISPLAY PMU (TI CHESTNUT, 338S1149)



D500 BACKLIGHT DRIVER



MESA BOOST A0

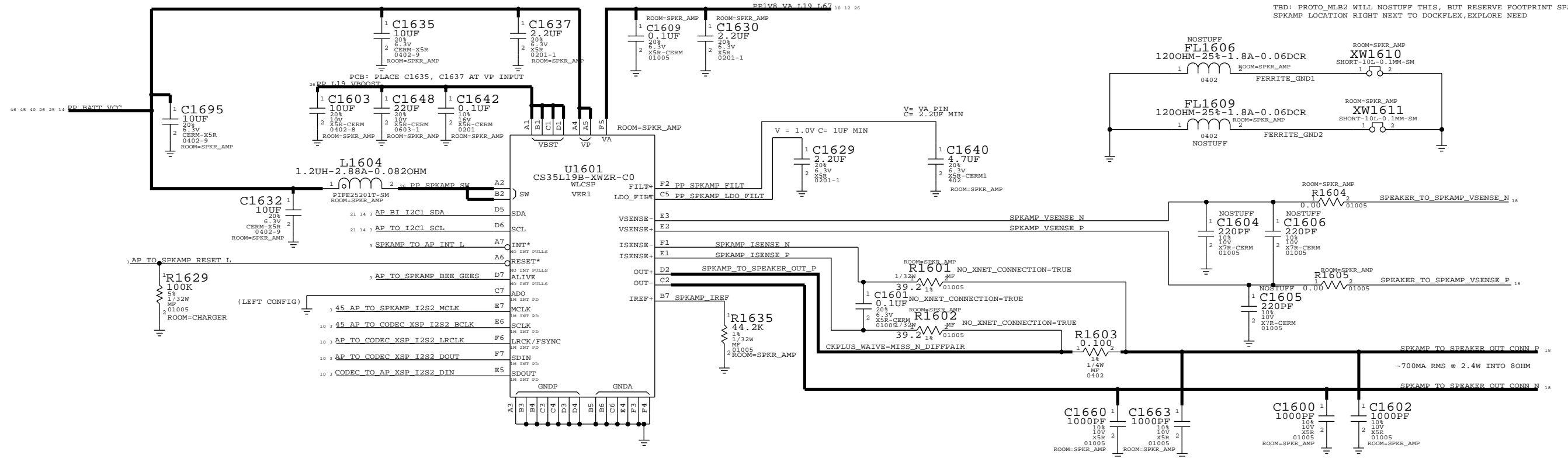


SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE DISPLAY: CHESTNUT, BACKLIGHT DRIVER			
DRAWING NUMBER 051-9903		SIZE D	
REVISION 7.0.0		BRANCH	
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SPEAKER AMP, LED DRIVER

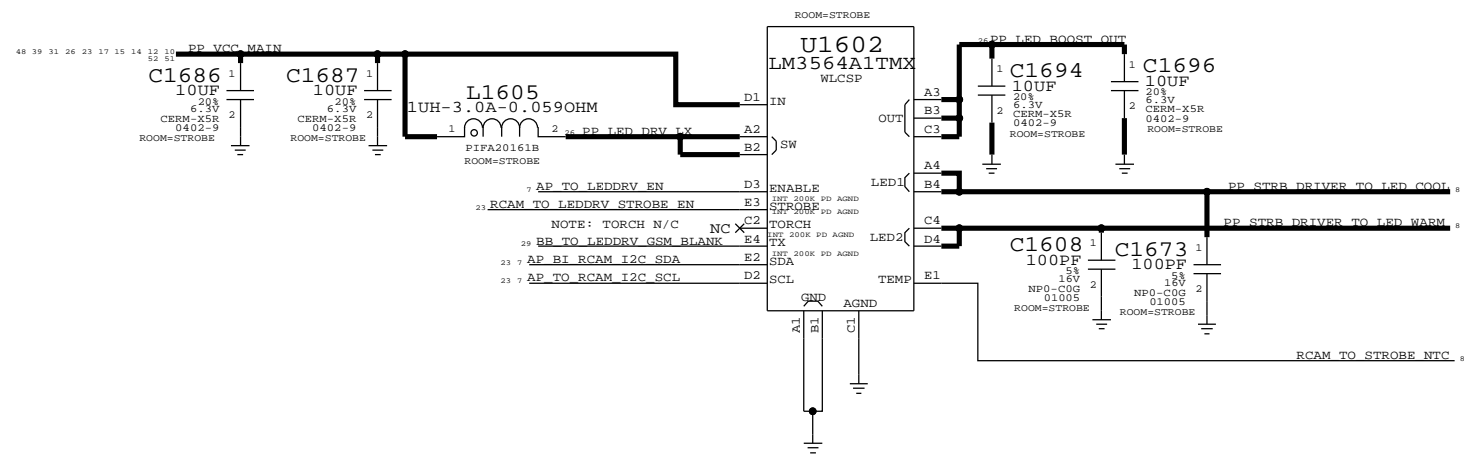
SPEAKER AMP

I2C ADDRESS: 1000000X



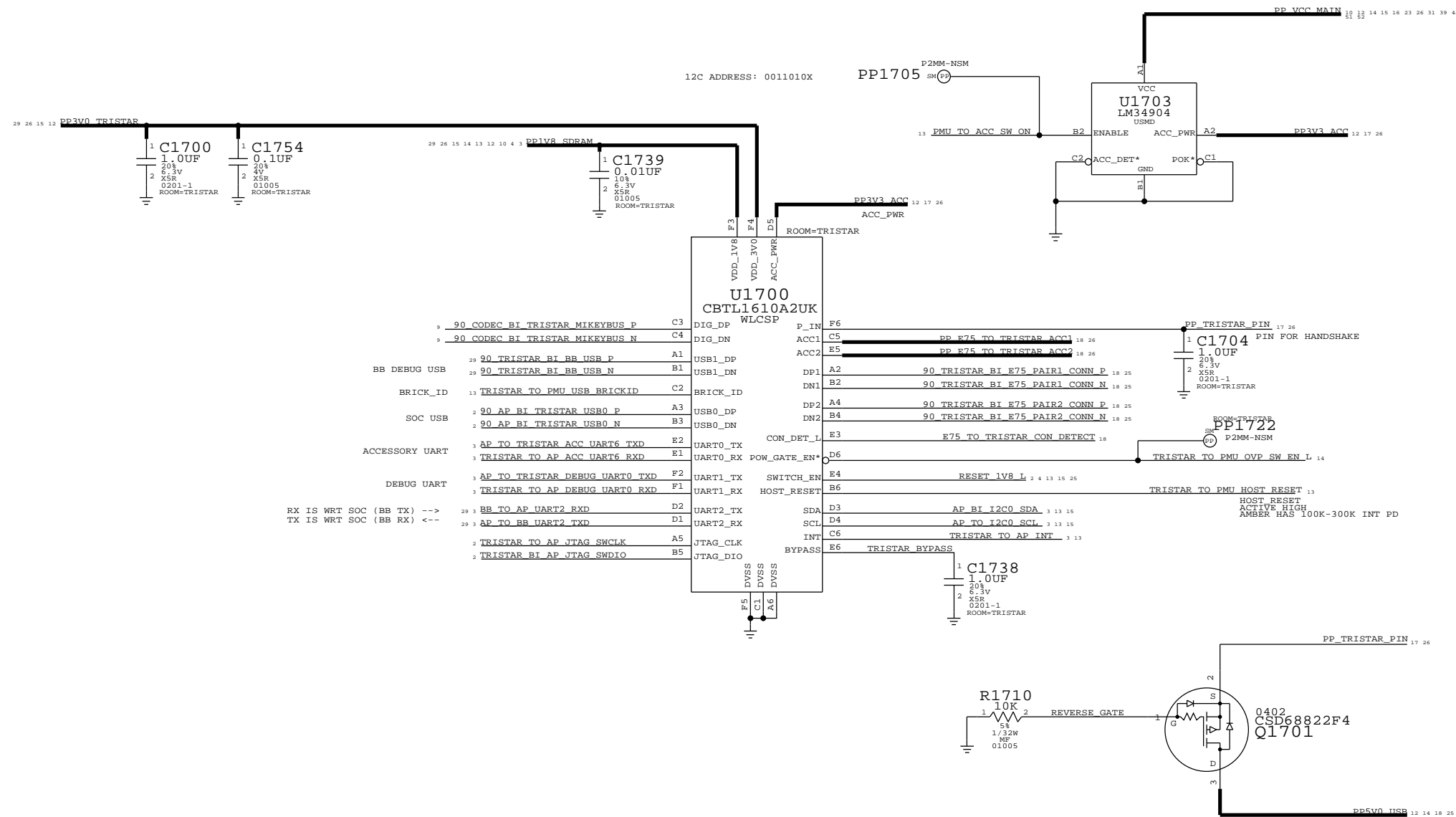
STROBE DRIVER

TI: APN 353S3899



SYNC MASTER=N61_MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
AUDIO:SPKR AMP,STROBE			
DRAWING NUMBER		051-9903	
REVISION		7.0.0	
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TRISTAR2



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
IO:TRISTAR2			
		DRAWING NUMBER	051-9903
		REVISION	7.0.0
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		PAGE	17 OF 55
		SHEET	17 OF 54
		SIZE	D

DOCKFLEX B2B (USB VBUS, SPEAKER, ANTENNA LAT SW CTRL, MIC1 (PRIMARY MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)

MLB: 516S1281 (RCPT)
 ROOM=DOCK_B2B J1817
 24-5859-036-201-829
 F-ST-SM

D
 LOWER MIC1
 (PRIMARY
 VOICE MIC)

C
 HEADPHONE

B

A
 CODEC TO
 HEADPHONE

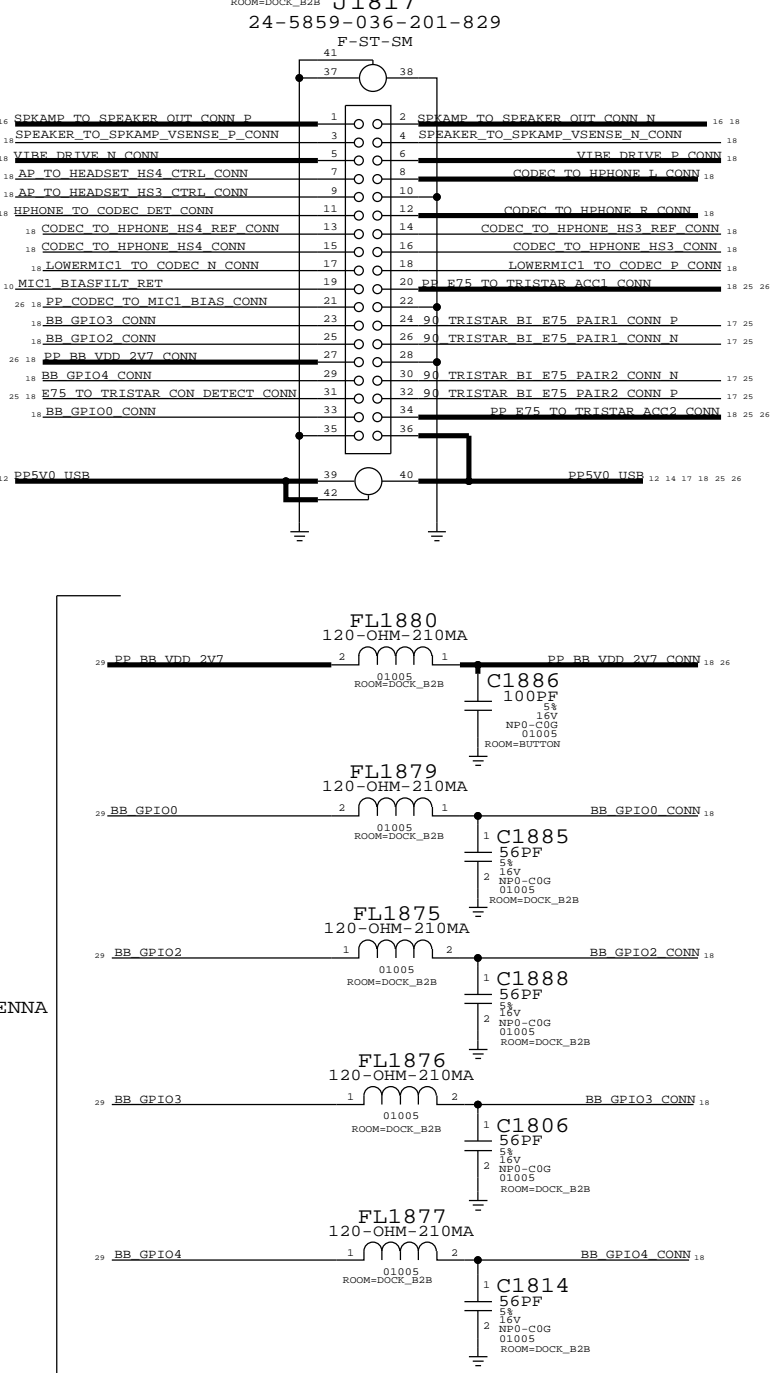
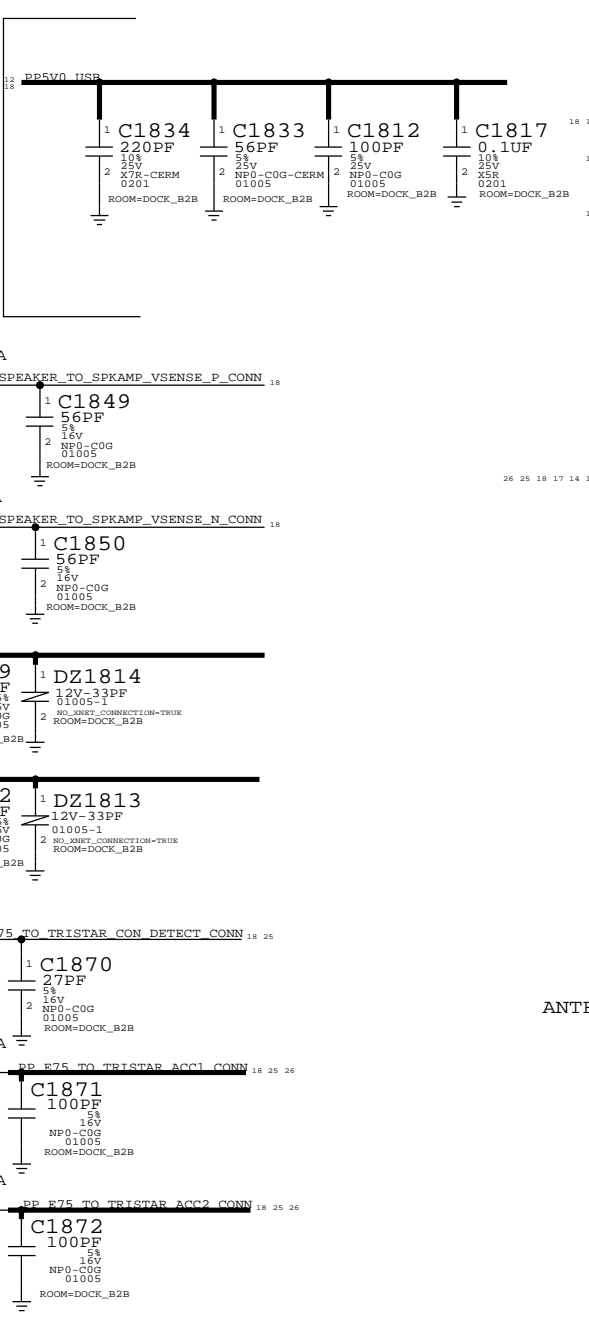
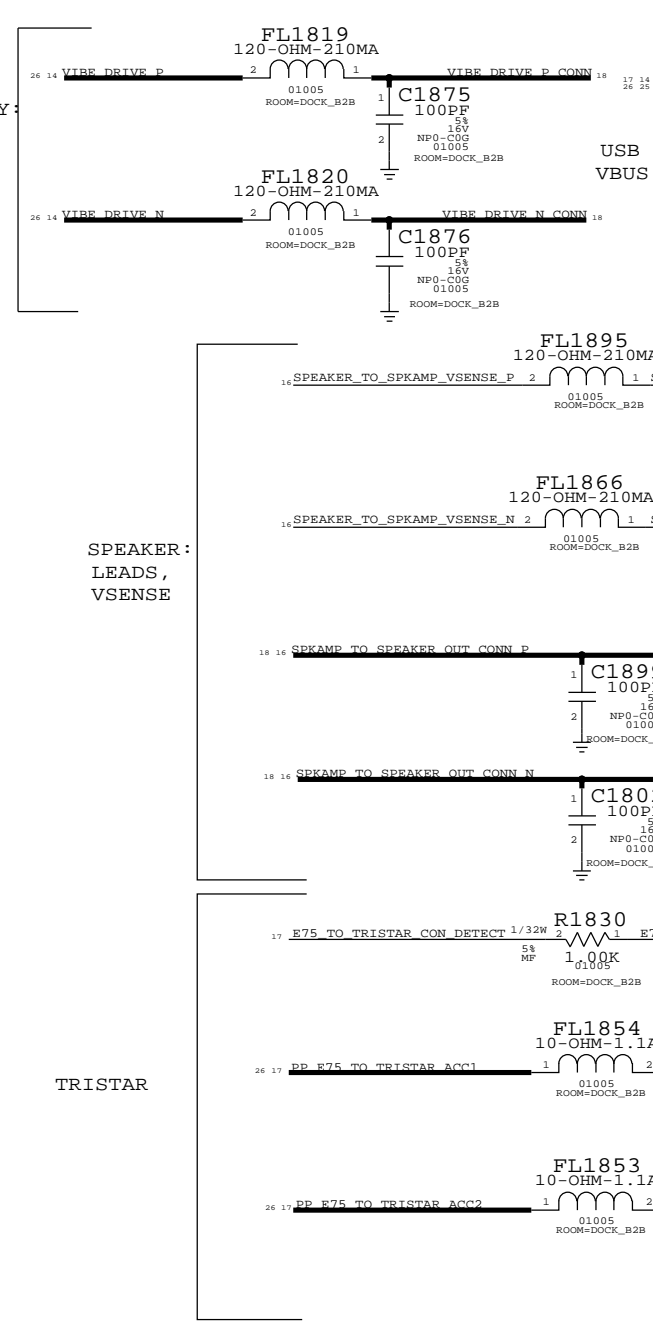
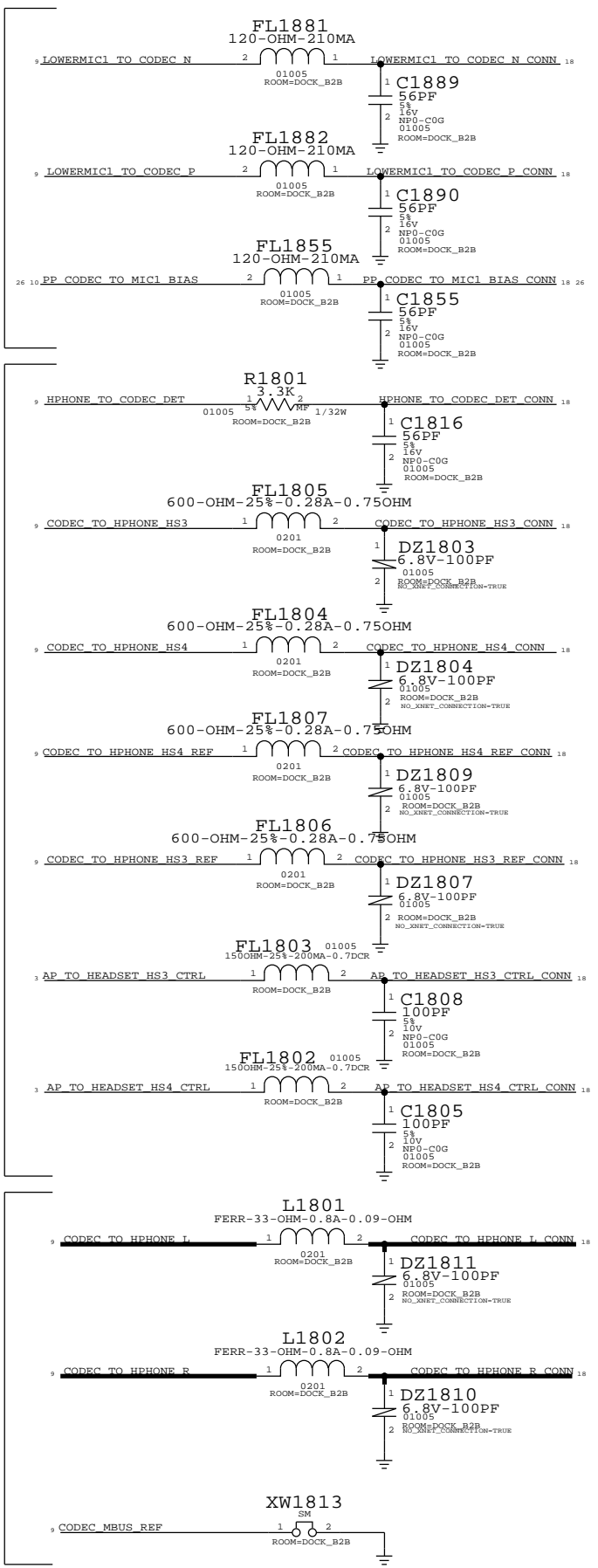
ACCESSORY:
 VIBE
 DRIVE

USB
 VBUS

SPEAKER:
 LEADS,
 VSENSE

TRISTAR

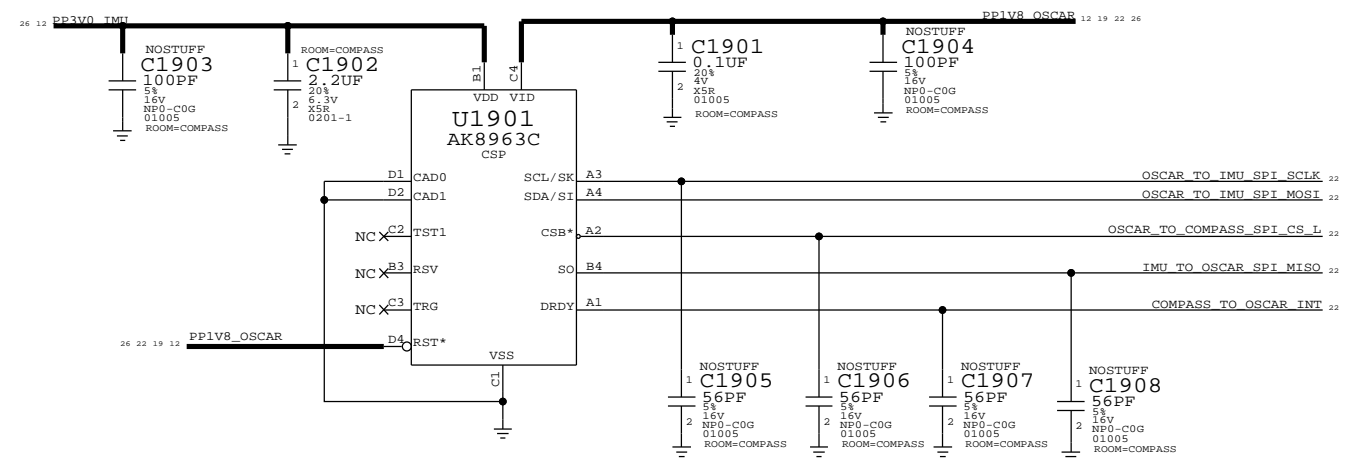
ANTENNA



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
IO:DOCK FLEX CONN			
Apple Inc.	DRAWING NUMBER	051-9903	SIZE
	REVISION	7.0.0	
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COMPASS - AKM COMPASS IN POR LOCATION

COMPASS CSP: 338S1014

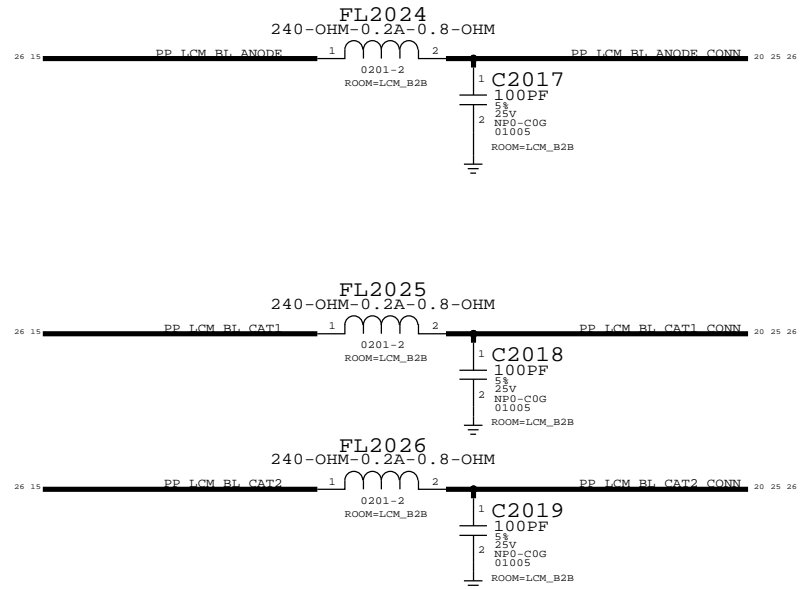


SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
SENSORS: COMPASS			
		DRAWING NUMBER	SIZE
		051-9903	D
		REVISION	
		7.0.0	
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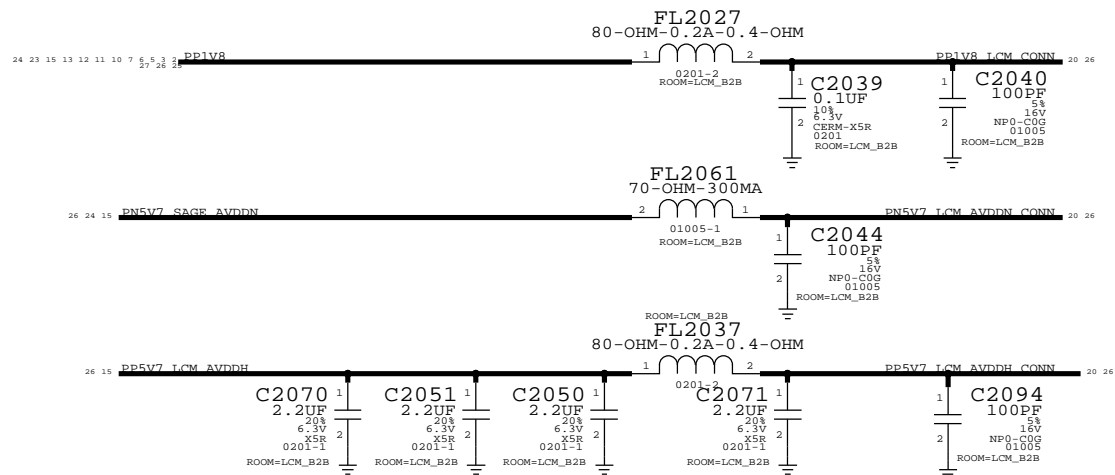
LCD B2B

Backlight

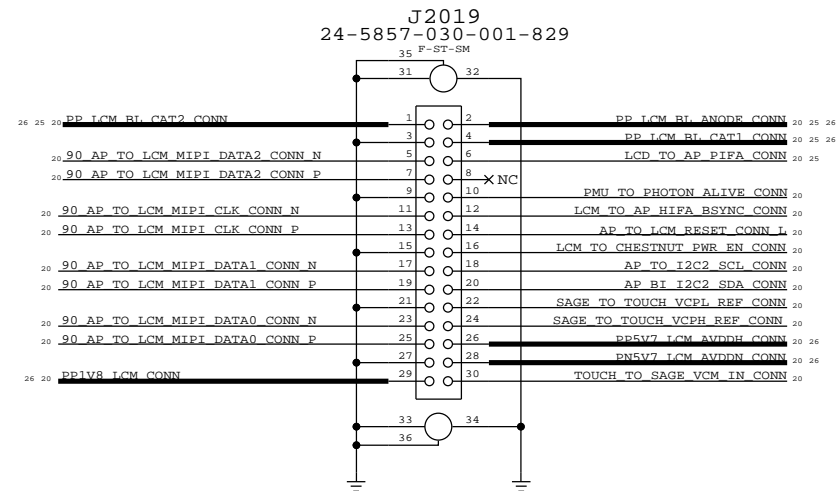
(N56 HAS A 2ND SET OF BL SIGNALS ON P. 19).



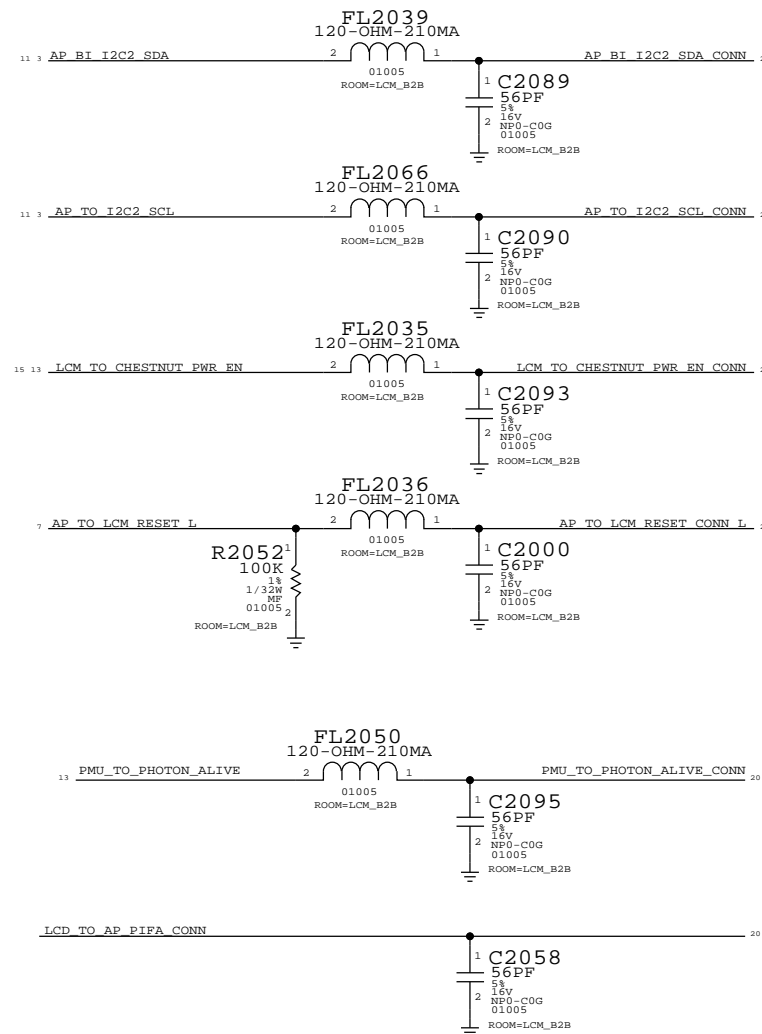
LCM Supplies



THIS ONE ON MLB ---> 516S1164

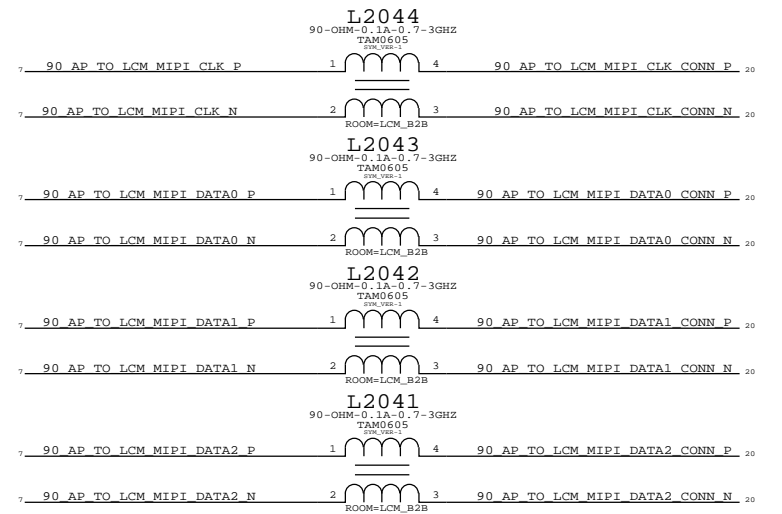


Digital Interfaces

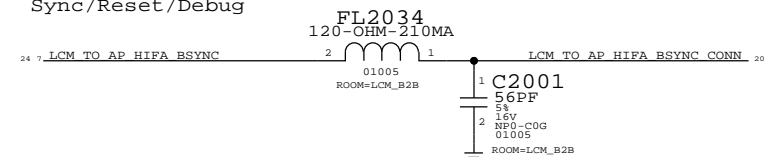


MIPI Common Mode Chokes

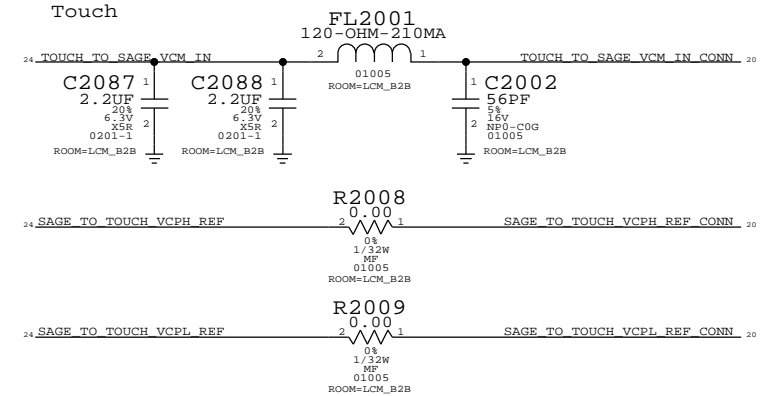
(N56 HAS A 4TH MIPI LANE ON P. 19).



Sync/Reset/Debug

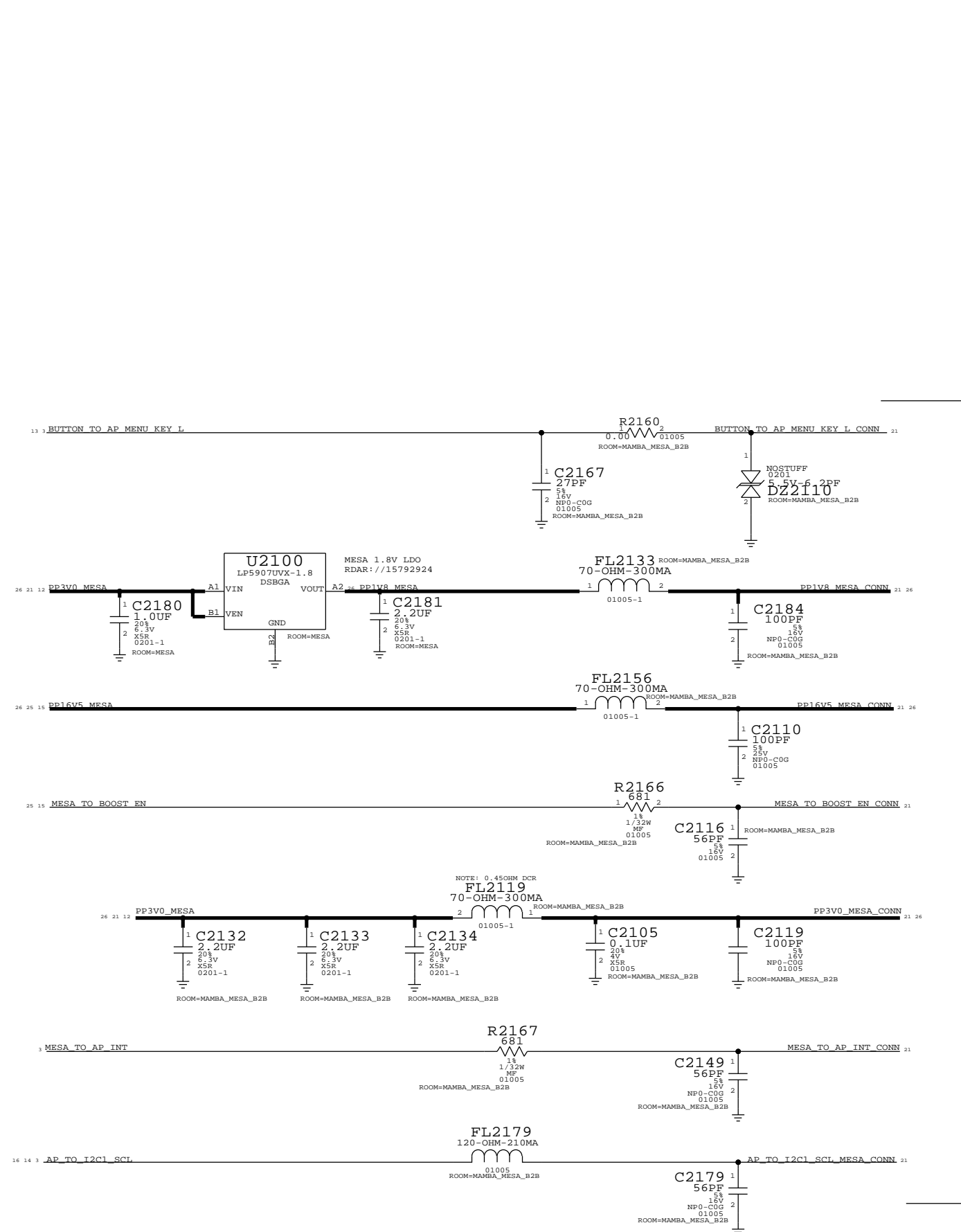


Touch

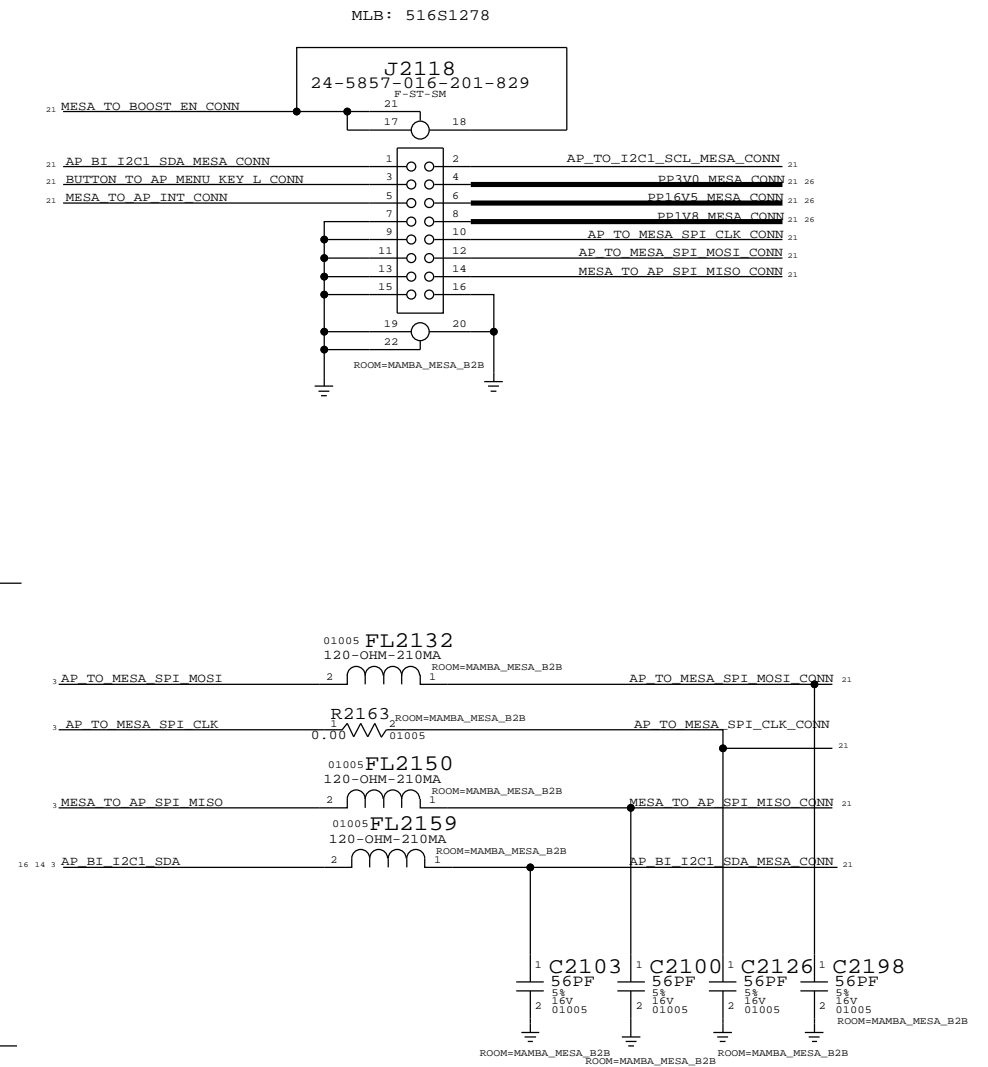


SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
DISPLAY: FLEX CONN			
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		REVISION	7.0.0
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		PAGE	20 OF 55
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		SIZE	D

MESA CONNECTOR



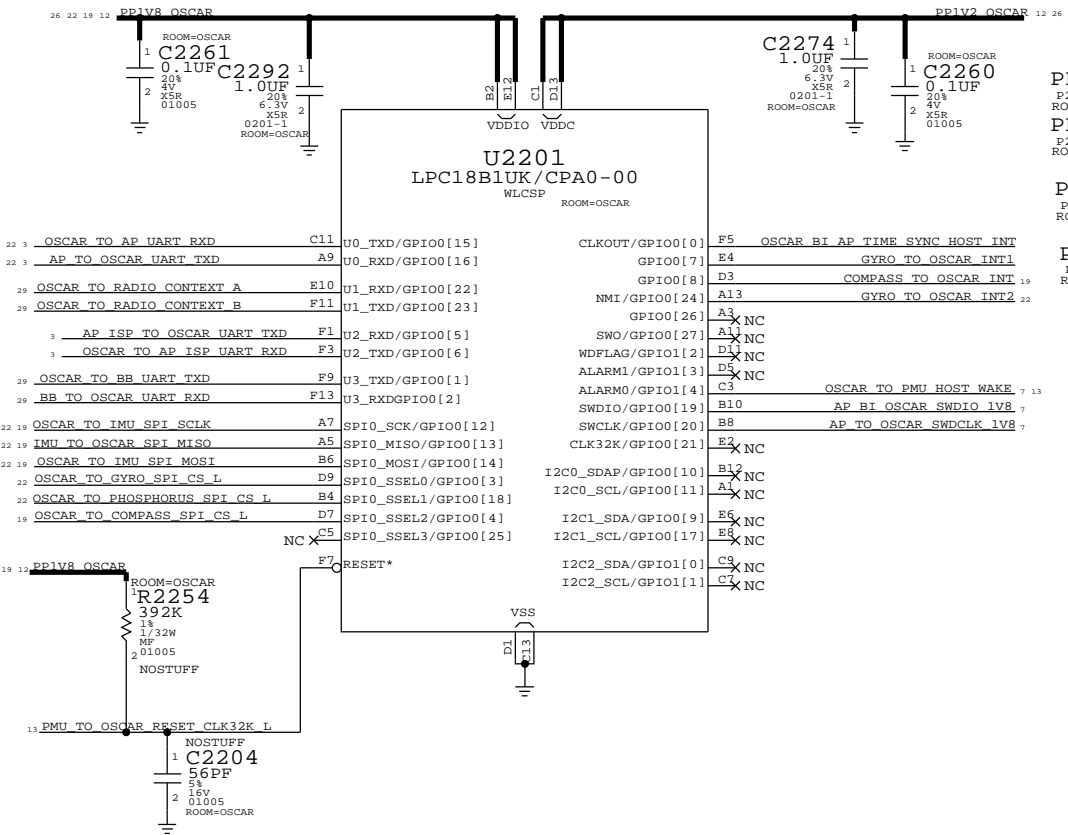
MESA SENSOR:



PAGE TITLE		
SENSORS:MESA FLEX CONN		
Apple Inc.		DRAWING NUMBER 051-9903
REVISION 7.0.0		SIZE D
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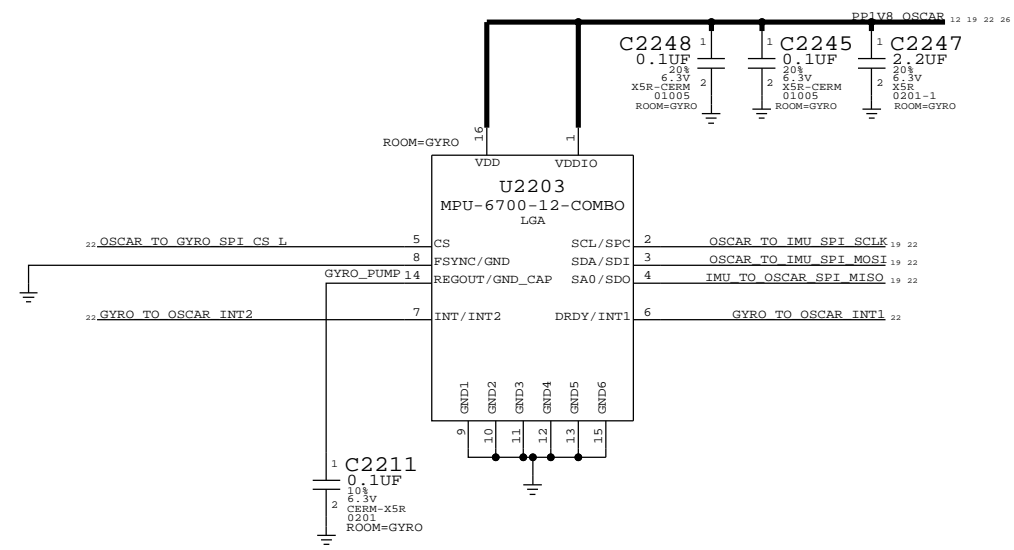
OSCAR + SENSORS

OSCAR VDDIO = 1.8V ALWAYS ON (NEED TO MAKE HOST & RUN PLL)
OSCAR CORE = 1.2V ALWAYS ON (NEED TO RUN IN SDRAM)

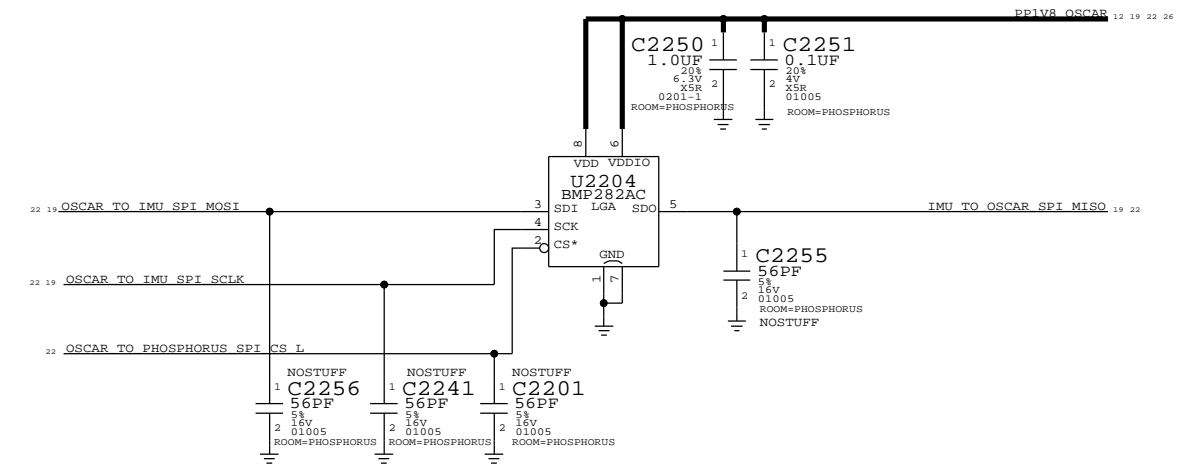


CARBON (ACCEL GYRO COMBO)

INVENSENSE, APN 338S00017, C2211=0.1uF
 BOSCH, APN 338S00028, C2211=0.1uF
 ST, APN 338S00029, C2211=0.01uF, 25V



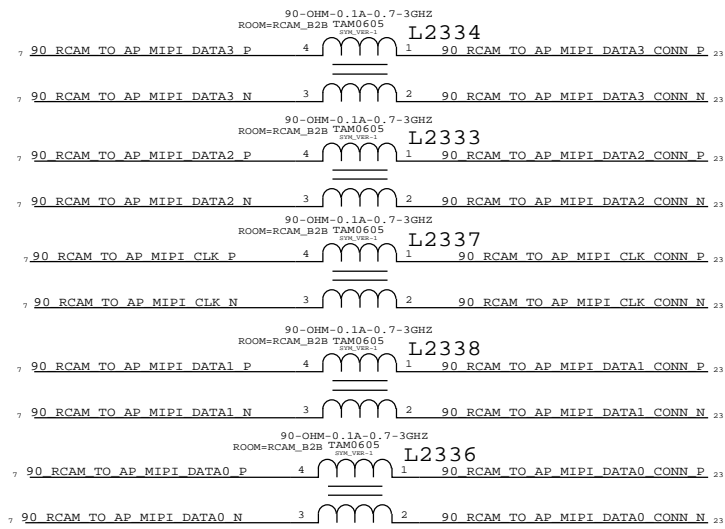
THIS IS OUTSIDE OF SHIELD IN
 TO THE RIGHT OF THE NAND
PHOSPHORUS



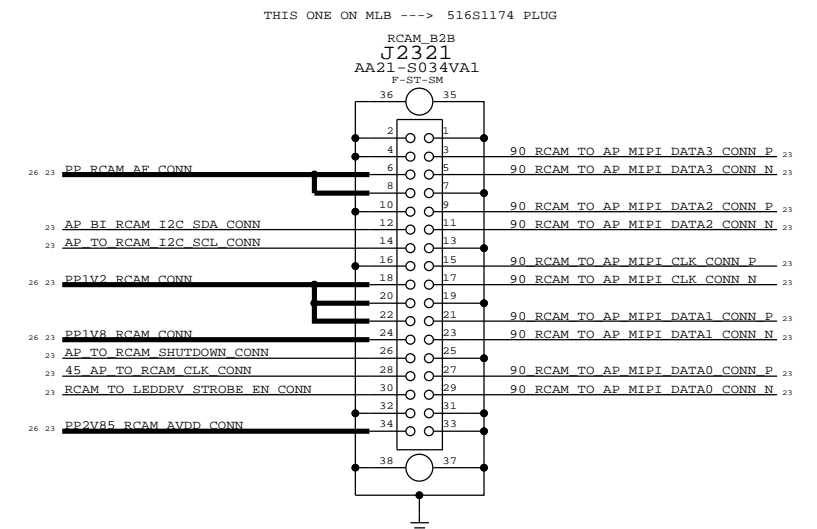
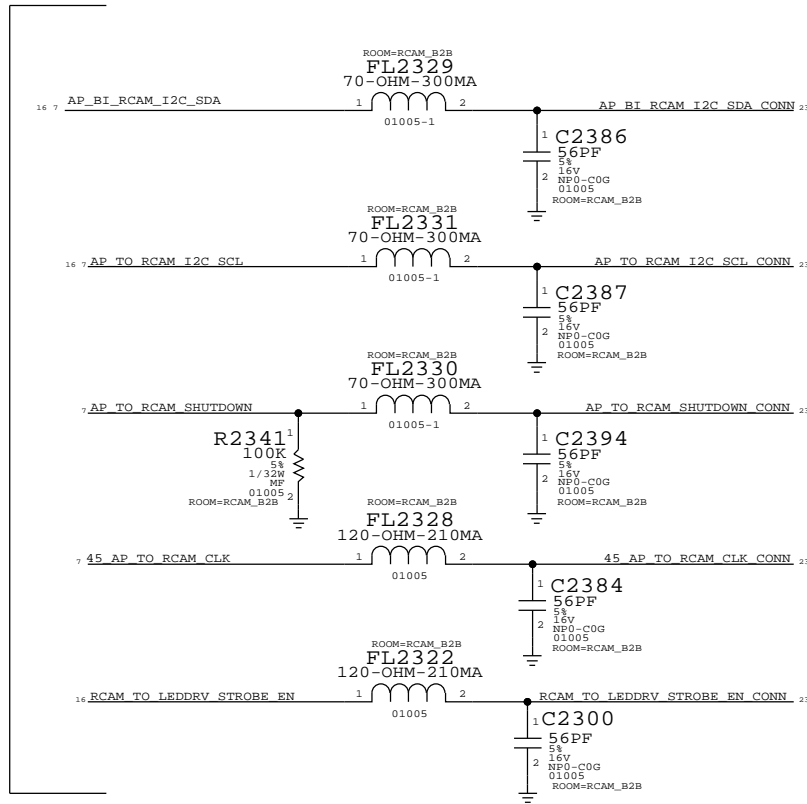
SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
SENSORS: OSCAR, CARBON, PHOS, MAGNESIUM			
DRAWING NUMBER		051-9903	
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RCAM B2B (REAR CAMERA CONNECTOR)

RCAM:
4-LANE MIPI

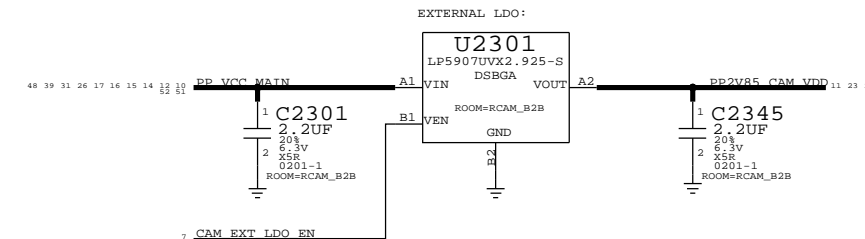
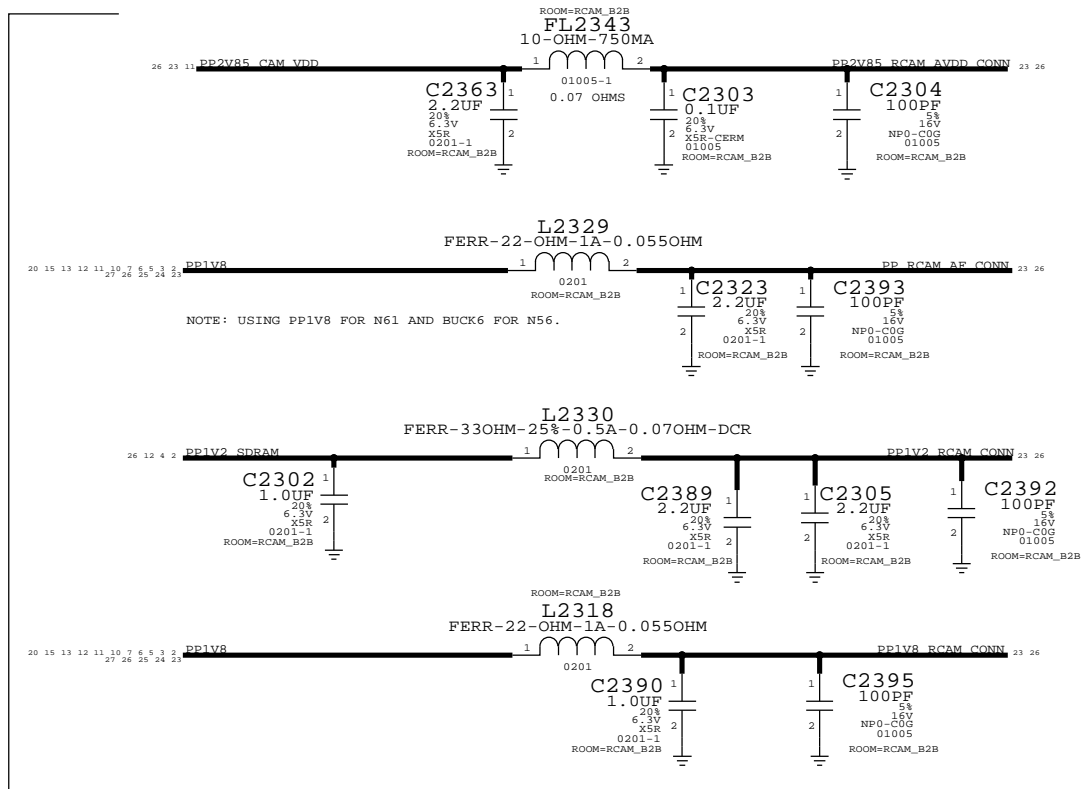


RCAM:
DIGITAL I/F
(I2C, CTRL, CLK)



RCAM/FCAM AVDD RAIL EXT. LDO:

RCAM:
POWER:
(1.8V DVDD)
(2.8V AVDD)
(1.2V VCC)
(1.8V/2V AF)



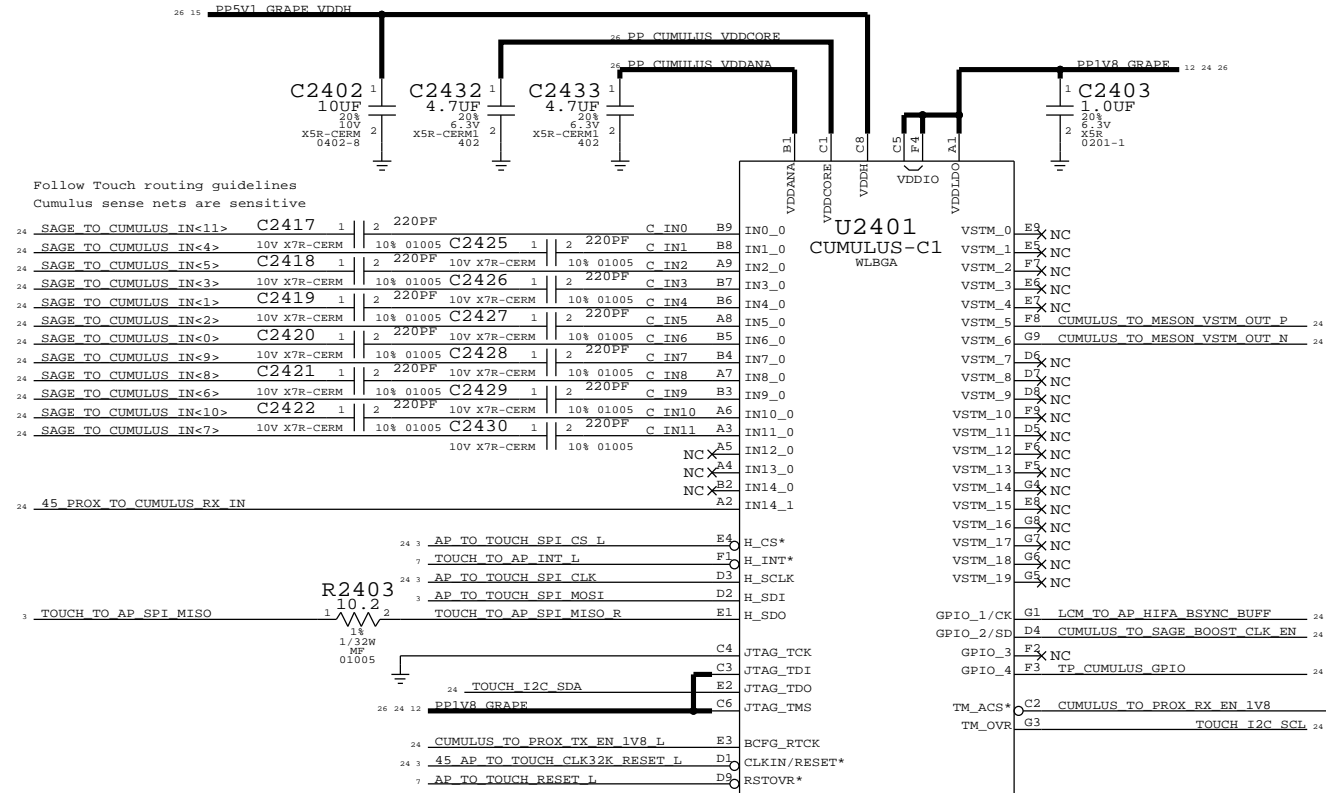
SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
CAMERA:REAR FLEX CONN			
Apple Inc.		DRAWING NUMBER	051-9903
		REVISION	7.0.0
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Touch (B2B, Driver ICs)

Cumulus

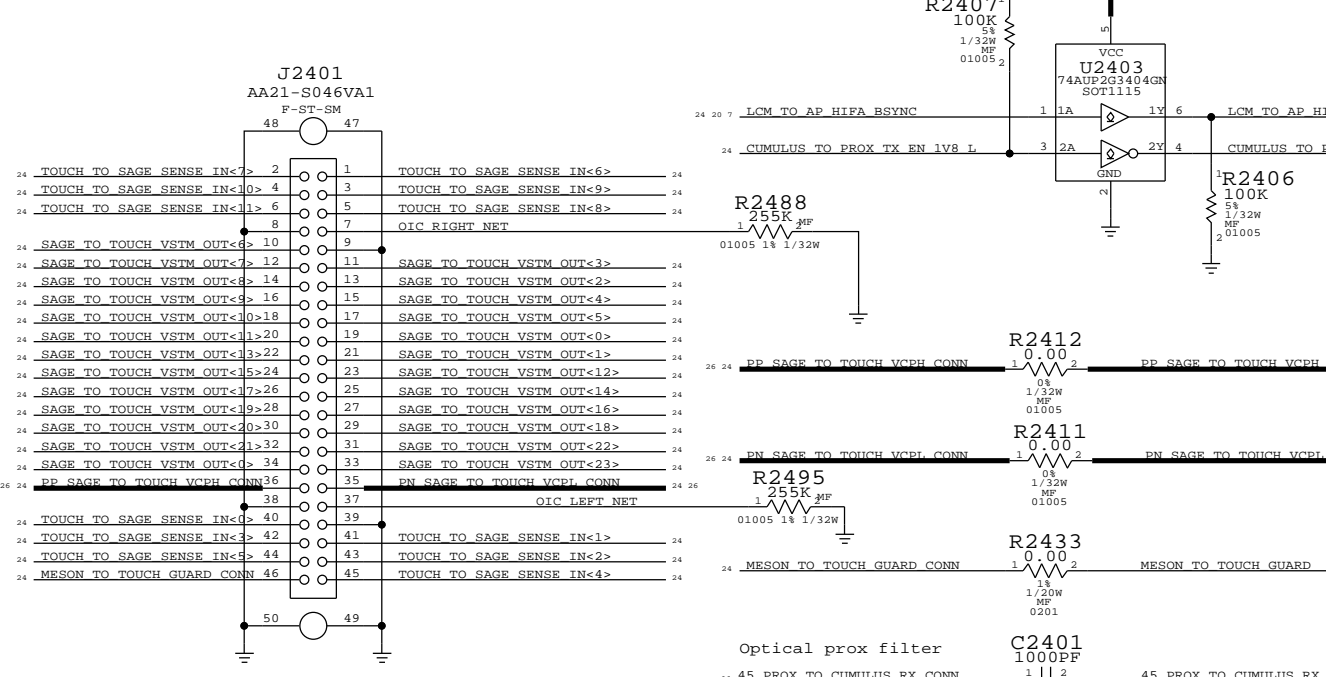
APN: 343S0638

Turn on is later than PPIV8_GRAPE
Turn off is same time as PPIV8_GRAPE

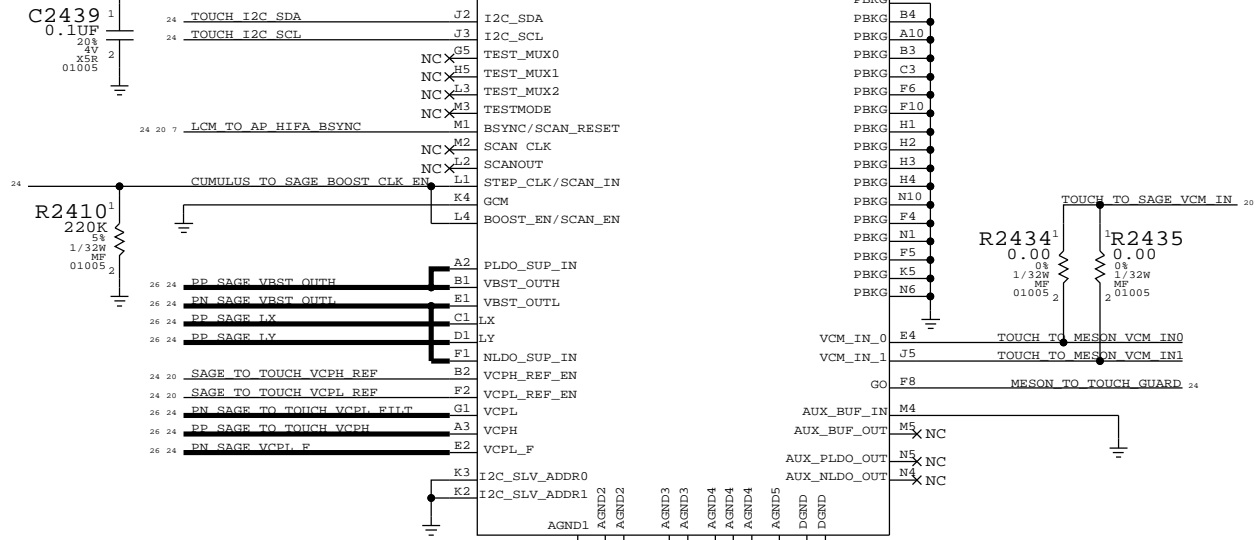
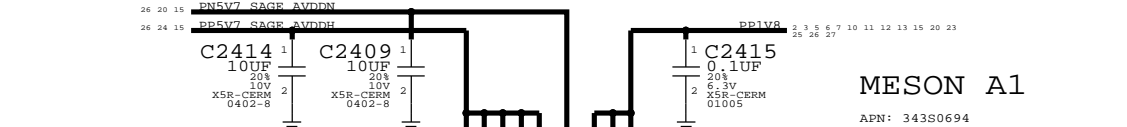
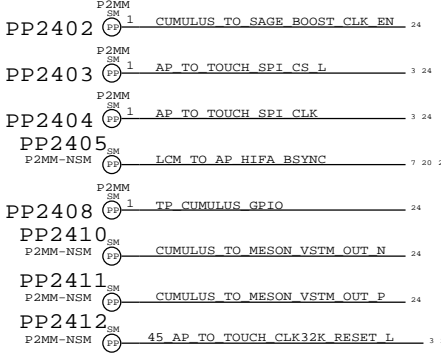


Touch B2B

MLB APN : 516S1086 (Receptacle)
Flex APN: 516S1087 (Plug)

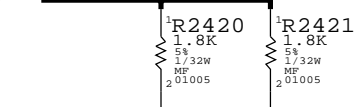


Touch probe points



Tantalums solved singing caps issue. Validate issue is resolved with Meson and replace with 0402 ceramics.

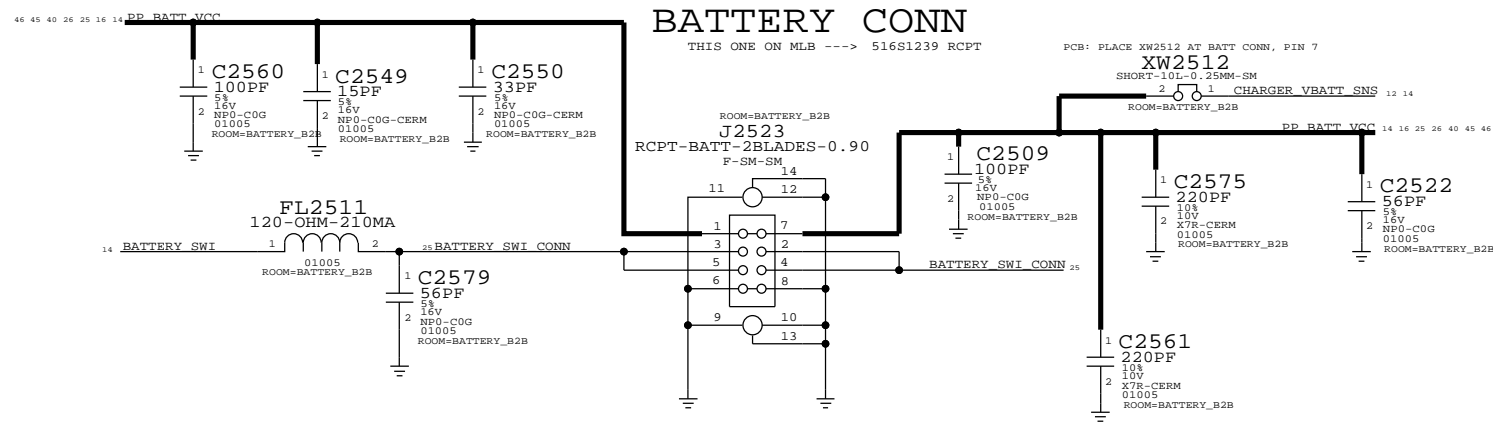
I2C pull-ups



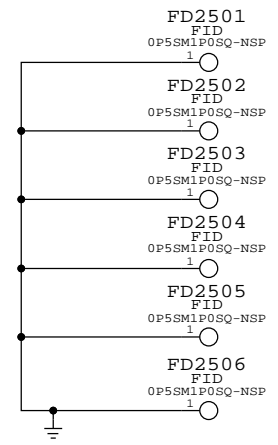
Meson VCPL rail: Effective impedance of 3 Ohms, at 115 kHz with 12 V bias.

TOUCH : CUMULUS , MESON	
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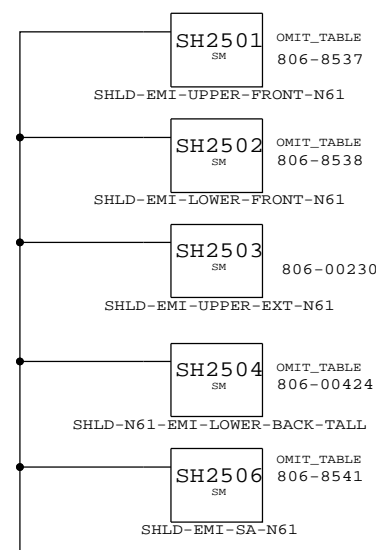
BATT CONN, TPS, STANDOFFS / SHIELDS / FIDUCIALS



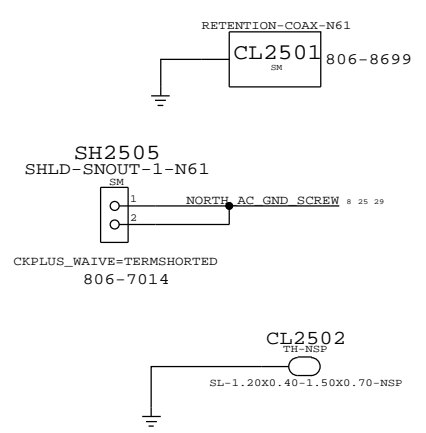
FIDUCIALS



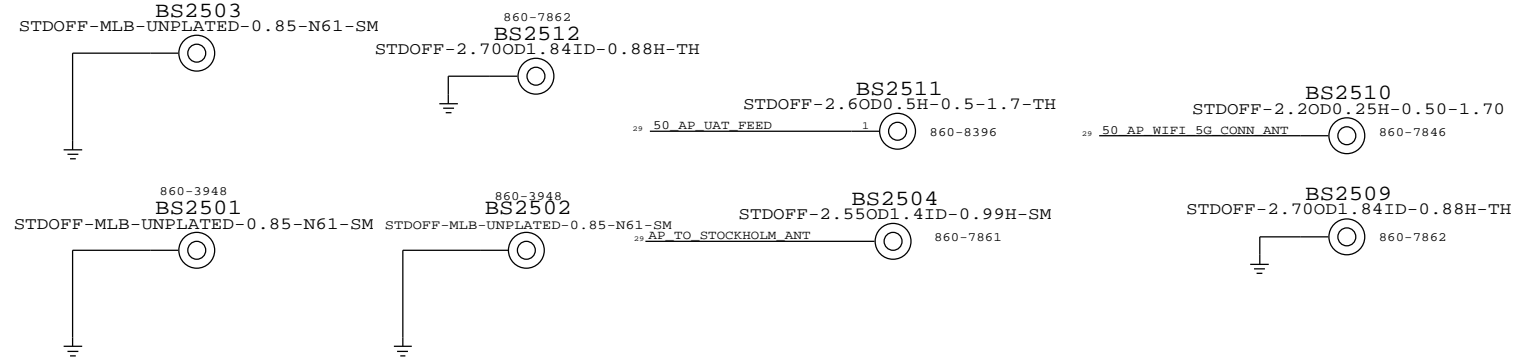
SHIELDS



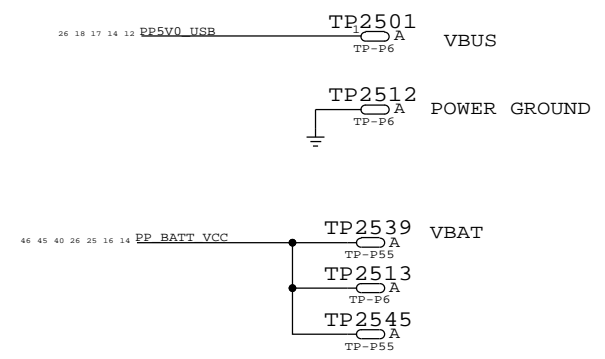
COWLING



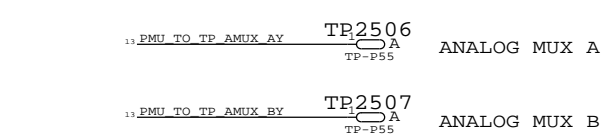
SCREW HOLES + STANDOFFS



POWER TP



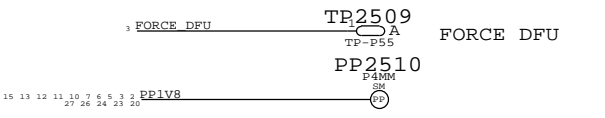
SUPER TP



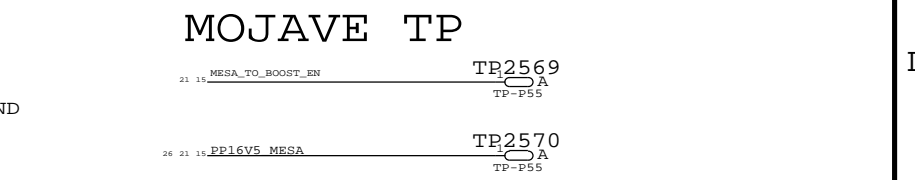
RESET



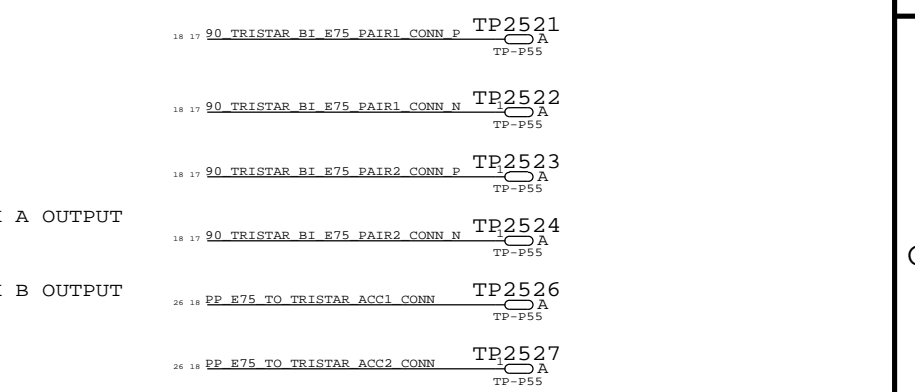
DFU



TESTPOINTS



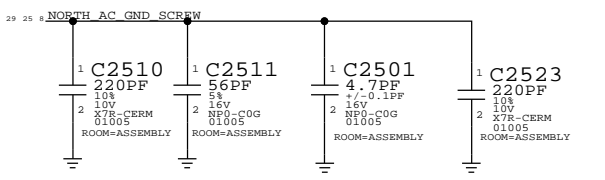
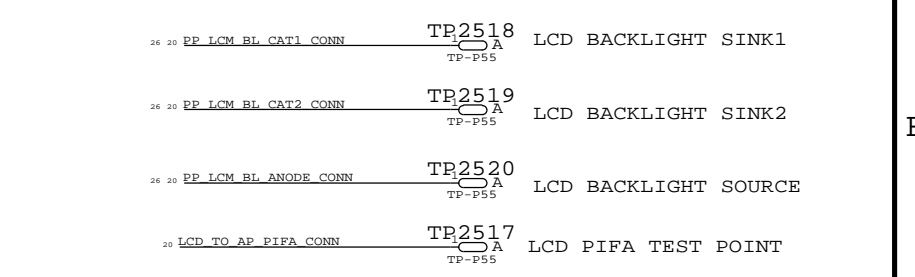
E75 - USB/UART/ID/POWER



TP IS TO HELP WITH USB SI IN THE FACTORY FIXTURE.

FOR DIAGS

LCM BACKLIGHT



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
POWER:BATT CONN,TPS,PD FEATURES			
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VOLTAGE PROPERTIES

E188	VOLTAGE=3.3V	PP3V3_USB	2 12
E189	VOLTAGE=1.8V	PP1V8_VA_I19_I67	10 12 16
E190	VOLTAGE=3.0V	PP3V0_TRISTAR	12 15 17 29
E191	VOLTAGE=3.0V	PP3V0_TMI	12 19
E192	VOLTAGE=3.0V	PP3V0_NAND	6 12
E193	VOLTAGE=3.0V	PP3V3_ACC	12 17
E194	VOLTAGE=3.0V	PP3V0_PROX_AIS	11 12
E195	VOLTAGE=4.6V	PP_VCC_MAIN	10 12 14 15 16 17 23 31 39
E196	VOLTAGE=1.0V	PP1V0	7 12
E197	VOLTAGE=3.0V	PP3V0_PROX_TRIPD	11 12
E198	VOLTAGE=1.8V	PP1V8_ALWAYS	3 5 12 14
E199	VOLTAGE=3.0V	PP3V0_MESA	12 21
E200	VOLTAGE=1.1V	PP_CPU	4 12
E201	VOLTAGE=1.1V	PP_GPU	4 12
E202	VOLTAGE=1.2V	PP1V2_SDRAM	2 4 12 23
E203	VOLTAGE=1.8V	PP1V8_SDRAM	3 4 10 12 13 14 15 17 29
E204	VOLTAGE=1.8V	PP1V8	2 3 4 5 7 10 11 12 13 15 20 23
E205	VOLTAGE=1.8V	PP1V8_GRAPE	12 24
E206	VOLTAGE=1.8V	PP1V8_OSCAR	12 19 22
E207	VOLTAGE=1.2V	PP1V2_NAND_VDDT	6
E208	VOLTAGE=1.8V	PP_EXTMIC_BIAS_FILT_IN	10
E209	VOLTAGE=1.8V	BOARD_ID2	3 27
E210	VOLTAGE=1.2V	PP1V2	3 4 5 11 12
E211	VOLTAGE=5.0V	PP_E75_TO_TRISTAR_ACC1_CONN	18 25
E212	VOLTAGE=5.0V	PP_E75_TO_TRISTAR_ACC1	17 18
E213	VOLTAGE=22.0V	PP_LCM_BL_ANODE	15 20
E214	VOLTAGE=0.2V	PP_LCM_BL_CAT2	15 20
E215	VOLTAGE=0.2V	PP_LCM_BL_CAT1	15 20
E216	VOLTAGE=0.2V	PP_LCM_BL_CAT2_CONN	20 25
E217	VOLTAGE=0.2V	PP_LCM_BL_CAT1_CONN	20 25
E218	VOLTAGE=-5.7V	PP5V7_SAGE_AVDDN	15 20 24
E219	VOLTAGE=1.2V	PP1V2_OSCAR	12 22
E220	VOLTAGE=3.0V	PP3V0_MESA_CONN	21
E221	VOLTAGE=6V	PP6V0_LCM_BOOST	15
E222	VOLTAGE=5.0V	PP_STRB_DRIVER_TO_LED_WARM	8 16
E223	VOLTAGE=5.0V	PP_STRB_DRIVER_TO_LED_COOL	8 16
E224	VOLTAGE=1.8V	PP_CODEC_TO_MIC1_BIAS	10 18
E225	VOLTAGE=1.8V	PP_EXTMIC_BIAS_FILT_IN	10
E226	VOLTAGE=1.8V	PP_EXTMIC_BIAS_FILT	10
E227	VOLTAGE=1.8V	PP_CODEC_TO_FRONTMIC2_BIAS	10 11
E228	VOLTAGE=1.8V	PP_CODEC_TO_REARMIC2_BIAS	8 10
E229	VOLTAGE=1.8V	PP_CODEC_FILT+	10
E230	VOLTAGE=2.2V	PP_CODEC_SPKR_VO	10
E231	VOLTAGE=2.5V	PP_CODEC_VCPBLT-	10
E232	VOLTAGE=2.5V	PP_CODEC_VCPBLT+	10
E233	VOLTAGE=2.5V	PP_CODEC_VHP_FLN	10
E234	VOLTAGE=0.2V	PP_CODEC_VHP_FLN2	10
E235	VOLTAGE=2.5V	PP_CODEC_VHP_FLN3	10
E236	VOLTAGE=1.8V	PP1V8_ECAM_CONN	11
E237	VOLTAGE=3.0V	PP2V85_ECAM_AVDD_CONN	11
E238	VOLTAGE=1.8V	PP_CODEC_TO_FRONTMIC1_BIAS_CONN	11
E239	VOLTAGE=3.0V	PP3V0_ALS_CONN	11
E240	VOLTAGE=1.2V	PP1V2_ECAM_VDDIO_CONN	11
E241	VOLTAGE=5.0V	PP5V0_USB	12 14 17 18 25
E242	VOLTAGE=5.0V	PP5V0_USB_TO_PMU	12
E243	VOLTAGE=4.6V	PP_BUCK5_LX0	12
E244	VOLTAGE=4.6V	PP_BUCK3_LX	12
E245	VOLTAGE=4.6V	PP_BUCK4_LX	12
E246	VOLTAGE=4.6V	PP_BUCK2_LX	12
E247	VOLTAGE=4.6V	PP_BUCK1_LX1	12
E248	VOLTAGE=4.6V	PP_BUCK1_LX0	12
E249	VOLTAGE=4.6V	PP_BUCK0_LX3	12
E250	VOLTAGE=4.6V	PP_BUCK0_LX2	12
E251	VOLTAGE=4.6V	PP_BUCK0_LX1	12
E252	VOLTAGE=4.6V	PP_BUCK0_LX0	12
E253	VOLTAGE=6.0V	PP_CHESTNUT_LXP	15
E254	VOLTAGE=6.0V	PP_CHESTNUT_CP	15
E255	VOLTAGE=6.0V	PP_CHESTNUT_CN	15
E256	VOLTAGE=5.7V	PP5V7_SAGE_AVDDH	15 24
E257	VOLTAGE=5.7V	PP5V7_LCM_AVDDH	15 20
E258	VOLTAGE=5.1V	PP5V1_GRAPE_VDDH	15 24
E259	VOLTAGE=22.0V	PP_NLED_LX	15
E260	VOLTAGE=18.0V	PP18V0_MESA_SW	15
E261	VOLTAGE=17.0V	PP17V0_MESA_SW	15
E262	VOLTAGE=16.5V	PP16V5_MESA	15 21 25
E263	VOLTAGE=8.0V	PP_SPKAMP_SW	16
E264	VOLTAGE=8.0V	PP_L19_VBOOST	16
E265	VOLTAGE=1.8V	PP_SPKAMP_FILT	16
E266	VOLTAGE=1.8V	PP_SPKAMP_LDO_FILT	16
E267	VOLTAGE=5.0V	PP_LED_DRV_LX	16
E268	VOLTAGE=5.0V	PP_LED_BOOST_OUT	16
E269	VOLTAGE=2.9V	PP2V9_LDO9	12
E270	VOLTAGE=1.8V	PP_CODEC_TO_MIC1_BIAS_CONN	18
E271	VOLTAGE=4.6V	PP_E75_TO_TRISTAR_ACC2	17 18
E272	VOLTAGE=4.6V	PP_E75_TO_TRISTAR_ACC2_CONN	18 25
E273	VOLTAGE=1.8V	PP1V8_LCM_CONN	20
E274	VOLTAGE=22.0V	PP_LCM_BL_ANODE_CONN	20 25
E275	VOLTAGE=-5.7V	PP5V7_LCM_AVDDN_CONN	20
E276	VOLTAGE=5.7V	PP5V7_LCM_AVDDH_CONN	20
E277	VOLTAGE=1.8V	PP1V8_MESA	21
E278	VOLTAGE=16.5V	PP16V5_MESA_CONN	21
E279	VOLTAGE=5.0V	PP_TRISTAR_PIN	17
E280	VOLTAGE=1.2V	PP1V2_ECAM_CONN	23
E281	VOLTAGE=1.8V	PP1V8_ECAM_CONN	23
E282	VOLTAGE=3.0V	PP2V85_CAM_VDD	11 23
E283	VOLTAGE=1.8V	PP2V85_ECAM_AVDD_CONN	23
E284	VOLTAGE=1.8V	PP_CUMULUS_VDDCORE	24
E285	VOLTAGE=1.2V	PP_CUMULUS_VDDANA	24
E286	VOLTAGE=13.5V	PP_SAGE_TO_TOUCH_VCPH_CONN	24
E287	VOLTAGE=-12V	PP_SAGE_TO_TOUCH_VCPH_CONN	24
E288	VOLTAGE=13.5V	PP_SAGE_TO_TOUCH_VCPH	24
E289	VOLTAGE=-12V	PP_SAGE_TO_TOUCH_VCPH	24
E290	VOLTAGE=-12V	PP_SAGE_VCPH_F	24
E291	VOLTAGE=5.7V	PP_SAGE_LX	24
E292	VOLTAGE=17.0V	PP_SAGE_LX	24
E293	VOLTAGE=1.8V	PP_PMU_VREF	13
E294	VOLTAGE=14V	PP_SAGE_VBST_OUTH	24
E295	VOLTAGE=5.0V	PP_TIGRIS_VBUS_DET	14
E296	VOLTAGE=2.5V	PP_PMU_VDD_RBE	13
E297	VOLTAGE=1.8V	PP_EXTMIC_BIAS	10
E298	VOLTAGE=1.8V	PP1V8_XTAL	2
E299	VOLTAGE=1.8V	PP_PMU_VDD_RTC	13
E300	VOLTAGE=4.6V	PP_BATT_VCC	14 16 25 40 45 46
E301	VOLTAGE=1.8V	PP1V8_MESA_CONN	21
E302	VOLTAGE=3.0V	PP3V0_PROX_CONN	11
E303	VOLTAGE=1.0V	PP0V95_FIXED_SOC	4 7 12
E304	VOLTAGE=1.0V	PP0V95_FIXED_SOC_PCIE	7
E305	VOLTAGE=1.2V	PP1V2_PLL	2
E306	VOLTAGE=1.0V	PP_BUCK5_LX1	12
E307	VOLTAGE=1.0V	PP_VIB_SOC	5 12
E308	VOLTAGE=5.0V	PPMID_CAP	14
E309	VOLTAGE=5.0V	CHARGER_LDO	14
E310	VOLTAGE=4.6V	CHG_BOOT	14
E311	VOLTAGE=4.6V	CHG_LX	14
E312	VOLTAGE=3.0V	VIBRE_DRIVE_P	14 18
E313	VOLTAGE=3.0V	VIBRE_DRIVE_N	14 18
E314	VOLTAGE=1.8V	PP_ECAM_AE_CONN	23
E315	VOLTAGE=-14.0V	PP_SAGE_VBST_OUTH	24
E316	VOLTAGE=-12.0V	PP_SAGE_TO_TOUCH_VCPH_FILT	24
E317	VOLTAGE=2.7V	PP_BB_VDD_2V7_CONN	18

PAGE TITLE		DRAWING NUMBER	SIZE
SYSTEM:VOLTAGE PROPERTIES		051-9903	D
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N61 SPECIFIC

BOOTSTRAPPING (BOARD_REV, BOARD_ID, BOOT_CFG)

```
BOARD_REV[3:0]={GPIO34, GPIO35, GPIO36, GPIO37}
FLOAT=LOW, PULLUP=HIGH
```

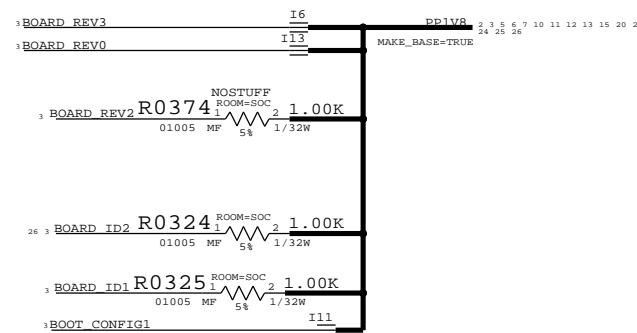
```
1111 PROTOMLB1
1110 PROTOMLB2
1101 PROTO1
1100 PROTO2
1011 EVT
1010 EVT SPLIT CARBON DOE
1001 CARRIER BUILD <--- SELECTED
1000 DVT
```

```
BOARD_ID[4:0]={GPIO29, GPIO16, SPI0_MISO, SPI0_MOSI, SPI0_SCLK}
FLOAT=LOW, PULLUP=HIGH
```

```
00100 N56, T133 MLB
00101 N56 DEV
00110 FIJI N61 MLB <--- SELECTED
```

```
BOOT_CONFIG[2:0]={GPIO28, GPIO25, GPIO18}
FLOAT=LOW, PULLUP=HIGH
```

```
000 SPI0
001 SPI0 TEST MODE
010 NAND <--- SELECTED
011 NAND TEST MODE
100 NVME
101 NVME TEST MODE
111 FAST SPI
```



PAGE TITLE		SYSTEM:N61 SPECIFIC	
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		SHEET	28 OF 54

RADIO_MLB HIERARCHICAL SYMBOL

POWER

VCC_MAIN, VBAT GOES TO RADIO_MLB DIRECTLY
CHECK ALL PAGES IN RF SIDE!

CELLULAR HOUSE KEEPING

AP_TO_RADIO_ON_L	MAKE_BASE-TRIP	I325	RADIO_ON_L	30	32
BB_TO_AP_RESET_DET_L	MAKE_BASE-TRIP	I324	BB_RESET_DET_L	30	35
PMU_TO_BB_RST_L	MAKE_BASE-TRIP	I323	RF_PMIC_RESET_L	30	32
AP_TO_BB_RST_L	MAKE_BASE-TRIP	I322	BB_RST_L	30	32
AP_TO_BB_WAKE_MODEM	MAKE_BASE-TRIP	I329	AP_WAKE_MODEM	35	35
BB_TO_PMU_HOST_WAKE_L	MAKE_BASE-TRIP	I328	BB_WAKE_HOST_L	30	35
BB_TO_AP_IPC_GPIO	MAKE_BASE-TRIP	I327	BB_IPC_GPIO	35	35
BB_TO_LEDDRVM_GSM_BLANK	MAKE_BASE-TRIP	I326	GSM_TXBURST_IND	35	35
BB_TO_AP_GPS_SYNC	MAKE_BASE-TRIP	I325	BB_GPS_SYNC	30	35

HSIC IPC

50_AP_BI_BB_HSIC1_DATA	MAKE_BASE-TRIP	I368	50_BB_HSIC_DATA	30	34
50_AP_BI_BB_HSIC1_STB	MAKE_BASE-TRIP	I367	50_BB_HSIC_STROBE	30	34
AP_TO_BB_HOST_RDY	MAKE_BASE-TRIP	I371	BB_HOST_RDY	30	35
BB_TO_AP_DEVICE_RDY	MAKE_BASE-TRIP	I370	BB_DEVICE_RDY	30	35
BB_TO_AP_IPC_GPIO1	MAKE_BASE-TRIP	I372	BB_IPC_GPIO1	35	35

UART IPC

AP_TO_BB_UART2_RTS_L	MAKE_BASE-TRIP	I373	BB_UART_CTS_L	30	35
BB_TO_AP_UART2_CTS_L	MAKE_BASE-TRIP	I374	BB_UART_RTS_L	30	35
AP_TO_BB_UART2_TXD	MAKE_BASE-TRIP	I374	BB_UART_RXD	30	35
BB_TO_AP_UART2_RXD	MAKE_BASE-TRIP	I375	BB_UART_TXD	30	35

AUDIO I2S

45_AP_TO_BB_I2S3_BCLK	MAKE_BASE-TRIP	I377	BB_I2S_CLK	35	35
AP_TO_BB_I2S3_DOUT	MAKE_BASE-TRIP	I378	BB_I2S_RXD	30	35
BB_TO_AP_I2S3_DIN	MAKE_BASE-TRIP	I379	BB_I2S_TXD	30	35
AP_TO_BB_I2S3_LRCLK	MAKE_BASE-TRIP	I380	BB_I2S_WS	30	35

OSCAR UART

OSCAR_TO_BB_UART_TXD	MAKE_BASE-TRIP	I382	BB_OTHER_RXD	30	35
BB_TO_OSCAR_UART_RXD	MAKE_BASE-TRIP	I381	BB_OTHER_TXD	30	35

BB DEBUG INTERFACES

AP_TO_BB_COREDUMP	MAKE_BASE-TRIP	I384	BB_CORE_DUMP	30	35
PMU_TO_BB_VBUS_DET	MAKE_BASE-TRIP	I387	BB_USB_VBUS	30	34
90_TRISTAR_BI_BB_USB_N	MAKE_BASE-TRIP	I386	90_BB_USB_N	30	34
90_TRISTAR_BI_BB_USB_P	MAKE_BASE-TRIP	I388	90_BB_USB_P	30	34

RADIO ANTENNA CONTROL

PP_BB_VDD_2V7	MAKE_BASE-TRIP	I389	PP_LDO14_RFSW	31	41
BB_GPIO0	MAKE_BASE-TRIP	I390	BB_LAT_GPIO0	35	35
BB_GPIO2	MAKE_BASE-TRIP	I391	BB_LAT_GPIO2	35	35
BB_GPIO3	MAKE_BASE-TRIP	I392	BB_LAT_GPIO3	35	35
BB_GPIO4	MAKE_BASE-TRIP	I394	BB_LAT_GPIO4	35	35

FCT TESTING

RADIO_TO_PMU_ADC_SMP51	MAKE_BASE-TRIP	I395	ADC_SMP51	30	30
RADIO_TO_PMU_ADC_PP_LD011_VDDIO	MAKE_BASE-TRIP	I396	ADC_PP_LD011	30	30
RADIO_TO_PMU_ADC_PP_LD05_SIM	MAKE_BASE-TRIP	I398	ADC_PP_LD05	30	30
RADIO_TO_PMU_ADC_SMP54	MAKE_BASE-TRIP	I397	ADC_SMP54	30	30

UPPER RADIO ANTENNA CONTROL

50_AP_WIFI_5G_CONN_ANT	MAKE_BASE-TRIP	I410	50_WIFI_5G_CONN_ANT	50	50
50_AP_UAT_FEED	MAKE_BASE-TRIP	I409	50_UPPER_ANT_FEED	50	50
UAT_ANT_GND	MAKE_BASE-TRIP	I411	ANT_GND	50	50
PP3V0_TRISTAR	MAKE_BASE-TRIP	I404	PAC_VDD_3V0	53	53
NORTH_AC_GND_SCREW	MAKE_BASE-TRIP	I412	NORTH_ANT_GND	50	50

POWER



WLAN/BT HOUSE KEEPING

45_PMU_TO_WLAN_CLK32K	MAKE_BASE-TRIP	I316	CLK32K_AP	30	51
PMU_TO_WLAN_REG_ON	MAKE_BASE-TRIP	I317	WLAN_REG_ON	30	51
WLAN_TO_PMU_HOST_WAKE	MAKE_BASE-TRIP	I318	HOST_WAKE_WLAN	30	51
PMU_TO_BT_REG_ON	MAKE_BASE-TRIP	I319	BT_REG_ON	30	51
AP_TO_BT_WAKE	MAKE_BASE-TRIP	I320	WAKE_BT	30	51
BT_TO_PMU_HOST_WAKE	MAKE_BASE-TRIP	I321	HOST_WAKE_BT	51	51

AP_TO_WLAN_JTAG_SWCLK	MAKE_BASE-TRIP	I333	WLAN_JTAG_SWCLK	30	51
AP_TO_WLAN_JTAG_SWDIO	MAKE_BASE-TRIP	I334	WLAN_JTAG_SWDIO	30	51
WLAN_TO_PMU_PCIE_WAKE_L	MAKE_BASE-TRIP	I335	WLAN_PCIE_WAKE_L	30	51
AP_TO_WLAN_DEVICE_WAKE	MAKE_BASE-TRIP	I336	PCIE_DEV_WAKE	30	51
90_WLAN_TO_AP_PCIE1_RXDP_P	MAKE_BASE-TRIP	I337	90_WLAN_PCIE_TDP	30	51
90_WLAN_TO_AP_PCIE1_RXDP_N	MAKE_BASE-TRIP	I338	90_WLAN_PCIE_TDN	30	51
90_AP_TO_WLAN_PCIE1_TXDP_P	MAKE_BASE-TRIP	I338	90_WLAN_PCIE_RDP	30	51
90_AP_TO_WLAN_PCIE1_TXDP_N	MAKE_BASE-TRIP	I339	90_WLAN_PCIE_RDN	30	51
90_AP_TO_WLAN_PCIE1_REFCLK1_P	MAKE_BASE-TRIP	I342	90_WLAN_PCIE_REFCLK_P	51	51
90_AP_TO_WLAN_PCIE1_REFCLK1_N	MAKE_BASE-TRIP	I341	90_WLAN_PCIE_REFCLK_N	51	51
WLAN_TO_AP_PCIE1_CLKREQ_L	MAKE_BASE-TRIP	I344	WLAN_PCIE_CLKREQ_L	30	51
AP_TO_WLAN_PCIE1_RST_L	MAKE_BASE-TRIP	I343	WLAN_PCIE_PERST_L	30	51

WLAN HSIC IPC

WLAN_TO_AP_UART4_RXD	MAKE_BASE-TRIP	I345	WLAN_UART_TXD	30	51
AP_TO_WLAN_UART4_TXD	MAKE_BASE-TRIP	I346	WLAN_UART_RXD	30	51
WLAN_TO_AP_UART4_CTS_L	MAKE_BASE-TRIP	I347	WLAN_UART_RTS_L	30	51
AP_TO_WLAN_UART4_RTS_L	MAKE_BASE-TRIP	I346	WLAN_UART_CTS_L	30	51

BT UART IPC

AP_TO_BT_UART1_RTS_L	MAKE_BASE-TRIP	I349	BT_UART_CTS_L	51	51
BT_TO_AP_UART1_CTS_L	MAKE_BASE-TRIP	I352	BT_UART_RTS_L	51	51
AP_TO_BT_UART1_TXD	MAKE_BASE-TRIP	I351	BT_UART_RXD	30	51
BT_TO_AP_UART1_RXD	MAKE_BASE-TRIP	I350	BT_UART_TXD	30	51

BT AUDIO PCM

45_AP_TO_BT_I2S1_BCLK	MAKE_BASE-TRIP	I354	BT_PCM_CLK	51	51
AP_TO_BT_I2S1_DOUT	MAKE_BASE-TRIP	I353	BT_PCM_IN	51	51
BT_TO_AP_I2S1_DIN	MAKE_BASE-TRIP	I355	BT_PCM_OUT	51	51
AP_TO_BT_I2S1_LRCLK	MAKE_BASE-TRIP	I356	BT_PCM_SYNC	51	51

OSCAR STATES

OSCAR_TO_RADIO_CONTEXT_A	MAKE_BASE-TRIP	I358	OSCAR_CONTEXT_A	51	51
OSCAR_TO_RADIO_CONTEXT_B	MAKE_BASE-TRIP	I357	OSCAR_CONTEXT_B	51	51

STOCKHOLM

STOCKHOLM_TO_AP_UART3_CTS_L	MAKE_BASE-TRIP	I359	STOCKHOLM_RTS_L	30	52
AP_TO_STOCKHOLM_UART3_RTS_L	MAKE_BASE-TRIP	I360	STOCKHOLM_CTS_L	30	52
STOCKHOLM_TO_AP_UART3_RXD	MAKE_BASE-TRIP	I361	STOCKHOLM_UART_TXD	30	52
AP_TO_STOCKHOLM_UART3_TXD	MAKE_BASE-TRIP	I362	STOCKHOLM_UART_RXD	30	52
AP_TO_STOCKHOLM_DWLD_REQ	MAKE_BASE-TRIP	I363	STOCKHOLM_FW_DWLD_REQ	52	52
STOCKHOLM_TO_PMU_HOST_WAKE	MAKE_BASE-TRIP	I364	STOCKHOLM_HOST_WAKE	30	52
AP_TO_STOCKHOLM_EN	MAKE_BASE-TRIP	I365	STOCKHOLM_ENABLE	53	53
PP3V0_TRISTAR	MAKE_BASE-TRIP	I366	STOCKHOLM_VDD_MUX_3V0	54	54
AP_TO_STOCKHOLM_SIM_SEL	MAKE_BASE-TRIP	I367	STOCKHOLM_SIM_SEL	54	54
AP_TO_STOCKHOLM_ANT	MAKE_BASE-TRIP	I406	STOCKHOLM_ANT	52	52

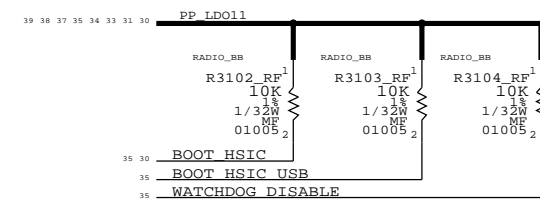
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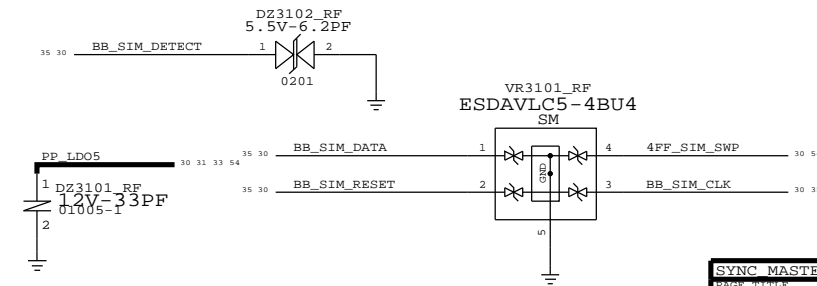
PROBE POINTS

PP3105_RF P2MM-NSM SM 1 1 CLK32K_AP 29 51	PP3121_RF P2MM-NSM SM 1 STOCKHOLM_HOST_WAKE 29 52	PP3115_RF P4MM-NSM SM 1 50_BB_HSIC_STROBE 29 34	PP3130_RF P4MM-NSM SM 1 1_BB_JTAG_RST_L 34	PP3141_RF P4MM-NSM SM 1 1_BB_UART_TXD 29 35	PP3170_RF P4MM-NSM SM 1 RFFE1_CLK 35 39 40 41 42 43 44
PP3113_RF P4MM-NSM SM 1 1_BB_COEX_UART_RXD 35 51	PP3122_RF P4MM-NSM SM 1 1_BB_REQUEST_XO_CLK 32 52	PP3116_RF P4MM-NSM SM 1 50_BB_HSIC_DATA 29 34	PP3131_RF P4MM-NSM SM 1 1_BB_JTAG_TCK 34	PP3142_RF P4MM-NSM SM 1 1_BB_UART_RXD 29 35	PP3171_RF P4MM-NSM SM 1 RFFE1_DATA 35 39 40 41 42 43 44
PP3114_RF P4MM-NSM SM 1 1_BB_COEX_UART_TXD 35 51	PP3123_RF P2MM-NSM SM 1 STOCKHOLM_UART_RXD 29 52	PP3101_RF P4MM-NSM SM 1 1_BB_DEBUG_ERROR 35	PP3132_RF P4MM-NSM SM 1 1_BB_JTAG_TMS 34	PP3143_RF P4MM-NSM SM 1 1_BB_UART_RTS_L 29 35	PP3172_RF P4MM-NSM SM 1 RFFE2_CLK 35 45 46 48
PP3119_RF P2MM-NSM SM 1 1_BT_UART_TXD 29 51	PP3124_RF P2MM-NSM SM 1 STOCKHOLM_UART_TXD 29 52	PP3102_RF P4MM-NSM SM 1 1_RF_PMIC_RESET_L 29 32	PP3133_RF P4MM-NSM SM 1 1_BB_JTAG_TDO 34	PP3144_RF P4MM-NSM SM 1 1_BB_UART_CTS_L 29 35	PP3173_RF P4MM-NSM SM 1 RFFE2_DATA 35 45 46 48
PP3120_RF P2MM-NSM SM 1 1_BT_UART_RXD 29 51	PP3125_RF P2MM-NSM SM 1 STOCKHOLM_CTS_L 29 52	PP3103_RF P4MM-NSM SM 1 1_PS_HOLD_PMIC 32	PP3134_RF P4MM-NSM SM 1 1_BB_JTAG_TDI 34	PP3145_RF P4MM-NSM SM 1 1_BB_HOST_RDY 29 35	PP3175_RF P4MM-NSM SM 1 BB_I2S_WS 29 35
PP3152_RF P2MM-NSM SM 1 1_WAKE_BT 29 51	PP3126_RF P2MM-NSM SM 1 STOCKHOLM_RTS_L 29 52	PP3127_RF P4MM-NSM SM 1 1_PMIC_RESOUT_L 32 34	PP3135_RF P4MM-NSM SM 1 1_BB_JTAG_TRST_L 34	PP3146_RF P4MM-NSM SM 1 1_BB_DEVICE_RDY 29 35	PP3176_RF P4MM-NSM SM 1 BB_I2S_RXD 29 35
PP3153_RF P2MM-NSM SM 1 1_WLAN_REG_ON 29 51	PP3128_RF P4MM-NSM SM 1 PP_PN65_VCC_SIM 52	PP3104_RF P4MM-NSM SM 1 1_MDM_CLK 32 34	PP3136_RF P4MM-NSM SM 1 1_BB_DEBUG_STATUS 35	PP3147_RF P4MM-NSM SM 1 1_BB_GPS_SYNC 29 35	PP3177_RF P4MM-NSM SM 1 BB_I2S_TXD 29 35
PP3154_RF P4MM-NSM SM 1 1_BT_REG_ON 29 51	PP3174_RF P4MM-NSM SM 1 STOCKHOLM_SIM_SWP 52 54	PP3109_RF P4MM-NSM SM 1 1_PP_LD011 30 31 33 34 35 37 38	PP3137_RF P4MM-NSM SM 1 1_BB_CORE_DUMP 29 35	PP3148_RF P4MM-NSM SM 1 1_BB_WAKE_HOST_L 29 35	PP3178_RF P4MM-NSM SM 1 BB_OTHER_TXD 29 35
PP3155_RF P2MM-NSM SM 1 1_HOST_WAKE_WLAN 29 51	PP3129_RF P4MM-NSM SM 1 REF_CLK_FROM_BB 32 52	PP3110_RF P4MM-NSM SM 1 1_RADIO_ON_L 29 32	PP3138_RF P4MM-NSM SM 1 1_BB_USB_VBUS 29 34	PP3149_RF P4MM-NSM SM 1 1_BB_RESET_DET_L 29 35	PP3179_RF P4MM-NSM SM 1 BB_OTHER_RXD 29 35
PP3156_RF P2MM-NSM SM 1 1_WLAN_PCIE_WAKE_L 29 51	PP3165_RF P4MM-NSM SM 1 1_DSDS_SIM_CLK 34 54	PP3111_RF P4MM-NSM SM 1 1_SPMI_DATA 32 34	PP3139_RF P4MM-NSM SM 1 1_90_BB_USB_N 29 34	PP3150_RF P4MM-NSM SM 1 1_BB_RST_L 29 32	
PP3157_RF P2MM-NSM SM 1 1_WLAN_PCIE_PERST_L 29 51	PP3183_RF P4MM-NSM SM 1 1_DSDS_SIM_RESET 34 54	PP3112_RF P4MM-NSM SM 1 1_SPMI_CLK 32 34	PP3140_RF P4MM-NSM SM 1 1_90_BB_USB_P 29 34	PP3151_RF P4MM-NSM SM 1 1_BOOT_HSIC 30 35	
PP3158_RF P4MM-NSM SM 1 1_WLAN_PCIE_CLKREQ_L 29 51	PP3184_RF P4MM-NSM SM 1 1_DSDS_SIM_DATA 34 54				
PP3159_RF P4MM-NSM SM 1 1_PCIE_DEV_WAKE 29 51	PP3188_RF P4MM-NSM SM 1 1_DSDS_SIM_DETECT 34				
PP3160_RF P2MM-NSM SM 1 1_WLAN_UART_RTS_L 29 51	PP3187_RF P4MM-NSM SM 1 PP_LD06 31 33 54				
PP3161_RF P2MM-NSM SM 1 1_WLAN_UART_CTS_L 29 51	PP3188_RF P4MM-NSM SM 1 1_DSDS_SIM_SWP 54				
PP3162_RF P2MM-NSM SM 1 1_WLAN_UART_RXD 29 51	PP3189_RF P4MM-NSM SM 1 1_DSDS_SIM_DATA_R 54				
PP3163_RF P2MM-NSM SM 1 1_WLAN_UART_TXD 29 51	PP 3178_RF P2MM-NSM SM 1 1_BB_SIM_RESET 30 35				
PP3190_RF P2MM-NSM SM 1 1_WLAN_JTAG_SWCLK 29 51	PP 3179_RF P2MM-NSM SM 1 1_BB_SIM_CLK 30 35				
PP3191_RF P2MM-NSM SM 1 1_WLAN_JTAG_SWDIO 29 51	PP 3180_RF P2MM-NSM SM 1 1_BB_SIM_DATA 30 35				
	PP 3183_RF P2MM-NSM SM 1 1_BB_SIM_DETECT 30 35				
	PP 3184_RF P2MM-NSM SM 1 1_PP_LD05 30 31 33 54				

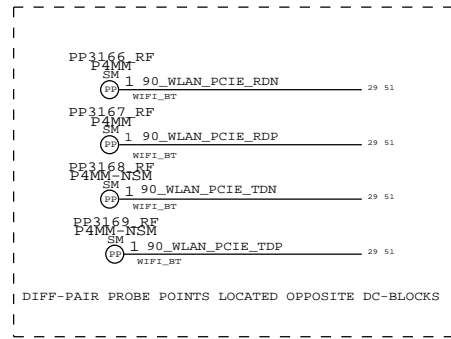
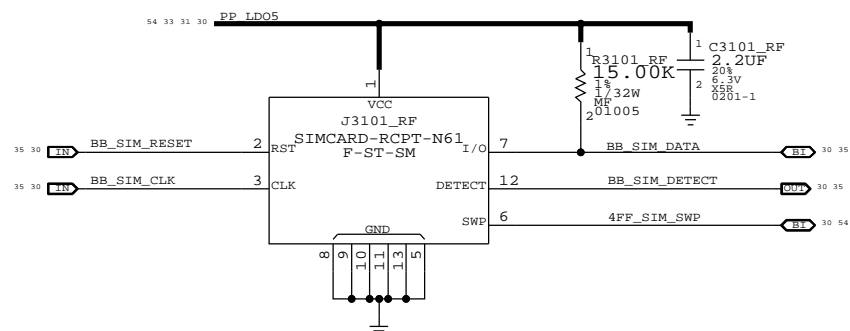
PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y3301_RF	KDS 19.2MHZ XTAL
197S0598	197S0593	ALTERNATE	Y3301_RF	AVX 19.2MHZ XTAL
138S00005	138S00003	ALTERNATE	C3216_RF	15UF CAPACITOR
138S0739	138S0706	ALTERNATE	C4207_RF	1.0UF CAPACITOR
138S0945	138S0706	ALTERNATE	C4207_RF	1.0UF CAPACITOR
138S1103	138S0719	ALTERNATE	C4007_RF	4.7UF CAPACITOR
339S0231	339S0228	ALTERNATE	U5201_RF	CORONA MODULE USI
339S0242	339S0228	ALTERNATE	U5201_RF	CORONA MODULE TDK
155S00024	155S0950	ALTERNATE	F_TRI_RF	TRIPLEXER BIN2



SIM CARD ESD PROTECTION



SIM CARD CONNECTOR

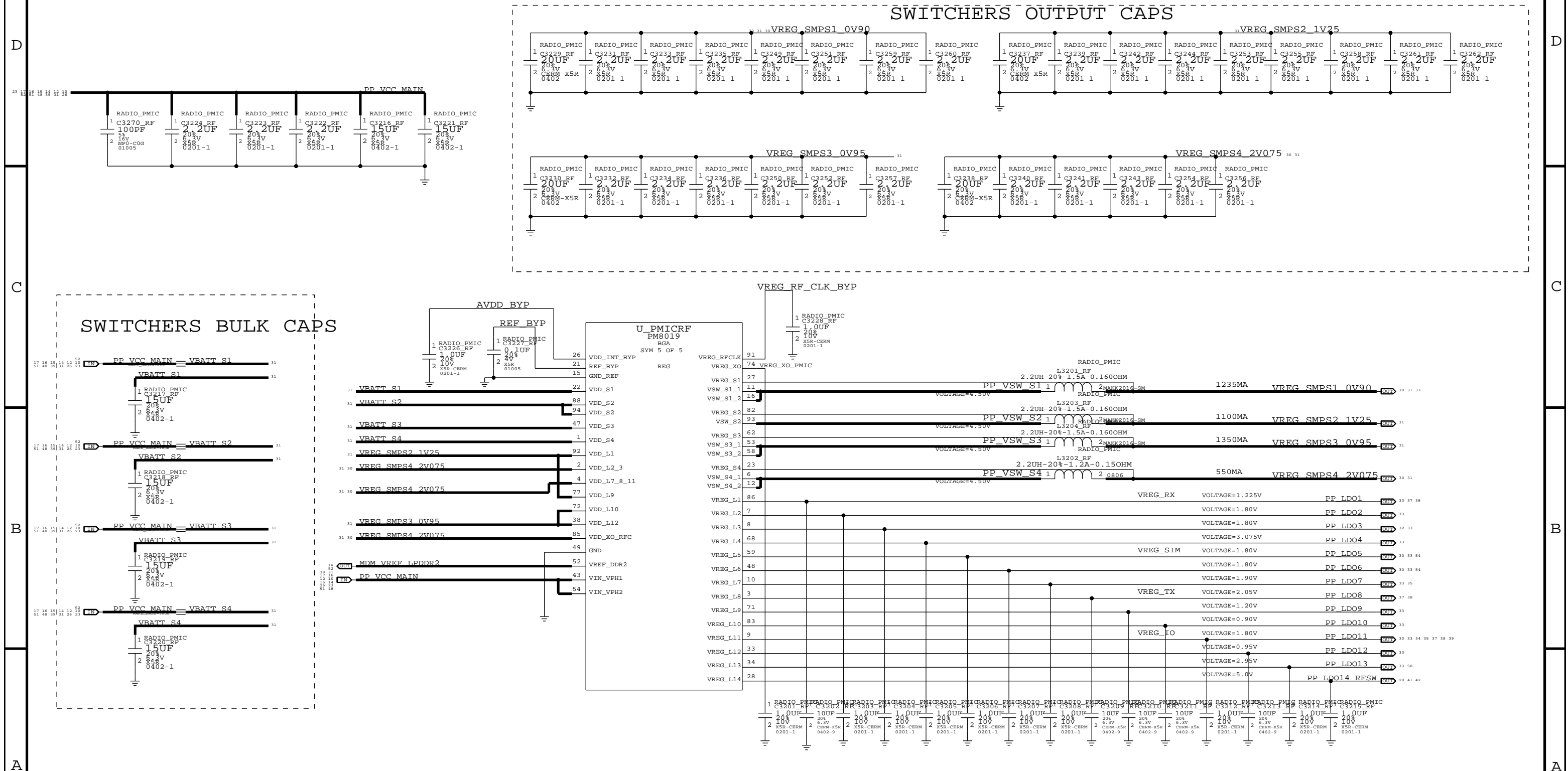


33 31	XW3101_RF	SHORT-10L-0.1MM-SM	ADC_SMPS1	29
38 37 35 34 33 32 31	PP_LD011	SHORT-10L-0.1MM-SM	ADC_PP_LD011	29
54 33 31 30	PP_LD05	SHORT-10L-0.1MM-SM	ADC_PP_LD05	29
31	VREG_SMPS4_2V075	SHORT-10L-0.1MM-SM	ADC_SMPS4	29


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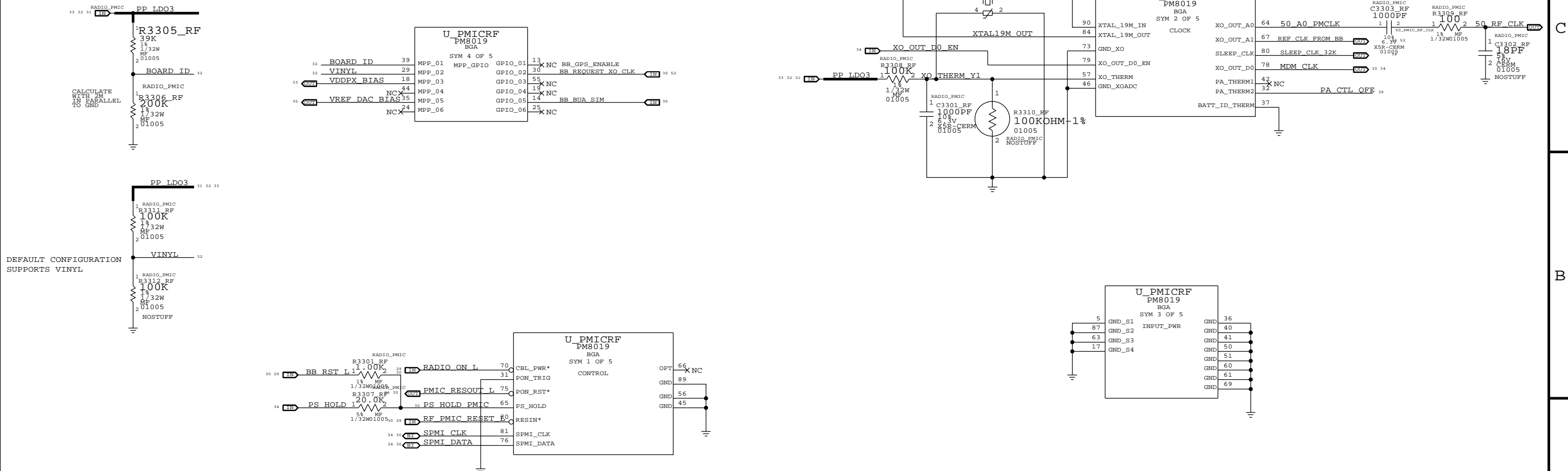
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C401
R411
L400
U404

BOARD REVISION	
0.00V	N61 PROTO_MLB1
0.50V	N61 DEV3
0.70V	N61 DEV4
0.90V	N61 PROTO_MLB2
1.10V	N61/N56 PROTO1
1.30V	N61/N56 PROTO2
1.40V	N61/N56 EVT1
1.50V	N61/N56 EVT2 (CARRIER)
1.60V	N61/N56 DVT
1.70V	N61/N56 PVT

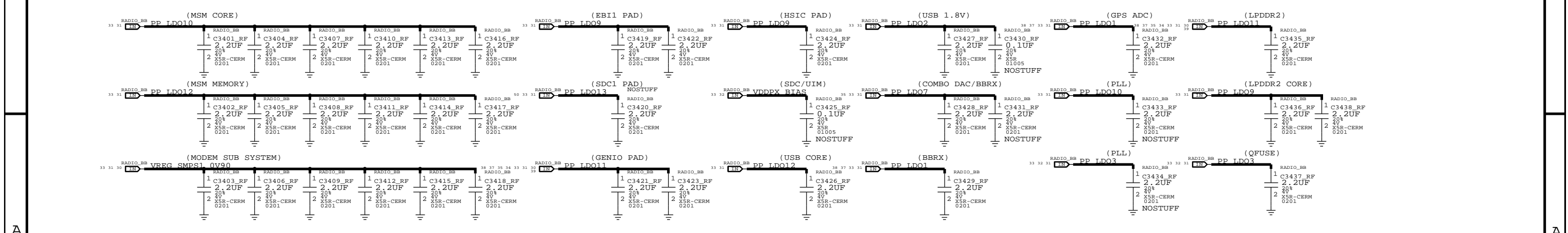
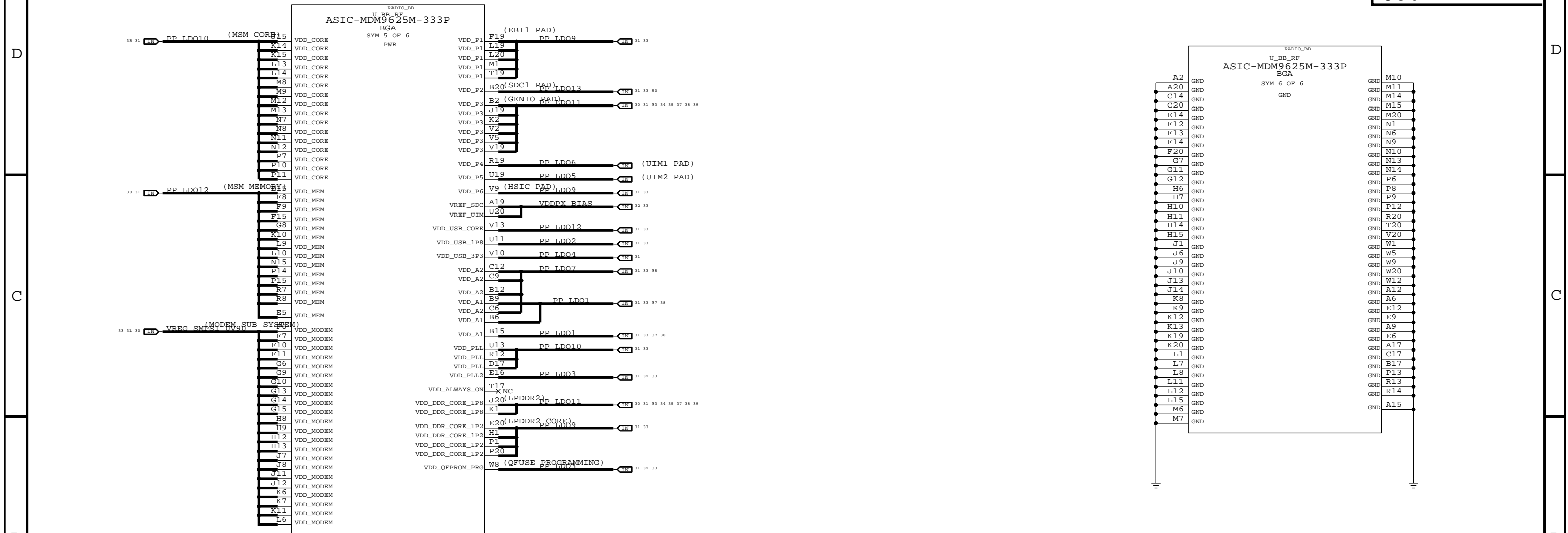


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C538
R500
L500
U502

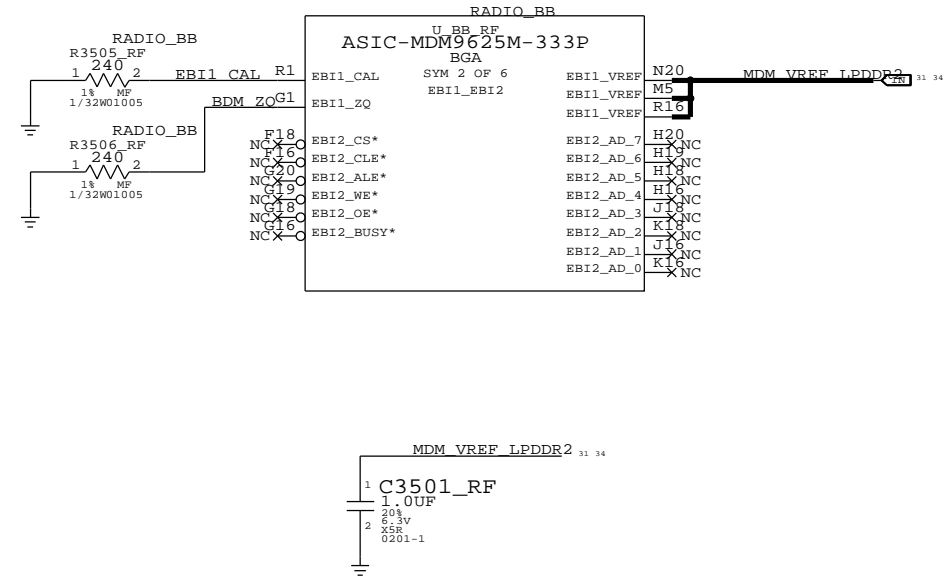
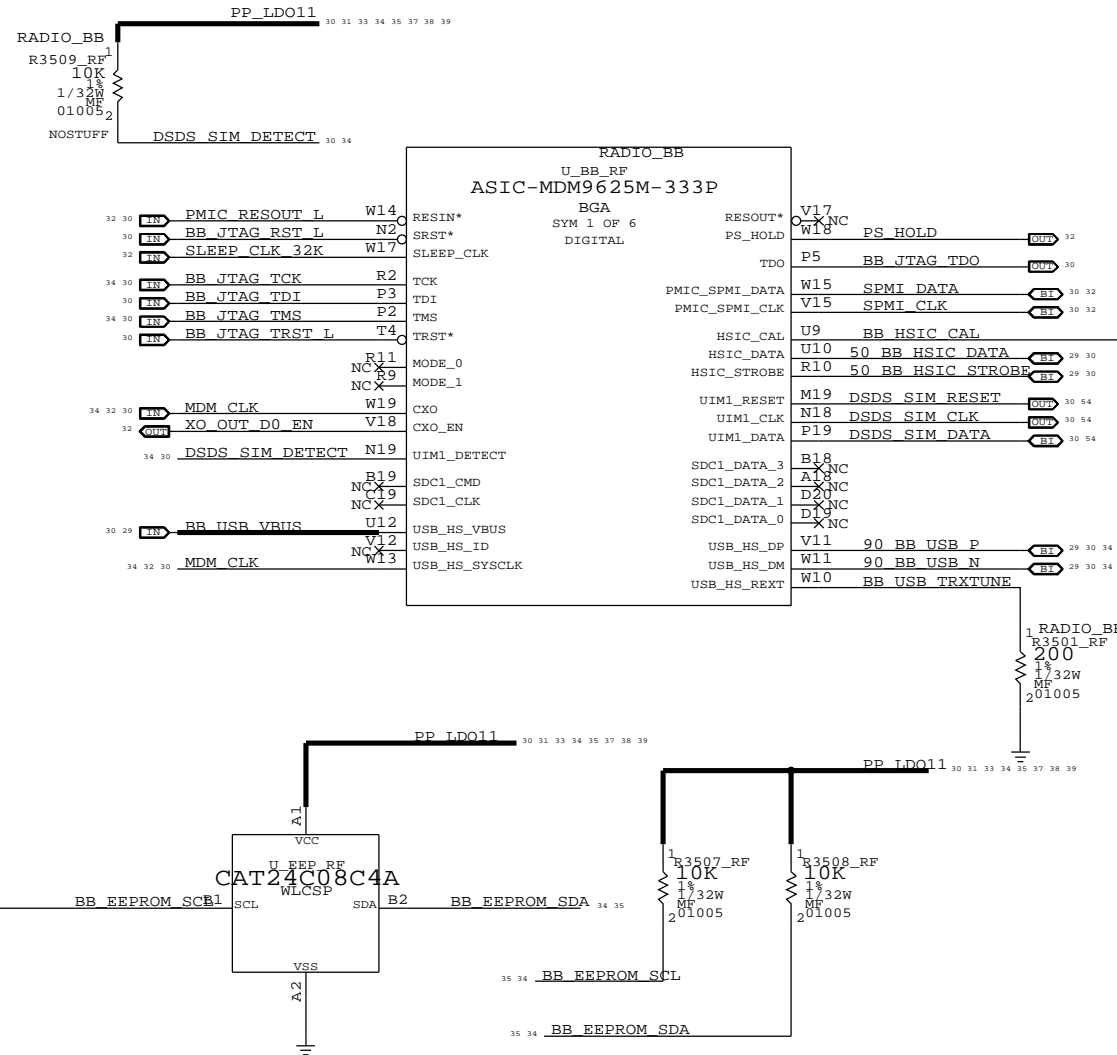
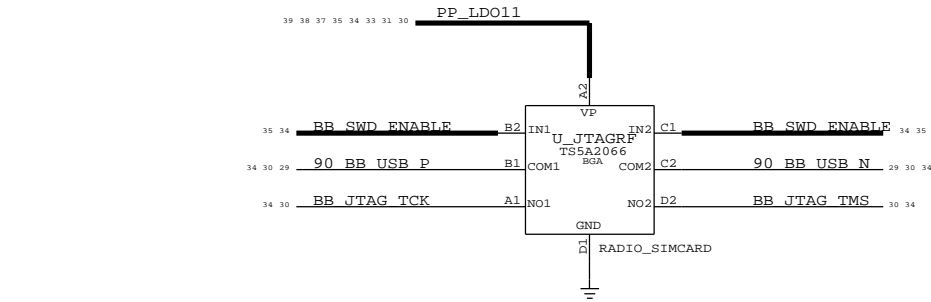


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C600
R606
L600
U602

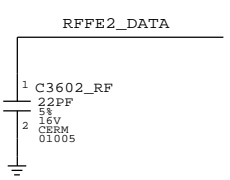
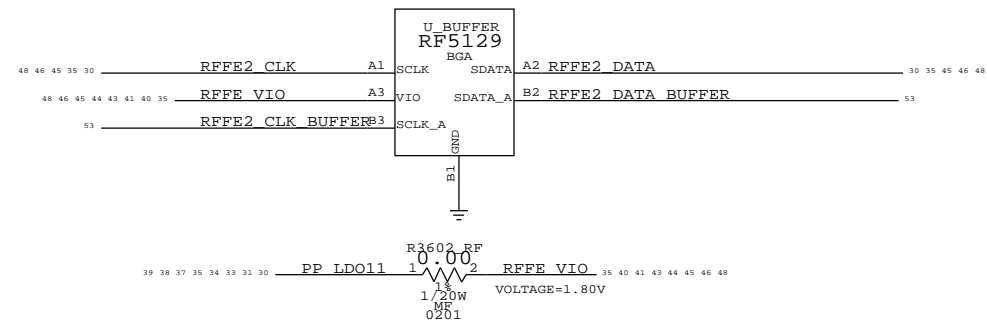
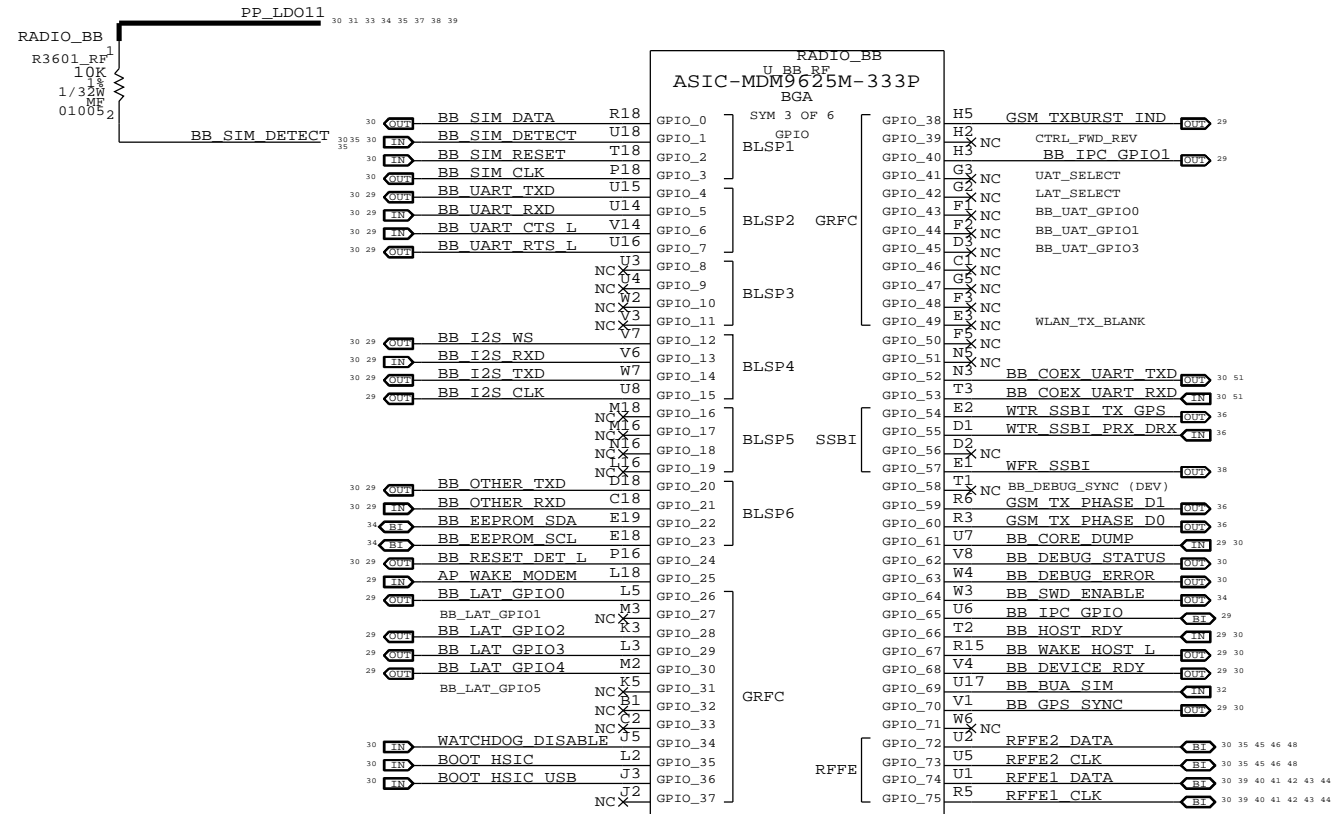
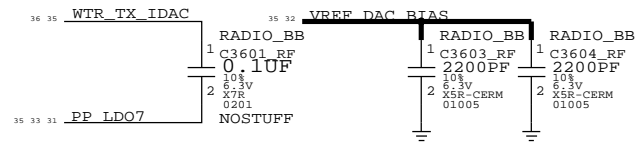
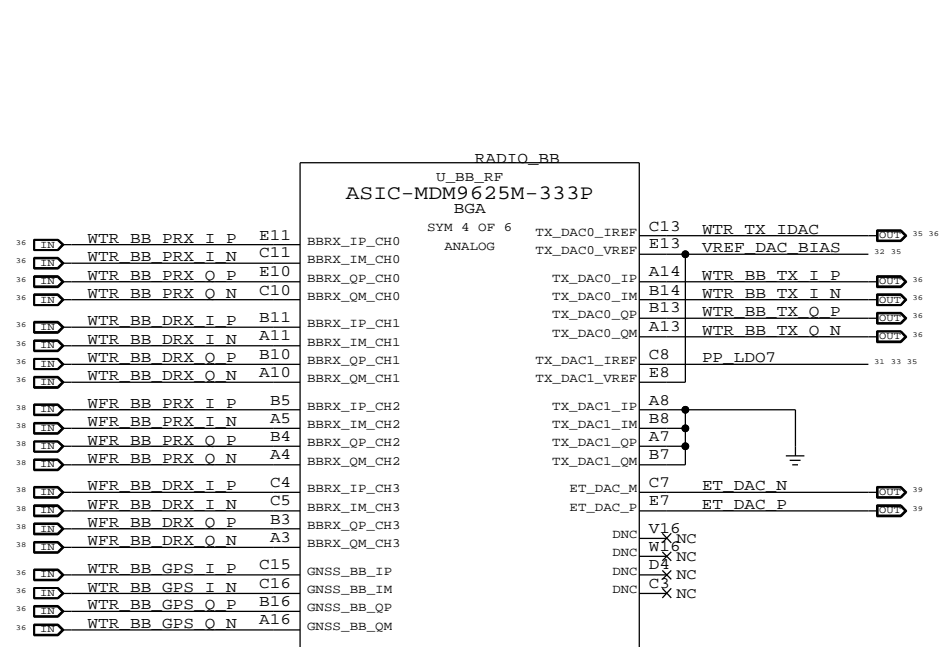


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C704
R700
L700
U702



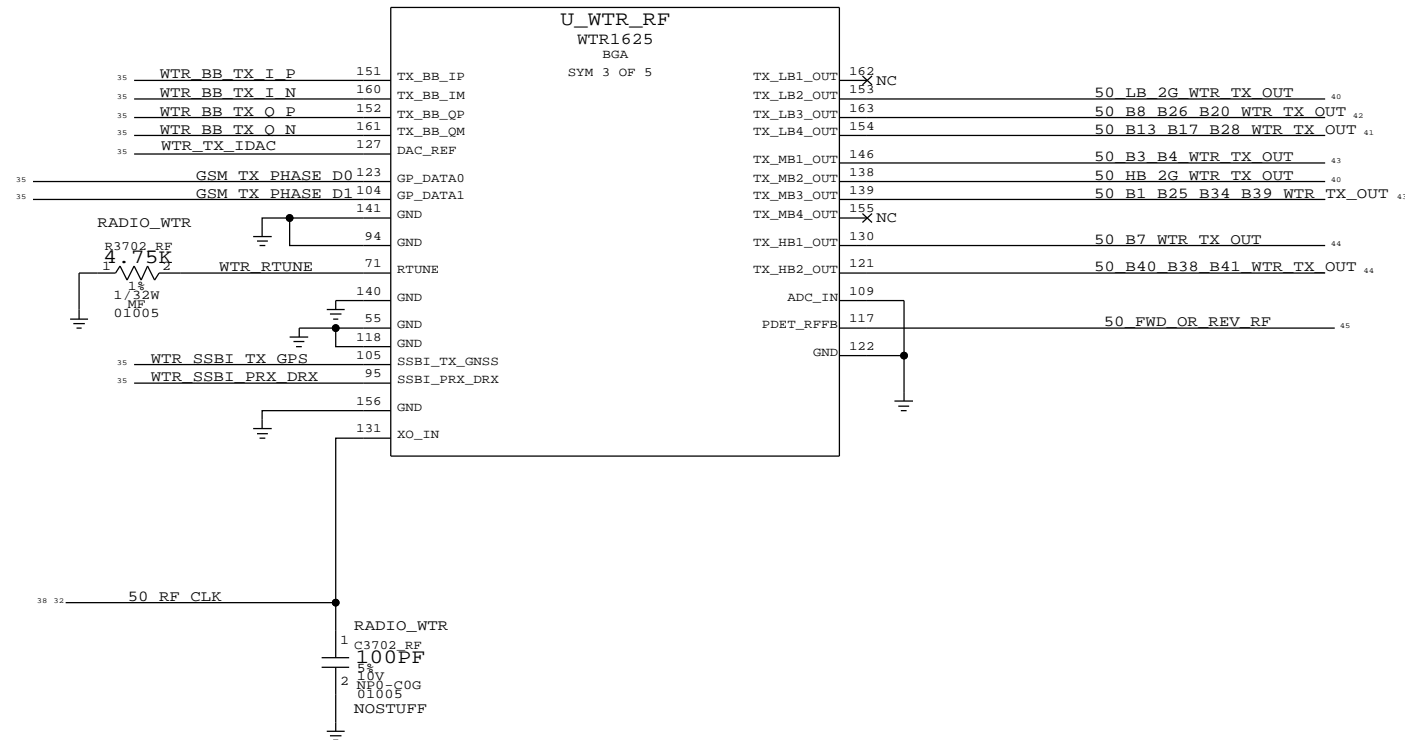
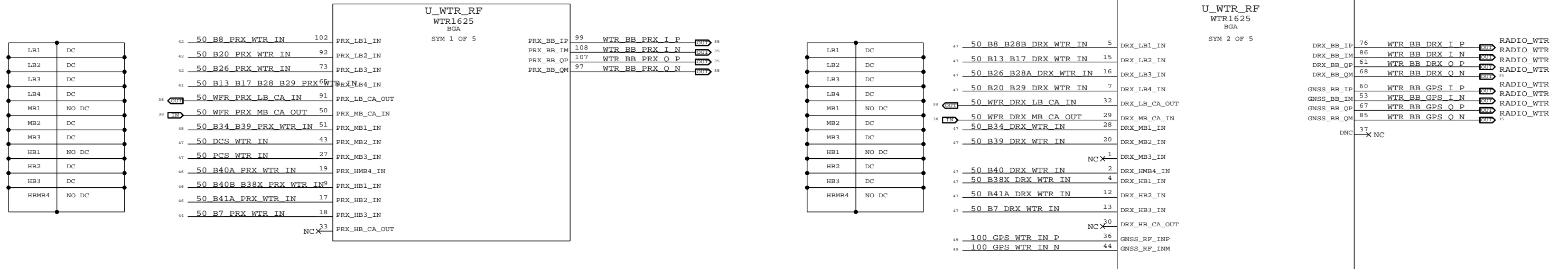
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WTR TRANSCEIVER (1 OF 2)

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C802
R802
L800
U803



RF_CLK IS SHARED BETWEEN WTR AND WFR. LENGTH DIFFERENCE BETWEEN THE TWO SHOULD BE < 5MM.

RF TRANSCEIVER (1 OF 3)

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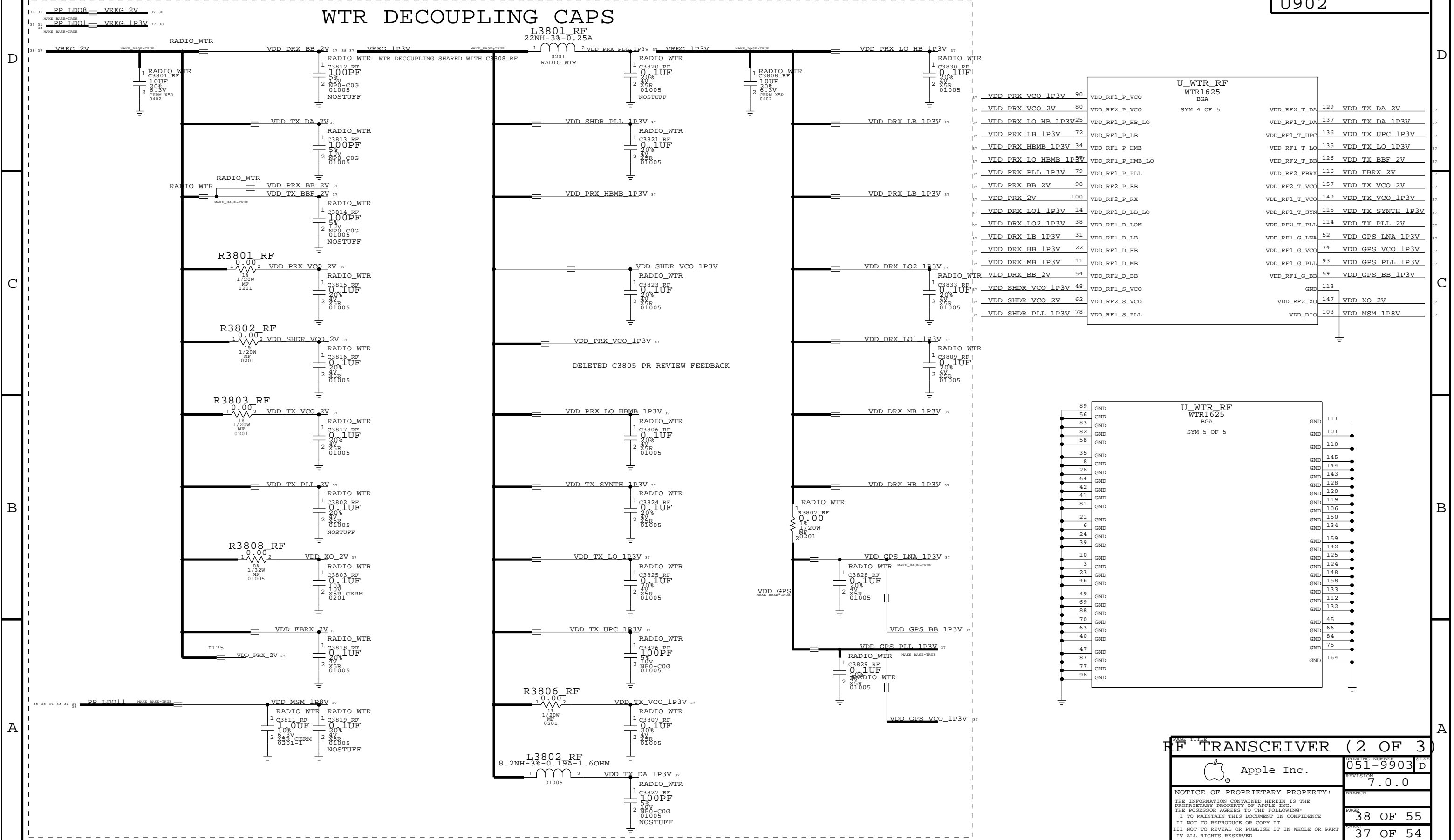
WTR TRANSCEIVER (2 OF 2)

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C934
R926
L3802_RF
U902

WTR DECOUPLING CAPS

L3801_RF
22NH-3%-0.25A



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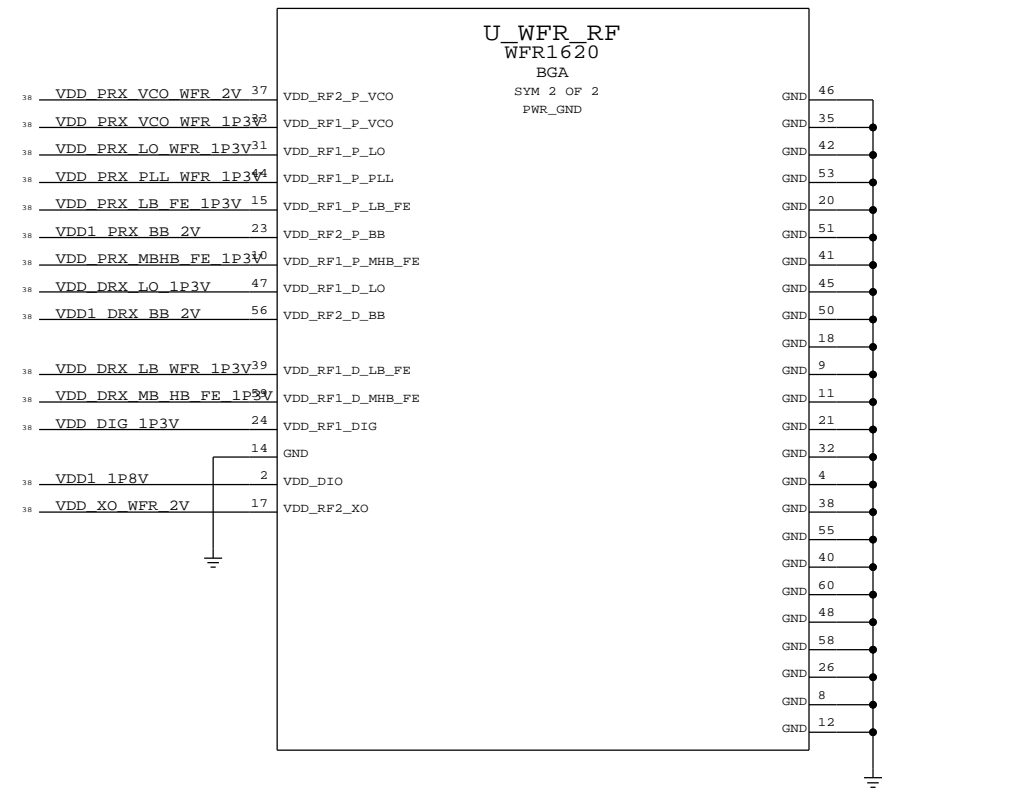
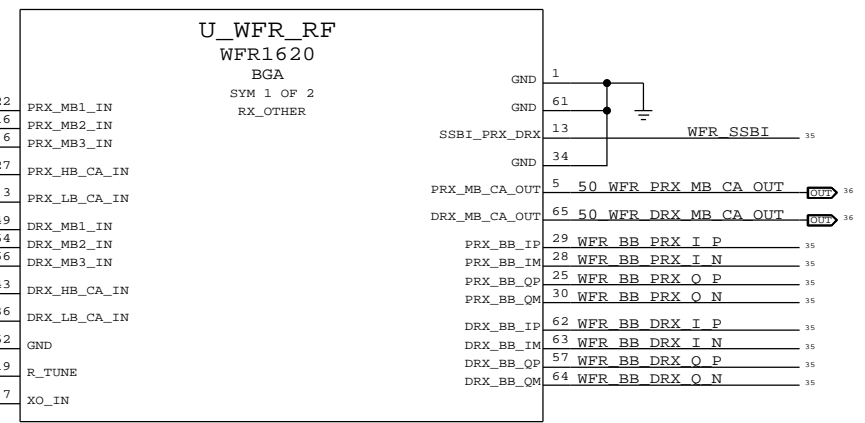
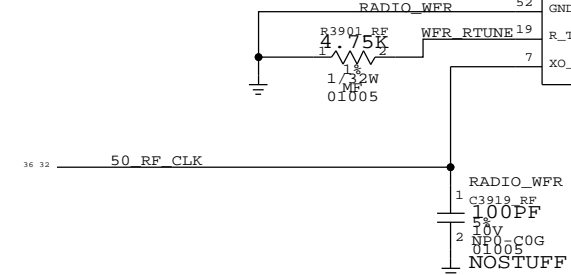
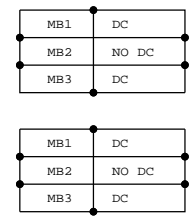
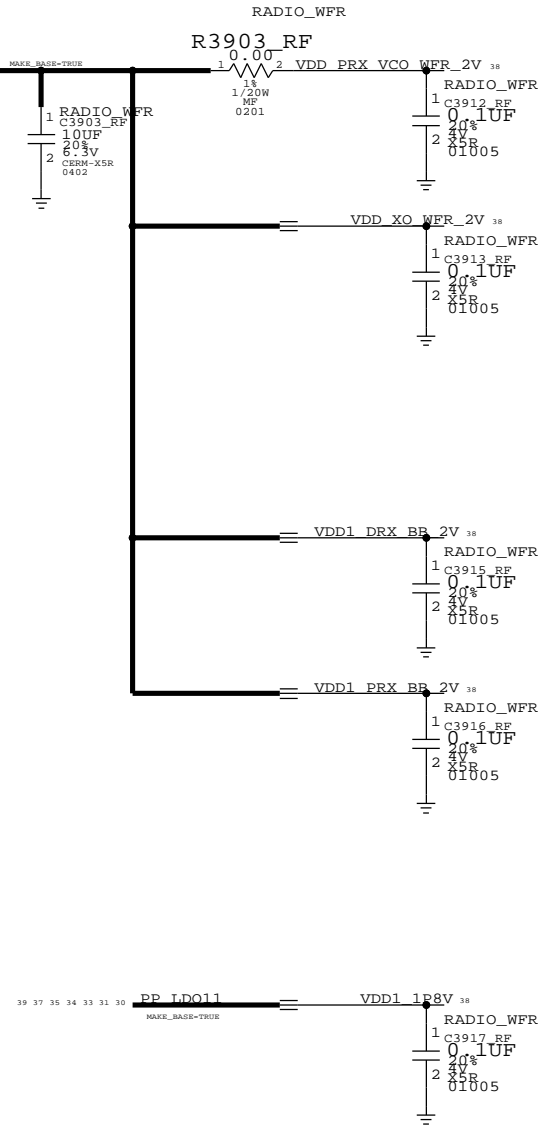
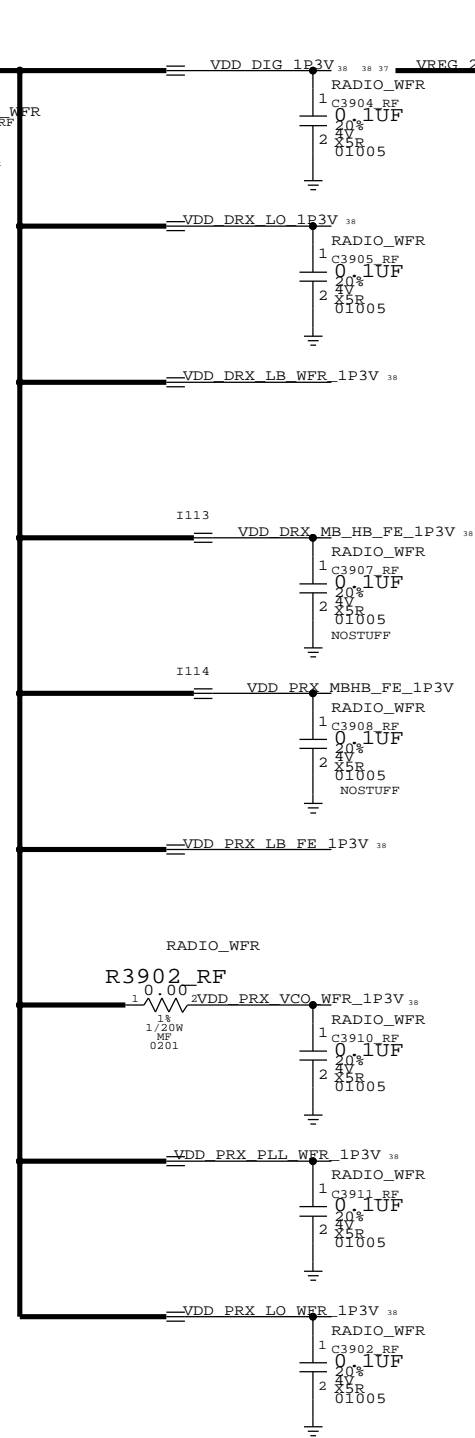
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WFR TRANSCEIVER

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C1019
R1016
L1000
U1002

37 31 PP_ID08 VREG 2V 37 38
MAKE_BASE-TRUE
37 33 31 PP_ID01 VREG 1P3V 37 38
MAKE_BASE-TRUE
38 37 VREG 1P3V
MAKE_BASE-TRUE



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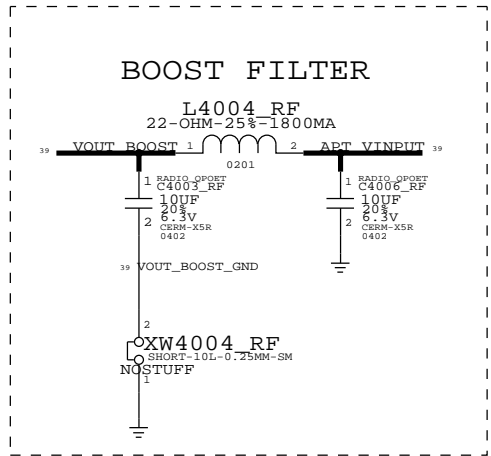
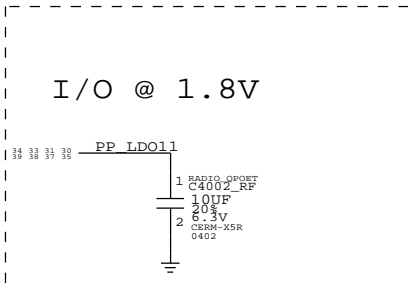
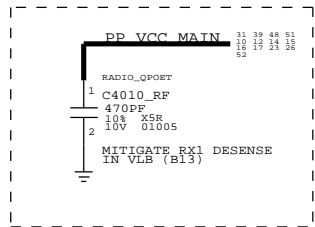
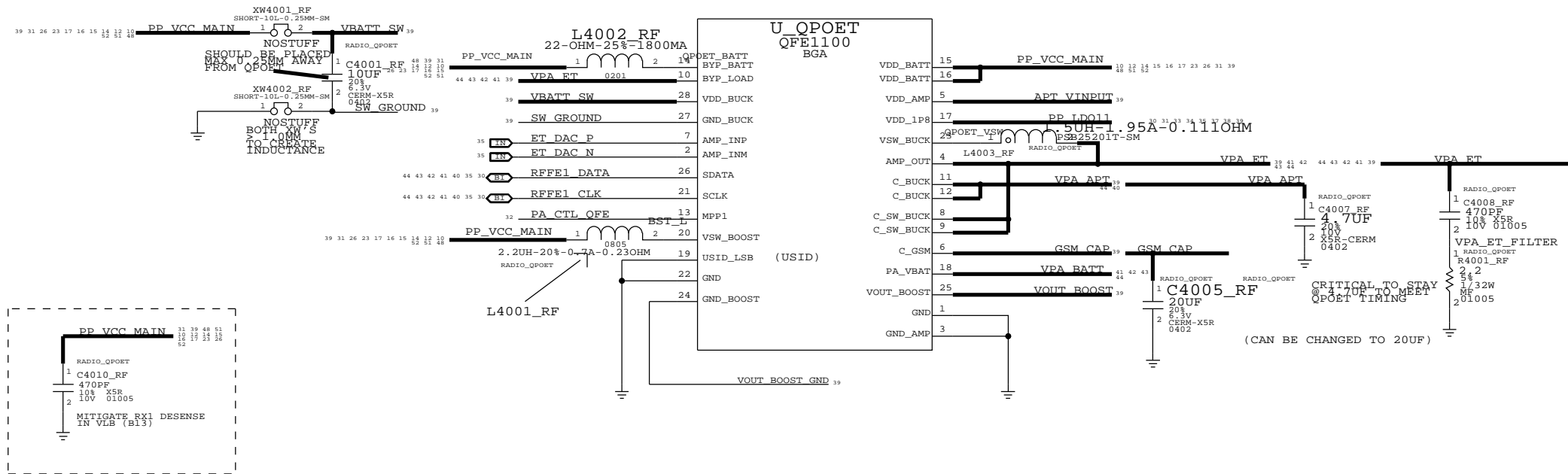
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QFE DCDC

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C1110
R1102
L1104
U1101

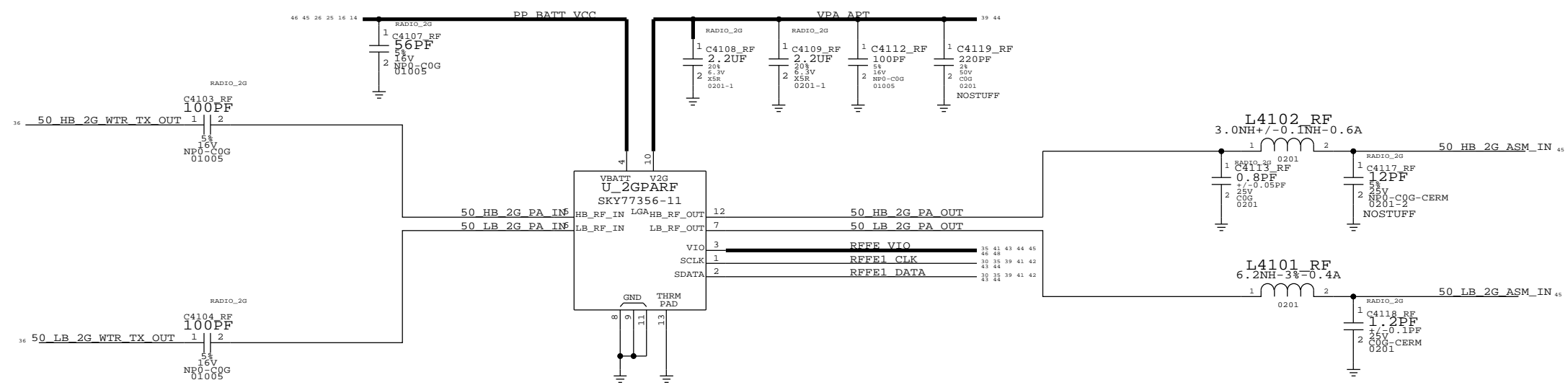



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2G PA

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C1208
R1200
L1204
U1201



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VERY LOW BAND PAD (B13, B17, B28)

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C1332
R1300
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U1304

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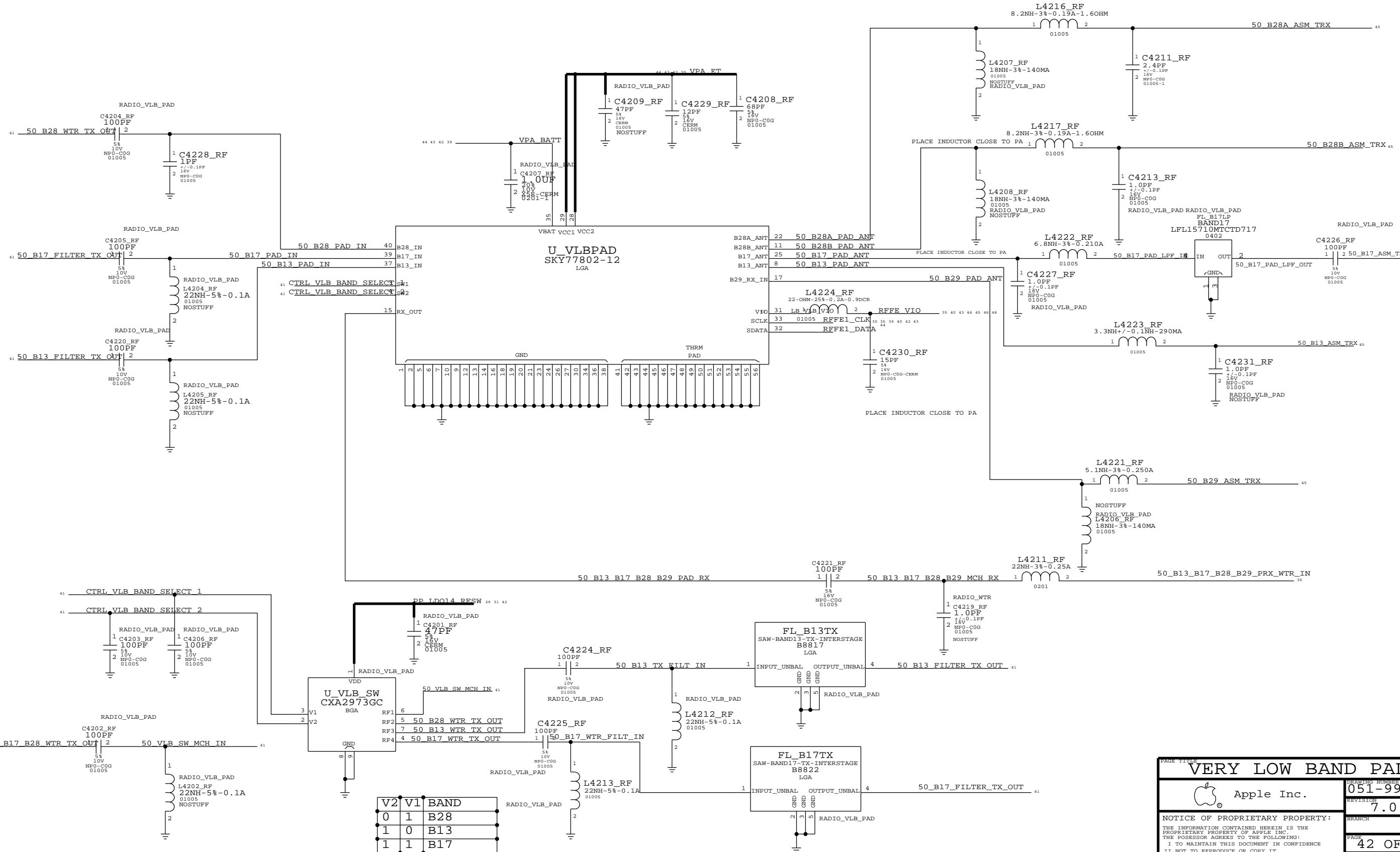
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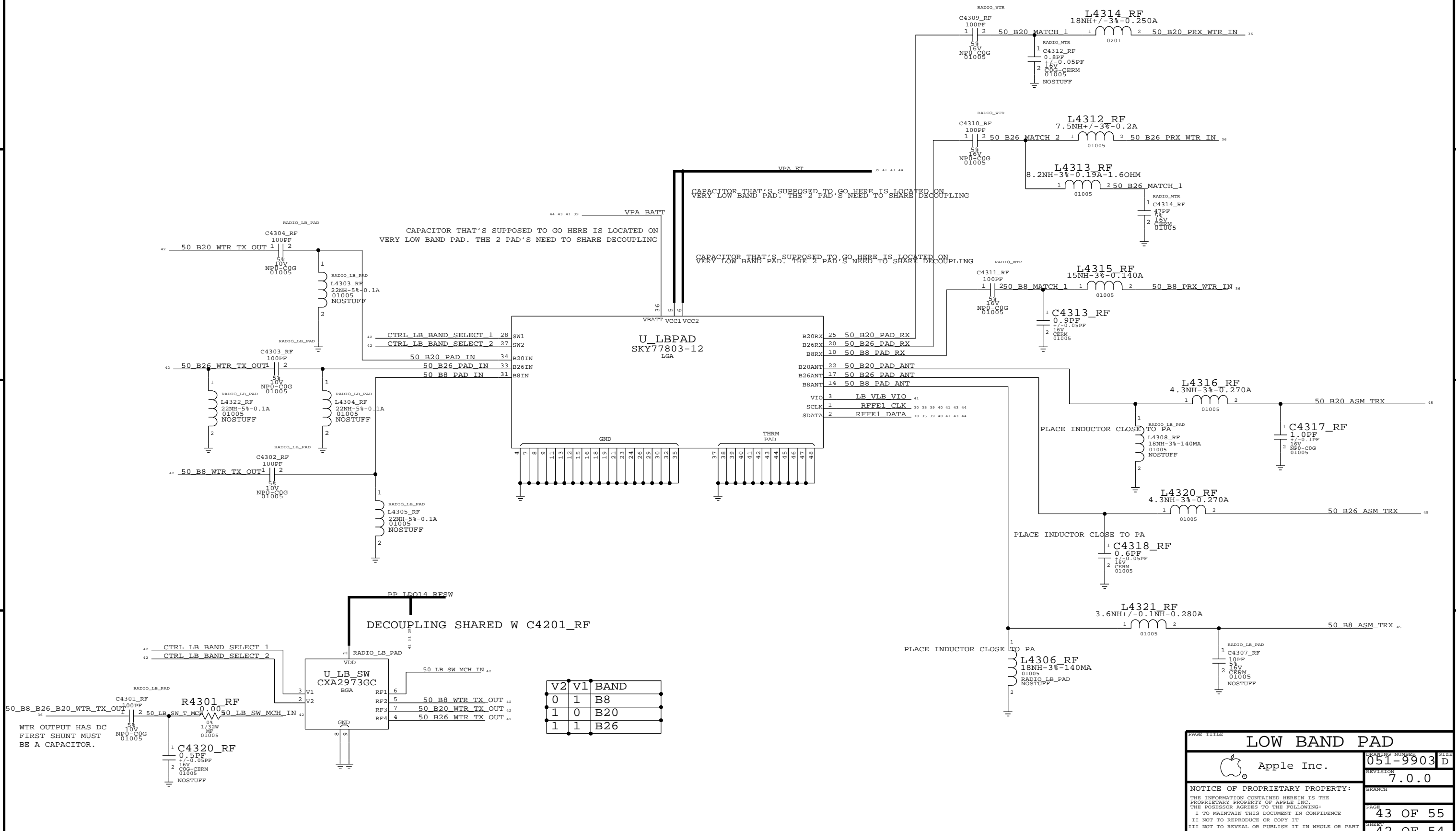
LOW BAND PAD (B8, B26, B20)

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C4318_RF
R1400
L4322_RF
U1402

D
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A

D
C
B
A



MID BAND PAD (B1, B25, B3, B4, B34, B39)

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C4426_RF
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L4409_RF
U1501

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C

B

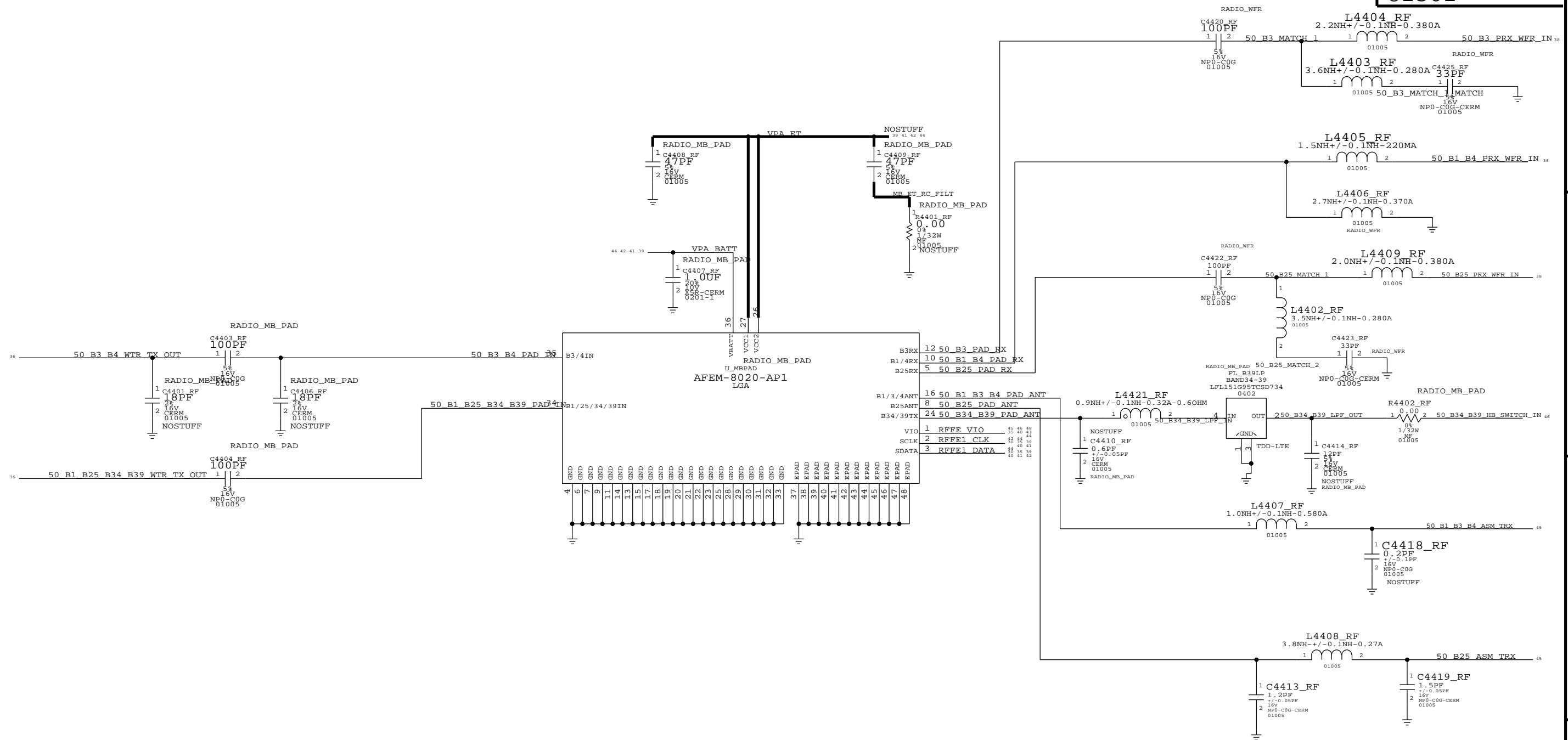
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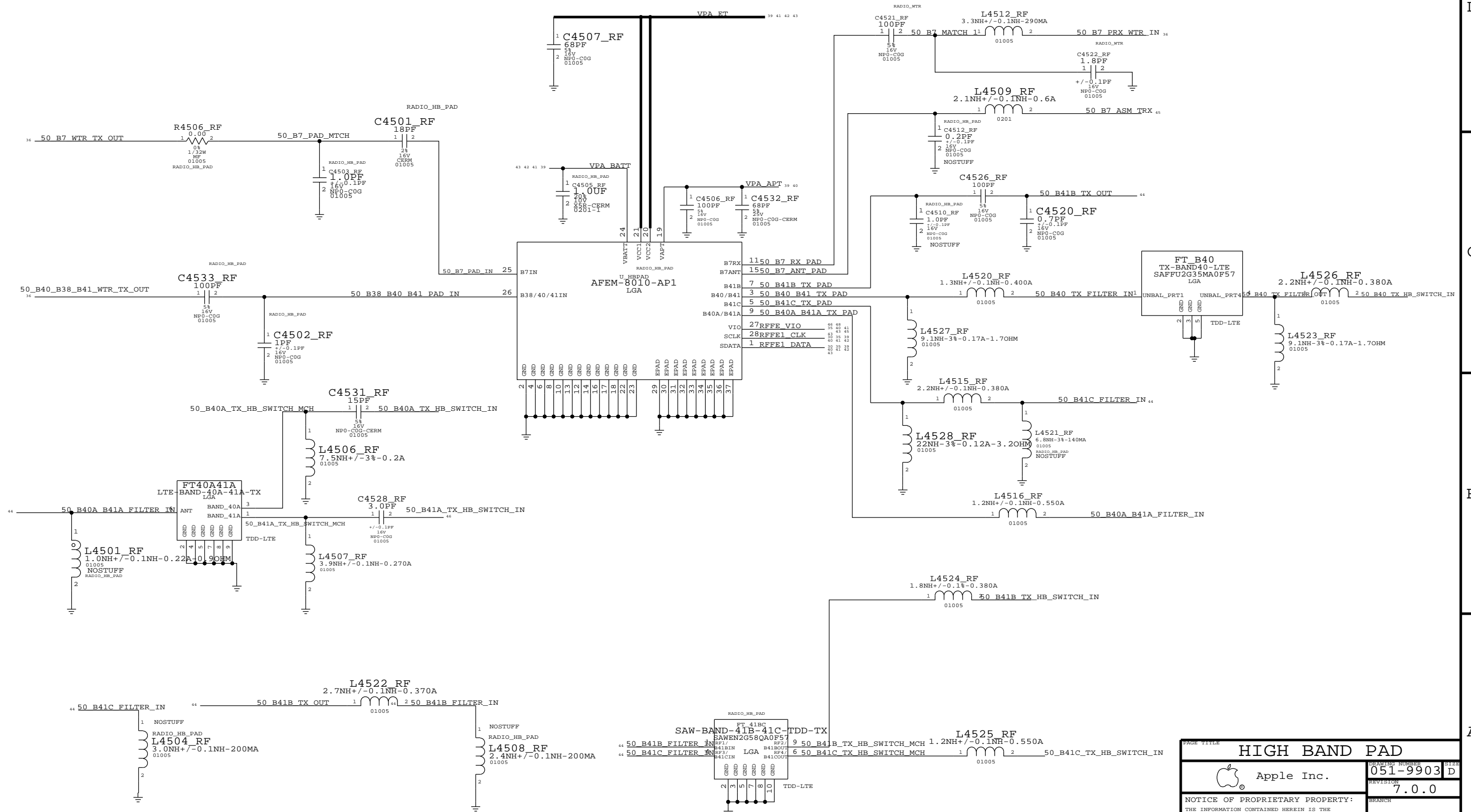


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HIGH BAND PAD (B7, B38, B40, B41, XGP)

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C4533_RF
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L1616
U1601

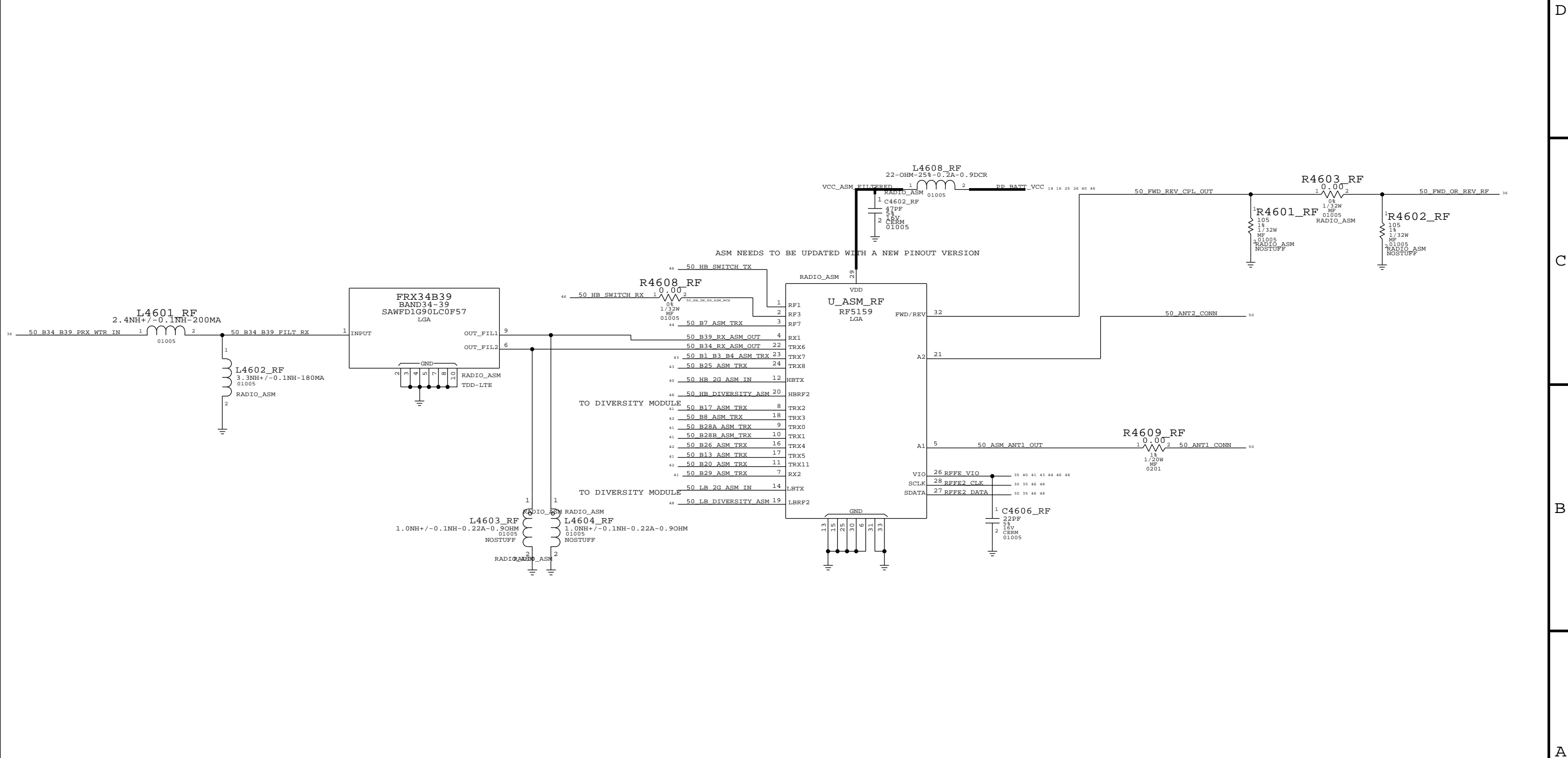


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ANTENNA SWITCH

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C1702
R1700
L4608_RF
U1702



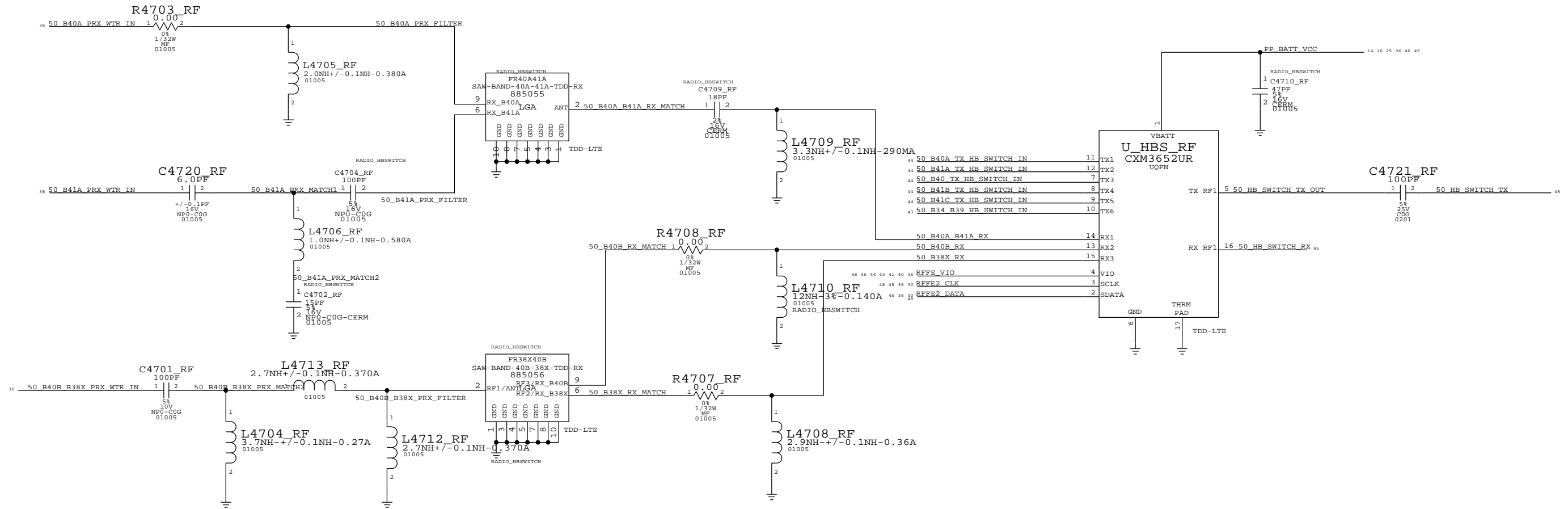
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HIGH BAND SWITCH

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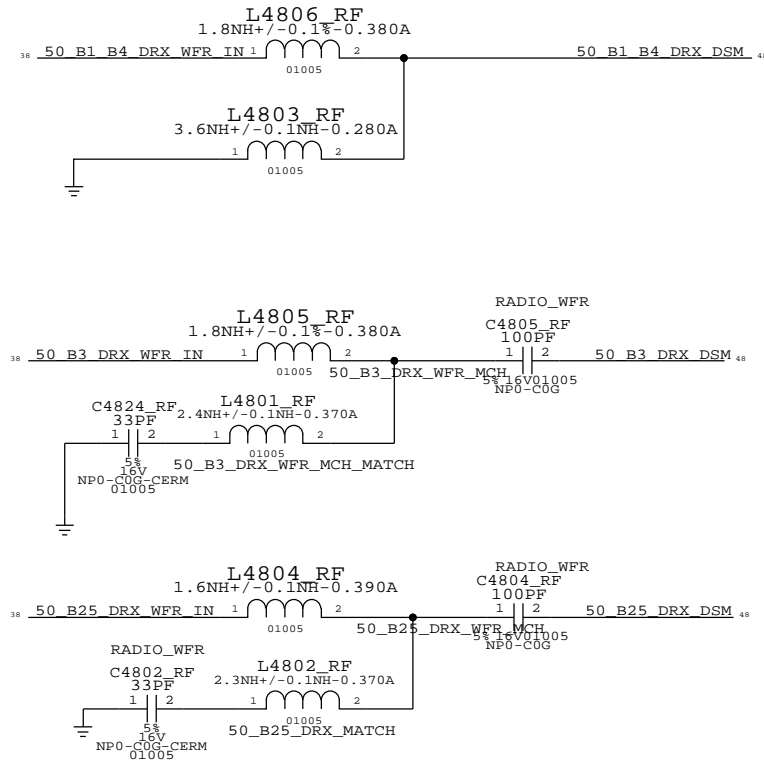
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		SHEET 46 OF 54

RX DIVERSITY (1)

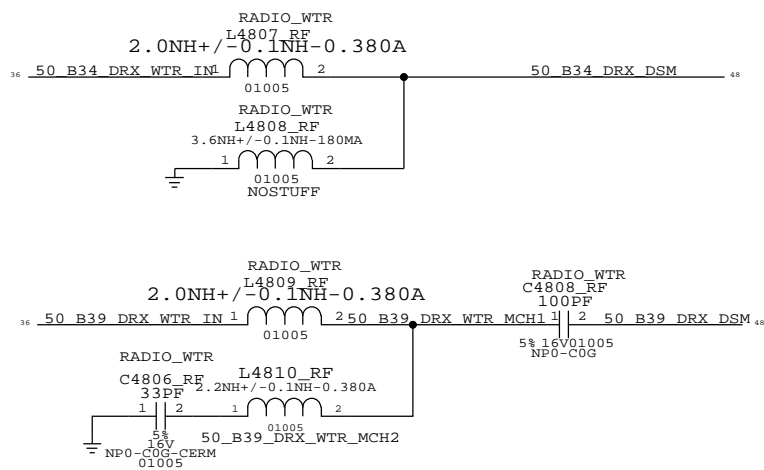
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U1801

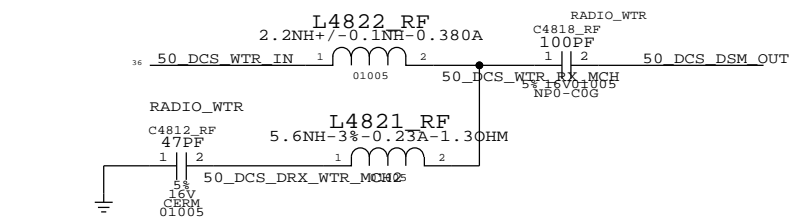
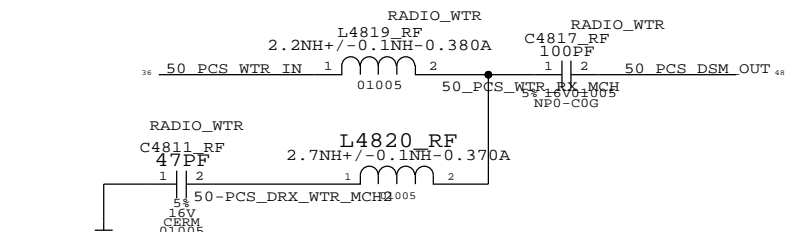
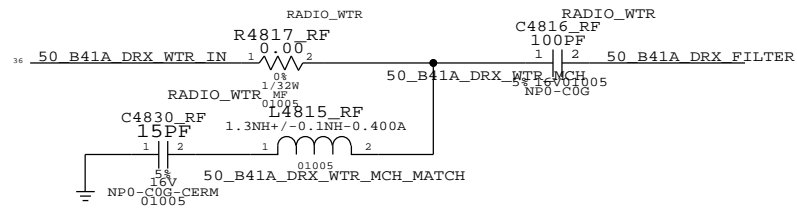
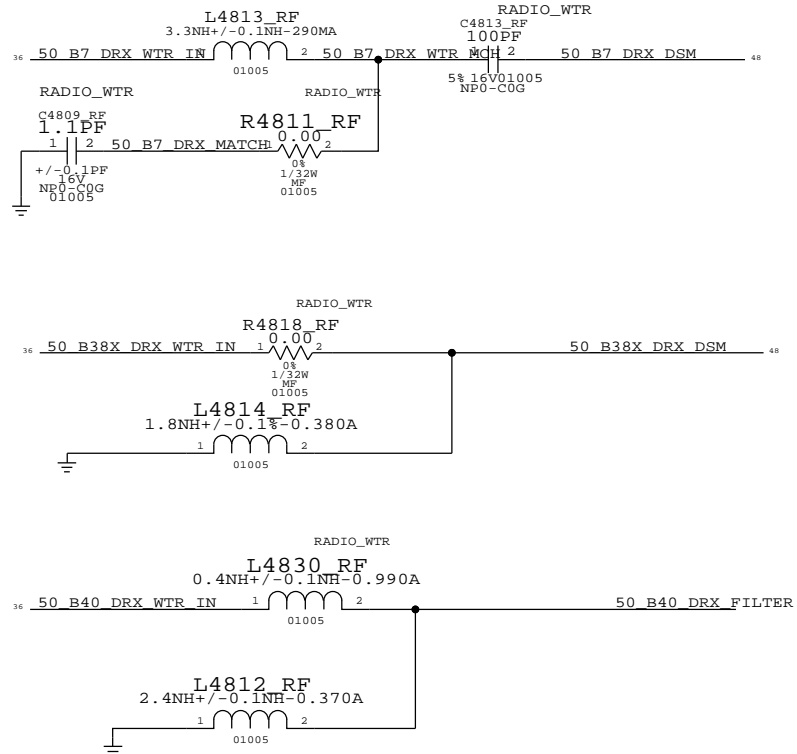
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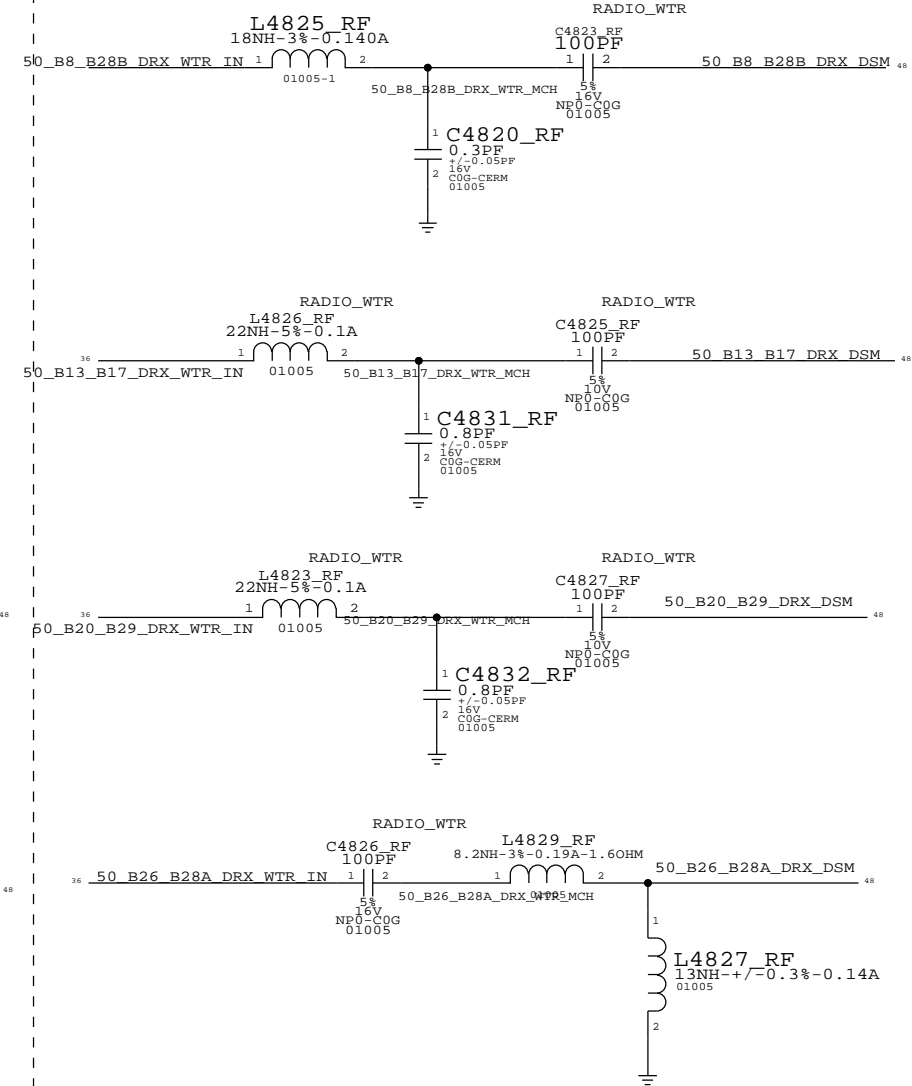
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HIGHBAND DIVERSITY - WTR



LOWBAND DIVERSITY - WTR

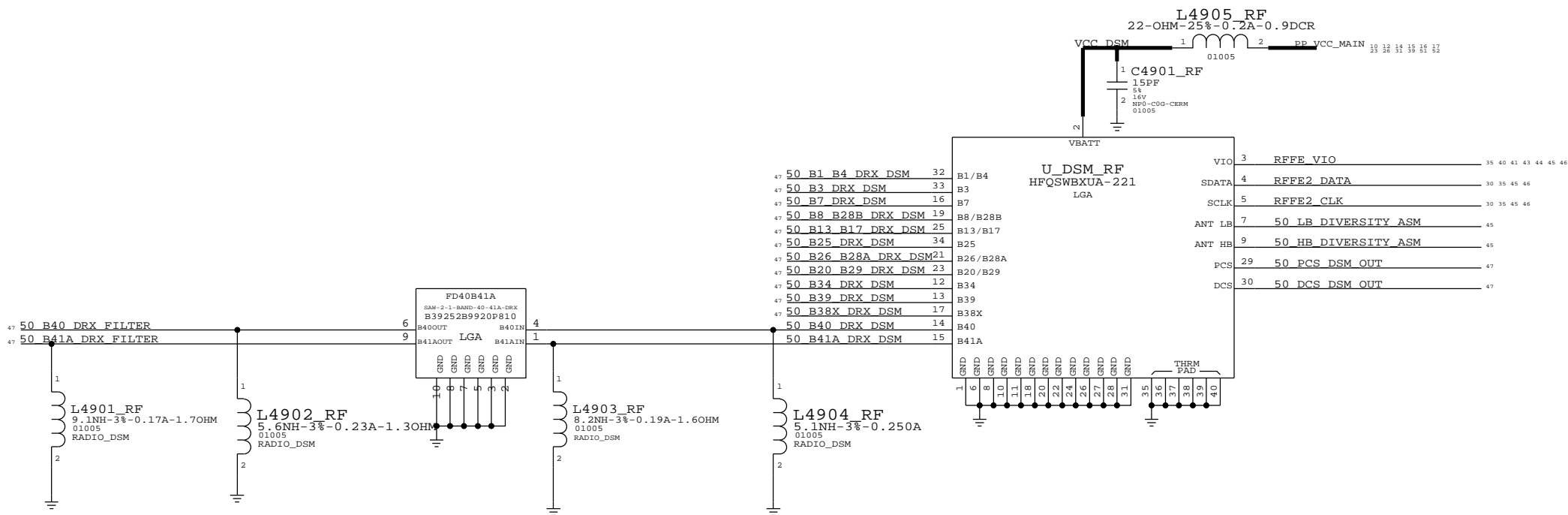


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RX DIVERSITY (2)

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C1900
R1900
L1900
U1901

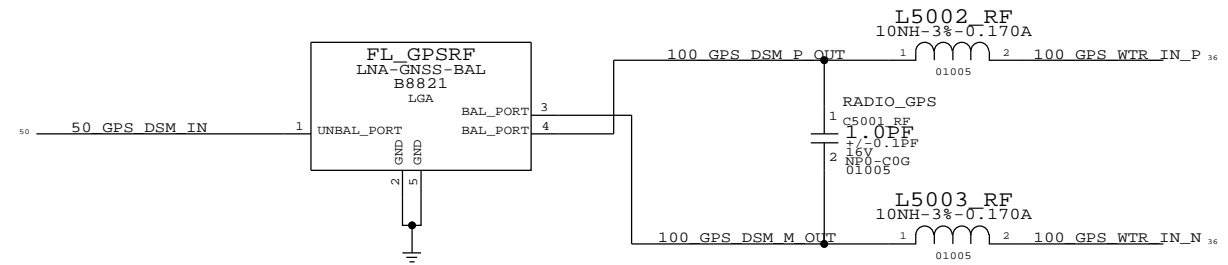



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GPS

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C1900
R1900
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U1901

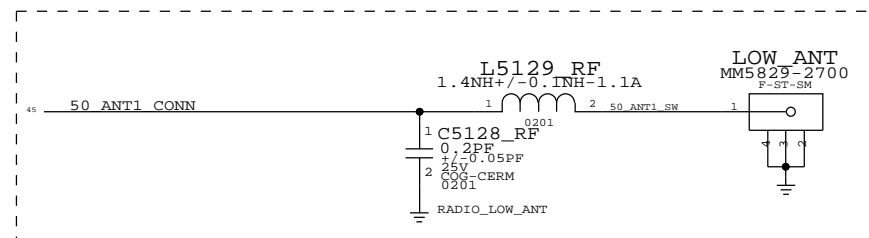
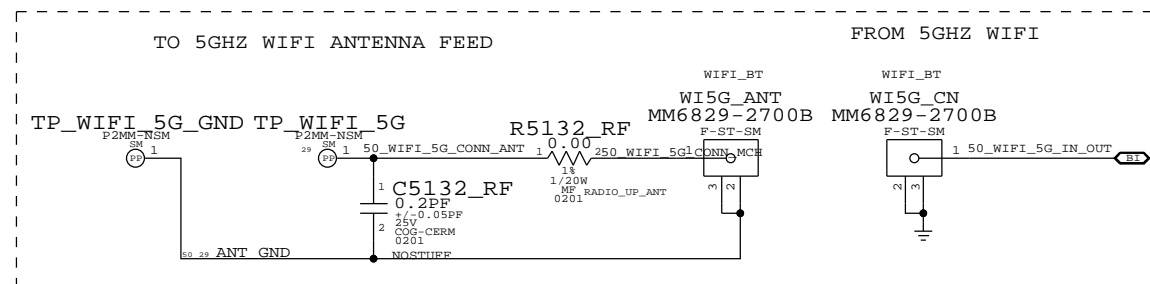
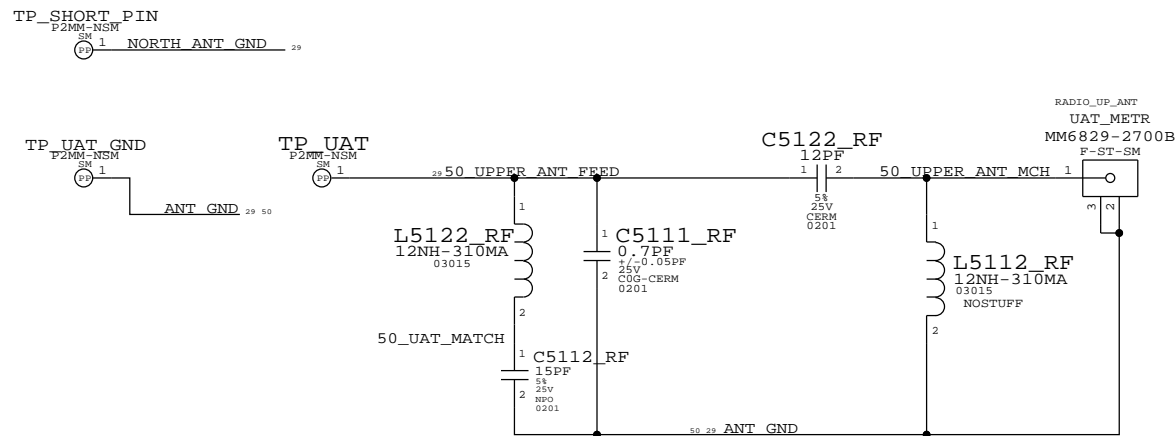
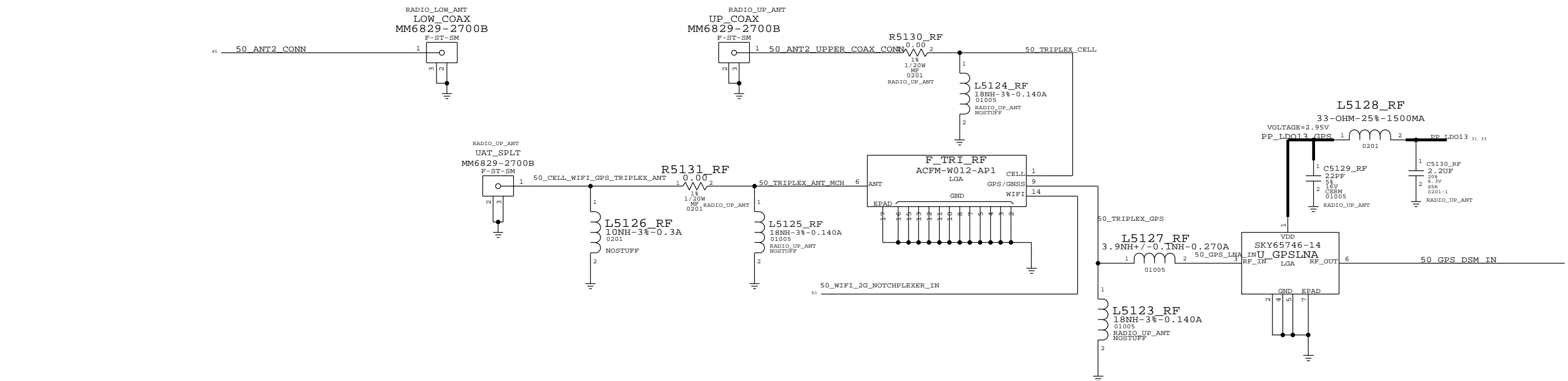


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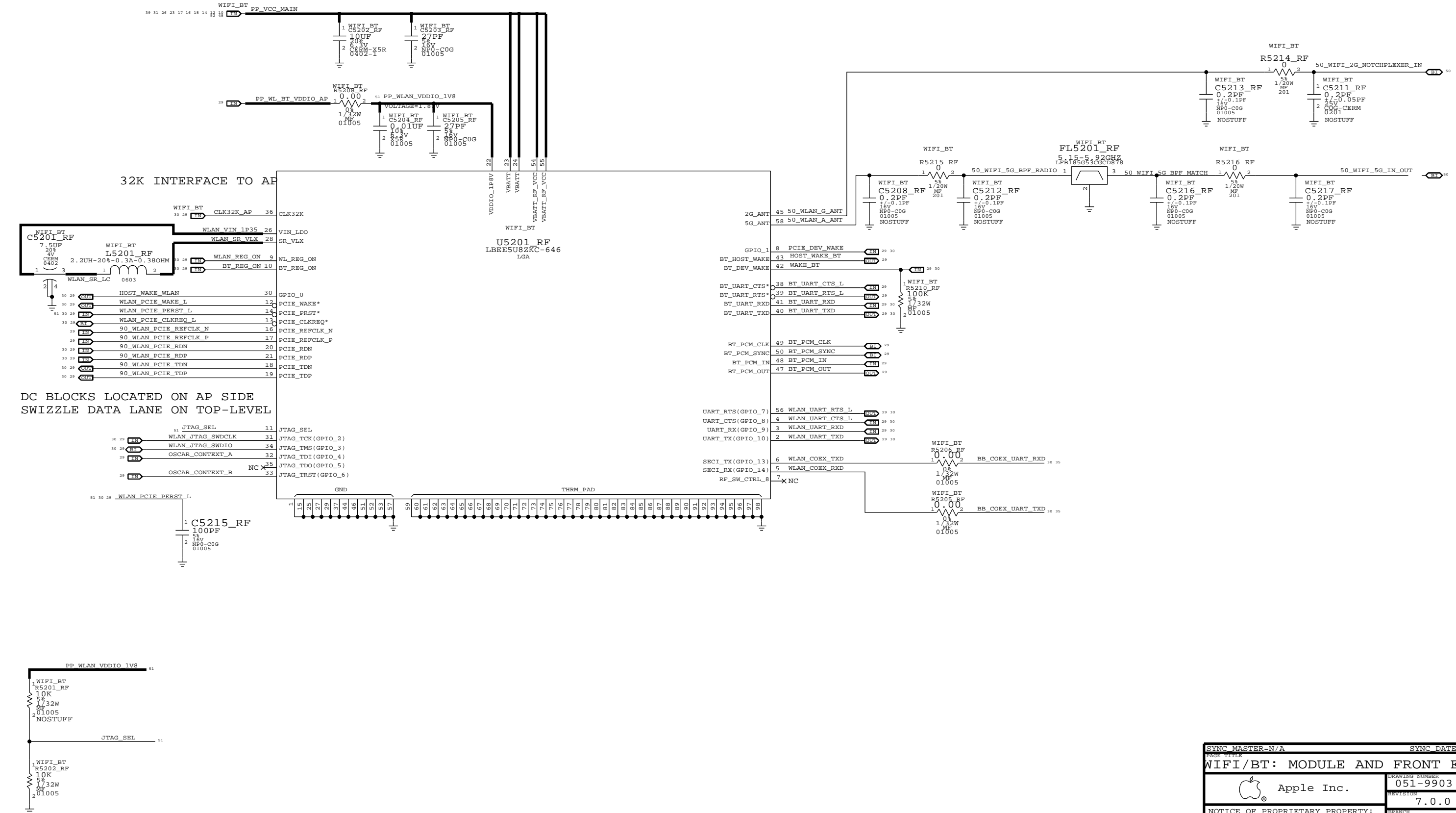
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WLAN / BT



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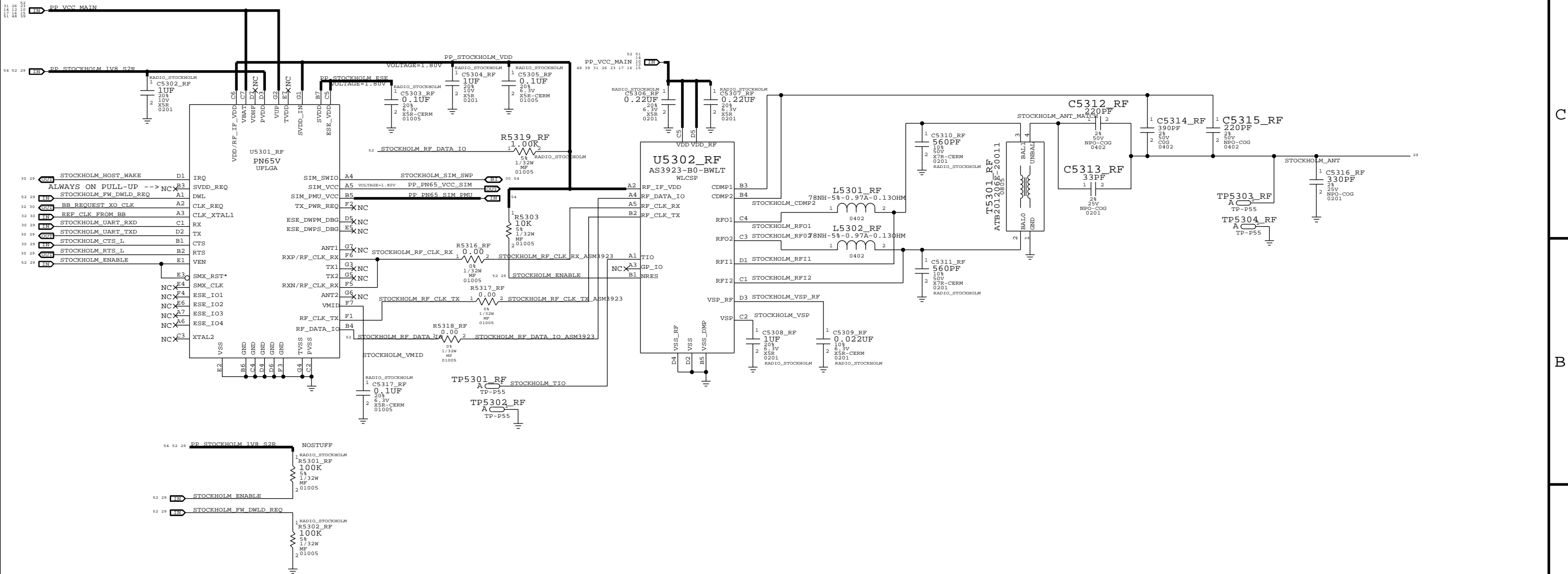
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C2101
R2100
L2102
U2100

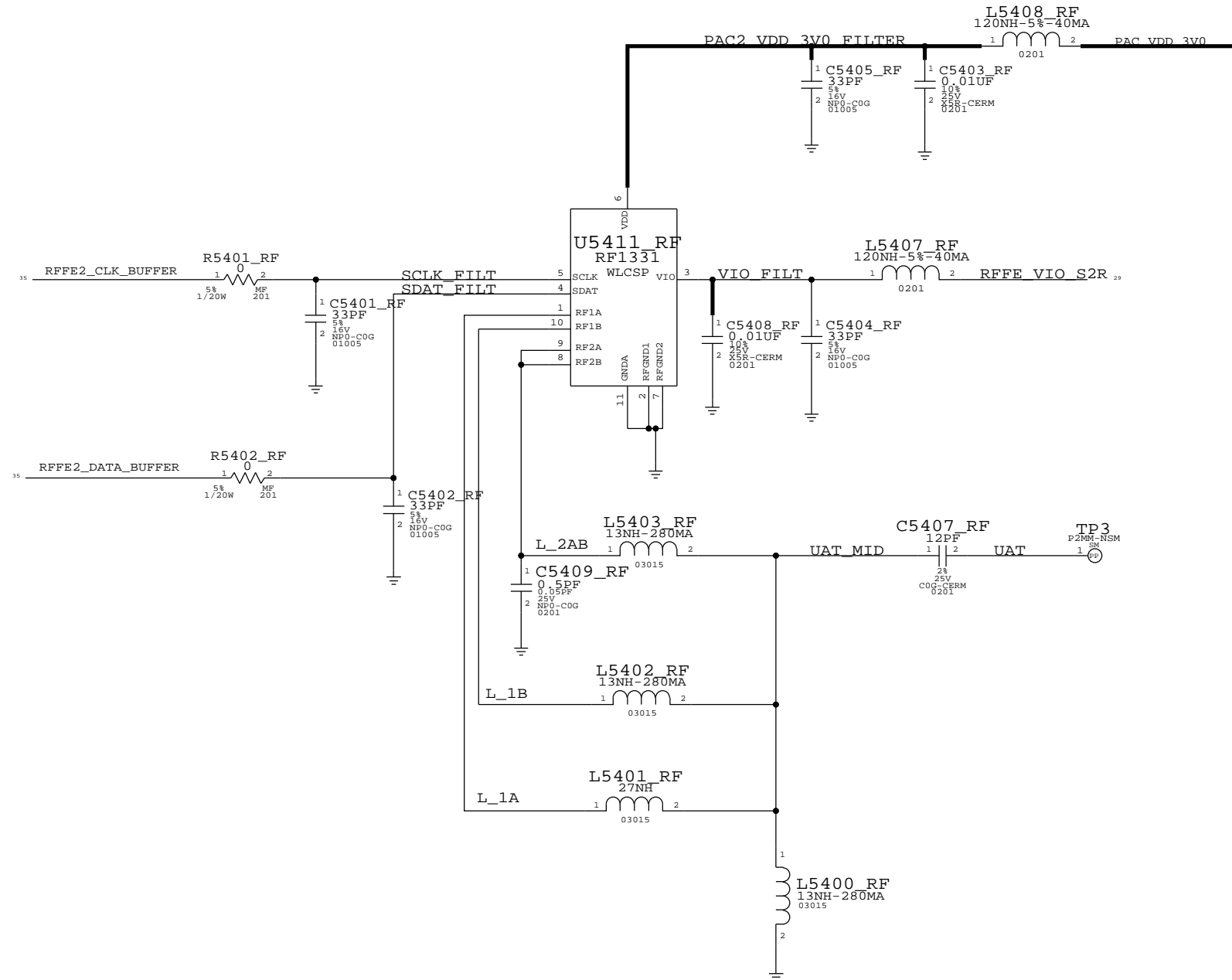
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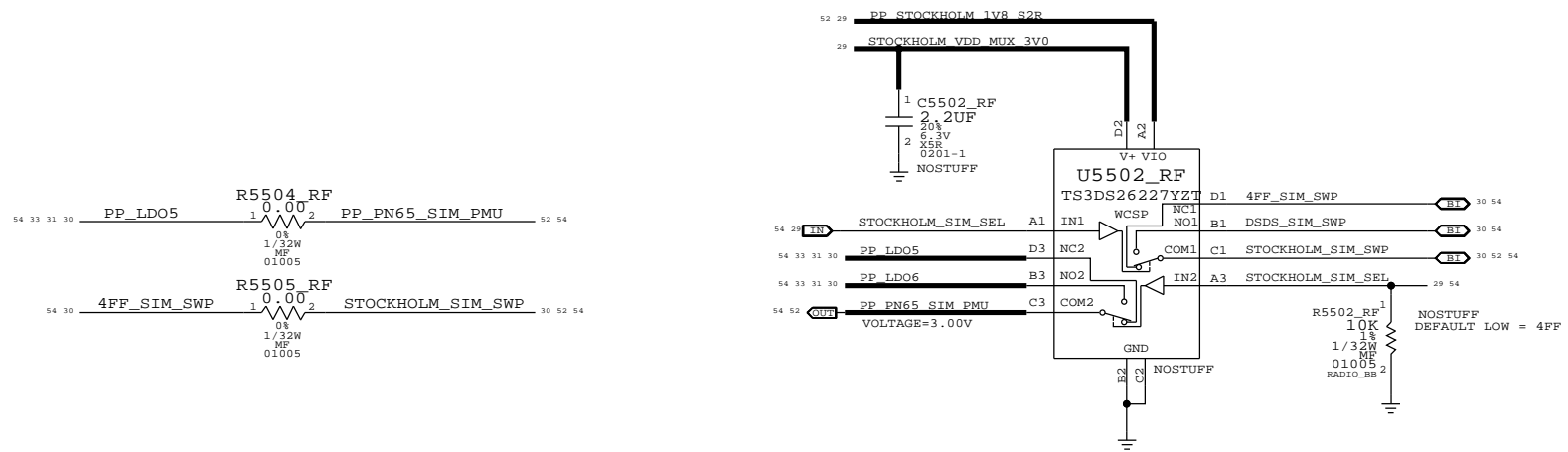
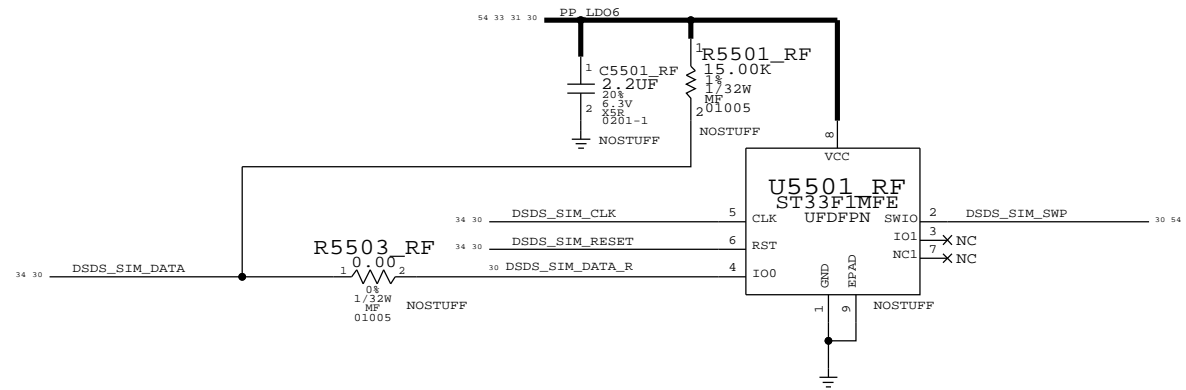
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